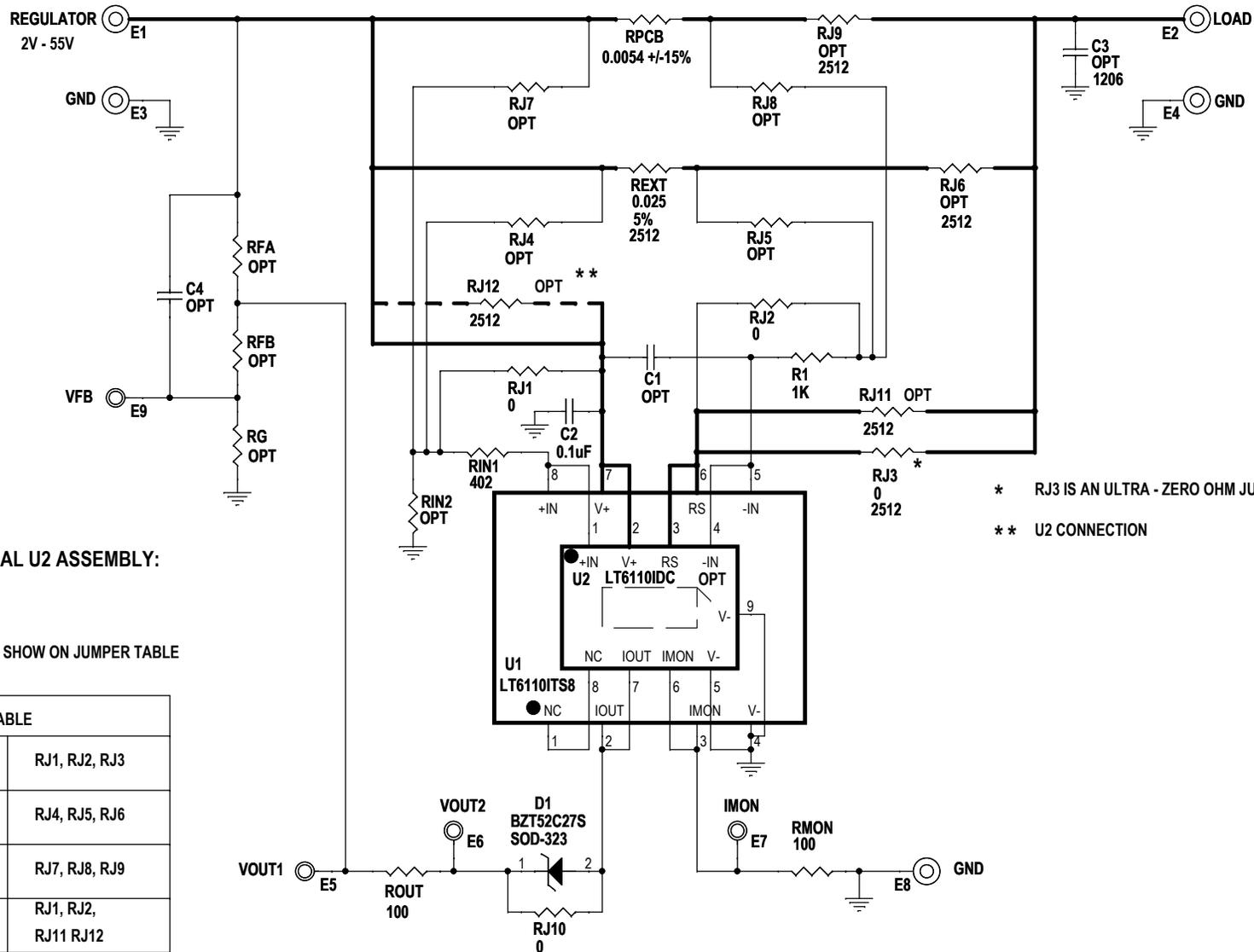


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	2	PRODUCTION	PHILIP K.	10-04-2012

A BOLD LINE INDICATES THE PATH OF THE LOAD CURRENT



* RJ3 IS AN ULTRA - ZERO OHM JUMPER
 ** U2 CONNECTION

NOTE FOR OPTIONAL U2 ASSEMBLY:

- A. REMOVE U1 AND RJ3
- B. INSTALL U2
- C. INSTALL JUMPERS AS SHOW ON JUMPER TABLE

JUMPER TABLE	
U1 WITH INTERNAL RSENSE	RJ1, RJ2, RJ3
U1 OR U2 WITH REXT	RJ4, RJ5, RJ6
U1 OR U2 WITH RPCB	RJ7, RJ8, RJ9
U2 WITH INTERNAL RSENSE	RJ1, RJ2, RJ11 RJ12

NOTE: UNLESS OTHERWISE SPECIFIED

- 1. ALL RESISTORS ARE 1%, 0805
- ALL CAPACITORS ARE 5%, 0603

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	A.K.
APP ENG.	PHILIP K.
SCALE = NONE	

1630 McCarthy Blvd.
 Milpitas, CA 95035
 Phone: (408)432-1900 www.linear.com
 Fax: (408)434-0507
 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC			
PRECISION WIRE DROP COMPENSATOR			
SIZE	IC NO.	LT6110	
N/A		DEMO CIRCUIT 2033A	
DATE:	Friday, January 25, 2013	SHEET	1 OF 1