

Fast CMOS 16-Bit Bidirectional Transceivers

Features

Common Features

- PI74FCT16245T and PI74FCT162245T are high-speed, low-power devices with high-current drive
- $V_{CC} = 5V \pm 10\%$
- · Hysteresis on all inputs

PI74FCT16245T Features

- High output drive: $I_{OH} = -32 \text{mA}$; $I_{OL} = 64 \text{mA}$
- Power off disable outputs permit "live insertion"
- Typical V_{OLP} (Output Ground Bounce)
 1.0V at V_{CC} = 5V, T_A = 25°C
- Packaging (Pb-free & Green available):
 - 48-pin 240-mil wide plastic TSSOP (A)
 - 48-pin 300-mil wide plastic SSOP (V)
 - 48-pin 300-mil wide plastic TVSOP (K)

PI74FCT162245T Features

- Balanced output drivers: ±24mA
- Reduced system switching noise
- Typical V_{OLP} (Output Ground Bounce) < 0.6V at V_{CC} = 5V, T_A = 25°C
- Packaging (Pb-free & Green available):
 - 48-pin 240-mil wide plastic TSSOP (A)
 - 48-pin 300-mil wide plastic SSOP (V)
 - 48-pin 300-mil wide plastic TVSOP (K)

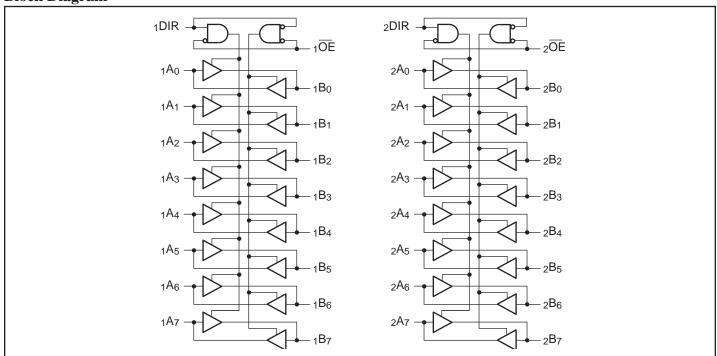
Description

Pericom Semiconductor's PI74FCT16245T and PI74FCT162245T are 16-bit bidirectional transceivers designed for asynchronous two-way communication between data buses. The direction control input pin (xDIR) determines the direction of data flow through the bidirectional transceiver. The Direction and Ouput Enable controls are designed to operate these devices as either two independent 8-bit transceivers or one 16-bit transceiver. The output enable (\overline{OE}) input, when HIGH, disables both A and B ports by placing them in Hi-Z condition.

The PI74FCT16245T output buffers are designed with a power off disable allowing "live insertion" of boards when used as backplane drivers.

The PI74FCT162245T has ± 24 mA balanced output drivers. It is designed with current limiting resistors at its outputs to control the output edge rate resulting in lower ground bounce and undershoot. This eliminates the need for external terminating resistors for most interface applications.

Block Diagram







Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C	ľ
Ambient Temperature with Power Applied $-40^{\circ}C$ to $+85^{\circ}C$	
Supply Voltage to Ground Potential (Inputs & $V_{CC}\ \mbox{Only})0.5V$ to $+7.0V$	
Supply Voltage to Ground Potential (Outputs & D/O Only)–0.5V to $+7.0V$	
DC Input Voltage0.5V to +7.0V	
DC Output Current	
Power Dissipation	

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Pin Configuration

Pin Comigurado	11	
		\neg
1DIR □	1	48 10E
1B ₀ □	2	47 🗖 1A0
1B1 □	3	46 🗖 1A1
GND □	4	45 GND
1B2 □	5	44 🗖 1A2
1B3 □	6	43 🗖 1A3
Vcc □	7	42 VCC
1B4 □	8	41
1B ₅ □	9	40 🗖 1A5
GND □	10	39 GND
1B6 □	11	38
1B ₇ □	12	37 🔲 1A7
2B ₀ □	13	36 🗖 ₂ A0
2B₁ □	14	35 2A ₁
GND □	15	34 GND
2B2 □	16	33 🗖 2A2
2B₃ □	17	32 2A ₃
Vcc □	18	31 VCC
2B4 □	19	30 🗖 ₂ A ₄
2B₅ □	20	29 🗖 ₂ A ₅
GND □	21	28 GND
2B ₆ □	22	27 2A ₆
2B7 □	23	26 2A ₇
2DIR □	24	25 2 OE

Truth Table

Inpu	ıts ⁽¹⁾	Outputs ⁽¹⁾		
_X ŌE	_X DIR	Outputs\"		
L	L	Bus B Data to Bus A		
L	Н	Bus A Data to Bus B		
Н	X	High Z State		

Note:

1. H = High Voltage Level, X = Don't Care, L = Low Voltage Level, Z = High Impedance

Pin Description

Pin Name	Description
$_{X}\overline{OE}$	3-State Enable Inputs (Active LOW)
XDIR	Direction Control Input
XAX	Side A Inputs or 3-State Outputs
$_{\rm X} { m B}_{ m X}$	Side B Inputs or 3-State Outputs
GND	Ground
V _{CC}	Power





DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 5.0\text{V} \pm 10\%$)

Param- eters	Description	Test Conditions ⁽¹⁾			Typ. (2)	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0			V
$V_{ m IL}$	Input LOW Voltage	Guaranteed Logic LOW Level				0.8	V
I I I I I I I I I I I I I I I I I I I		Standard Input, $V_{CC} = Max$.	$V_{IN} = V_{CC}$			1	
I_{IH}	Input HIGH Current	Standard I/O, V _{CC} = Max.	$V_{\rm IN} = V_{\rm CC}$			1	
T	Invest I OW Comment	Standard Input, V _{CC} = Min.	$V_{IN} = GND$			-1	4
${ m I}_{ m IL}$	Input LOW Current	Standard I/O, V _{CC} = Min.	$V_{IN} = GND$			-1	μA
I _{OZH}	High Impedance	$V_{CC} = Max.$	$V_{OUT} = 2.7V$			1	
I _{OZL}	Output Current	$V_{CC} = Max.$	$V_{OUT} = 0.5V$			-1	
V_{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$	$V_{CC} = Min., I_{IN} = -18mA$		-0.7	-1.2	V
I _{OS}	Short Circuit Current	$V_{CC} = Max.^{(3)}, V_{OUT} = GND$		-80	-140	-300	A
I _O	Output Drive Current	$V_{CC} = Max.^{(3)}, V_{OUT} = 2.5V$		-50		-180	mA
V_{H}	Input Hysteresis				100		mV

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 5.0V$, $+25^{\circ}C$ ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

PI74FCT16245T Output Drive Characteristics (Over the Operating Range)

Parameters	Description	Test Conditio	Min.	Typ. ⁽²⁾	Max.	Units	
			$I_{OH} = -3.0 \text{mA}$	2.5	3.5		
V_{OH}	Output HIGH Voltage	$V_{CC} = Min., V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -15.0 \text{mA}$	2.4	3.5		V
			$I_{OH} = -32.0 \text{mA}$				V
$V_{ m OL}$	Output LOW Voltage	$V_{CC} = Min., V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 64 \text{mA}$		0.2	0.55	
I _{OFF}	Power Down Disable	$V_{CC} = 0V$, V_{IN} or $V_{OUT} \le 4.5V$			±100	μΑ	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 5.0V$, $+25^{\circ}C$ ambient and maximum loading.

PI74FCT162245T Output Drive Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾			Typ. ⁽²⁾	Max.	Units
V _{OH}	Output HIGH Voltage	$V_{CC} = Min., V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -24.0 \text{mA}$	2.4	3.3		W
V _{OL}	Output LOW Voltage	$V_{CC} = Min., V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 24mA$		0.3	0.55	V
I_{ODL}	Output LOW Current	$V_{CC} = 5V$, $V_{IN} = V_{IH}$ or V_{IL} , $V_{OUT} = 1.5V^{(3)}$			115	160	m A
I _{ODH}	Output HIGH Current	$V_{CC} = 5V$, $V_{IN} = V_{IH}$ or V_{IL} , V_{O}	$t_{\rm UT} = 1.5 V^{(3)}$	-60	-115	-150	mA

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 5.0V$, $+25^{\circ}C$ ambient and maximum loading. 2.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

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Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

Parameters	Description	Test Conditions ⁽¹⁾	Typ. ⁽²⁾	Max.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0V$	4.5	6	ωŪ
C _{OUT}	Output Capacitance	$V_{OUT} = 0V$	5.5	8	pF

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 5.0V$, $+25^{\circ}C$ ambient.

Power Supply Characteristics

Parameters	Description	Test Condition	ons ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	V _{CC} = Max.	$V_{IN} = GND$ or V_{CC}		0.1	500	μΑ
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	$V_{IN} = 3.4V^{(3)}$		0.5	1.5	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., Outputs Open xOE = xDIR = GND One Bit Toggling 50% Duty Cycle	$V_{IN} = V_{CC},$ $V_{IN} = GND$		60	100	μΑ/ MHz
	Total Power Supply Current ⁽⁶⁾	V _{CC} = Max., Outputs Open	$\begin{aligned} V_{IN} &= V_{CC,} \\ V_{IN} &= GND \end{aligned}$		0.7	2.5 ⁽⁵⁾	
·		fr = 10 MHz 50% Duty Cycle xOE = xDIR = GND One Bit Toggling	$V_{\rm IN} = 3.4 V$		0.9	3.3 ⁽⁵⁾	
$I_{\mathbb{C}}$		V _{CC} = Max., Outputs Open	$\begin{aligned} V_{IN} &= V_{CC,} \\ V_{IN} &= GND \end{aligned}$		2.5	5.5 ⁽⁵⁾	mA
		fi = 2.5 MHz 50% Duty Cycle xOE = xDIR = GND 16 Bits Toggling	$V_{\rm IN} = 3.4 V$		6.5	17.5 ⁽⁵⁾	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 5.0V$, +25°C ambient.
- 3. Per TTL driven input ($V_{IN} = 3.4V$); all other inputs at V_{CC} or GND.
- 4. Guaranteed by design, not production tested.
- 5. Values for these conditions are examples of the I_{CC} formula. These limits are guaranteed but not tested.
- 6. $I_{C} = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}$
 - $I_C = I_{CC} + \Delta I_{CC} \ D{\scriptscriptstyle H}N{\scriptscriptstyle T} + I_{CCD} \ (f{\scriptscriptstyle CP/2} + f{\scriptscriptstyle I}N{\scriptscriptstyle I})$
 - I_{CC} = Quiescent Current
 - ΔI_{CC} = Power Supply Current for a TTL High Input (V $_{IN}$ = 3.4V)
 - D_H = Duty Cycle for TTL Inputs High
 - N_T = Number of TTL Inputs at D_H
 - I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)
 - fcp = Clock Frequency for Register Devices (Zero for Non-Register Devices)
 - $f_I = Input Frequency$
 - N_I = Number of Inputs at f_I
 - All currents are in milliamps and all frequencies are in megahertz.



PI74FCT16245T Switching Characteristics over Operating Range

			162	45T	1624	5AT	1624	5CT	
Parameters	Description	Conditions ⁽¹⁾	Co	Com.		Com.		Com.	
			Min.	Max.	Min.	Max.	Min.	Max.	
t _{PHL} t _{PHL}	Propagation Delay ⁽²⁾ A TO B, B TO A		1.5	7.0	1.5	4.6	1.5	4.1	ns
t _{PZH} t _{PZL}	Output Enable Time xOE to A or B		1.5	9.5	1.5	6.2	1.5	5.8	ns
t _{PHZ} t _{PLZ}	Output Disable Time ⁽³⁾ xOE to A or B	$C_L = 50 \text{pF}$ $R_L = 500 \Omega$	1.5	7.5	1.5	5.0	1.5	4.8	ns
t _{PZH} t _{PZL}	Output Enable Time xDIR to A or B ⁽³⁾	KL = 30022	1.5	9.5	1.5	6.2	1.5	5.8	ns
t _{PHZ} t _{PLZ}	Output Disable Time xDIR to A or B ⁽³⁾		1.5	7.5	1.5	5.0	1.5	4.8	ns
T _{SK(O)}	Output Skew ⁽⁴⁾			0.5		0.5		0.5	ns

Notes:

- 1. For Max or Min conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Minimum limits are guaranteed but not tested on Propagation Delays.
- 3. This parameter is guaranteed but not production tested.
- 4. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.

PI74FCT162245T Switching Characteristics over Operating Range

			1622	245T	1622	45AT	1622	45CT	
Parameters	Description	Conditions ⁽¹⁾	Co	m.	Com.		Com.		Units
			Min.	Max.	Min.	Max.	Min.	Max.	
t _{PHL} t _{PHL}	Propagation Delay ⁽²⁾ A to B, B to A		1.5	7.0	1.5	4.6	1.5	4.1	ns
t _{PZH}	Output Enable Time xOE to A or B		1.5	9.5	1.5	6.2	1.5	5.8	ns
t _{PHZ} t _{PLZ}	Output Disable Time ⁽³⁾ xOE to A or B	$C_L = 50 pF$ $R_L = 500 \Omega$	1.5	7.5	1.5	5.0	1.5	4.8	ns
t _{PZH} t _{PZL}	Output Enable Time xDIR to A or B ⁽³⁾	KL = 50022	1.5	9.5	1.5	6.2	1.5	5.8	ns
t _{PHZ} t _{PLZ}	Output Disable Time xDIR to A or B ⁽³⁾		1.5	7.5	1.5	5.0	1.5	4.8	ns
tsk(o)	Output Skew ⁽⁴⁾			0.5		0.5		0.5	ns

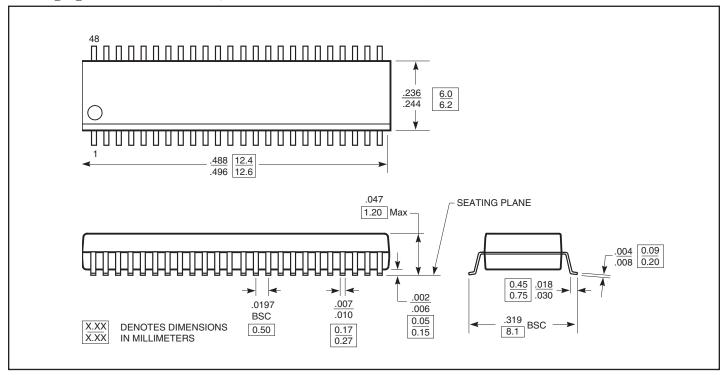
Notes:

- 1. For Max or Min conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Minimum limits are guaranteed but not tested on Propagation Delays.
- 3. This parameter is guaranteed but not production tested.
- 4. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.

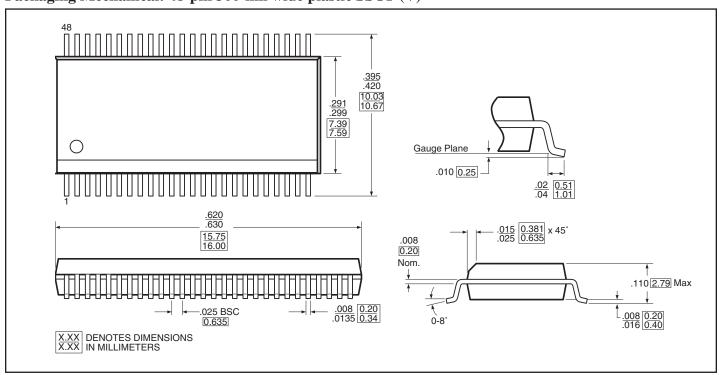
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Packaging Mechanical: 48-Pin, 240-mil wide Plastic TSSOP (A)



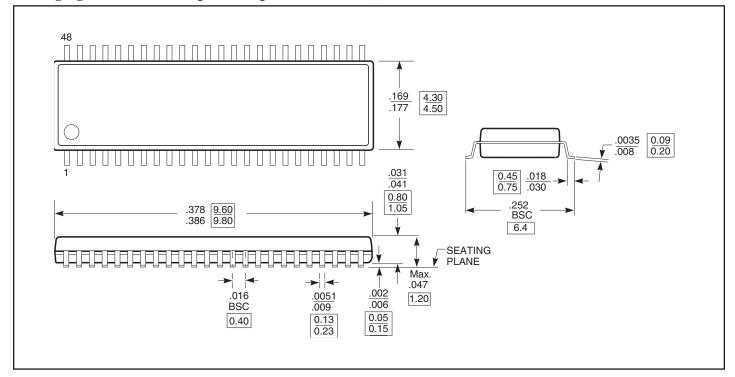
Packaging Mechanical: 48-pin 300-mil wide plastic SSOP (V)



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Packaging Mechanical: 48-pin wide plastic TVSOP (K)



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PI74FCT16245T Ordering Information

Ordering Code	Package Code	Speed Grade	Package Description
PI74FCT16245TAE	A	Blank	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74FCT16245TVE	V	Blank	Pb-free & Green, 48-pin 300-mil wide plastic SSOP
PI74FCT16245ATAE	A	A	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74FCT16245ATVE	V	A	Pb-free & Green, 48-pin 300-mil wide plastic SSOP
PI74FCT16245CTAE	A	С	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74FCT16245CTVE	V	С	Pb-free & Green, 48-pin 300-mil wide plastic SSOP

PI74FCT162245T Ordering Information

Ordering Code	Package Code	Speed Grade	Package Description
PI74FCT162245TAE	A	Blank	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74FCT162245TVE	V	Blank	Pb-free & Green, 48-pin 300-mil wide plastic SSOP
PI74FCT162245ATAE	A	A	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74FCT162245ATVE	V	A	Pb-free & Green, 48-pin 300-mil wide plastic SSOP
PI74FCT162245CTAE	A	С	Pb-free & Green, 48-pin 240-mil wide plastic TSSOP
PI74FCT162245CTVE	V	С	Pb-free & Green, 48-pin 300-mil wide plastic SSOP
PI74FCT162245CATK	K	A	48-pin 173-mil wide plastic TVSOP
PI74FCT162245CTK	K	С	48-pin 173-mil wide plastic TVSOP
PI74FCT162245TKE	K	Blank	Pb-free & Green, 48-pin 173-mil wide plastic TVSOP
PI74FCT162245ATKE	K	A	Pb-free & Green, 48-pin 173-mil wide plastic TVSOP
PI74FCT162245CTKE	K	С	Pb-free & Green, 48-pin 173-mil wide plastic TVSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X suffix = Tape/Reel

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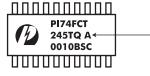
Part Marking Information

Pericom's standard product mark follows our standard part number ordering information, except for those products with a speed letter code. For marking purposes, the speed letter code mark is placed after the package code letter, rather than after the device number as it is ordered.

Although all products are marked immediately after assembly to assure material traceability, Pericom does not usually mark the speed code at that time. After electrical test screening and speed binning have been completed, we then perform an "add mark" operation which places the speed code letter at the end of the complete part number.

Please refer to the example shown below:

- Part Number as ordered: PI74FCT245ATQ
- Example of Part Number as marked:



"A" is the speed code letter identifier -

Notes:

1) 8-pin DIP, 8-pin SOIC, 8-pin TSSOP, 14-pin SOIC, 16-pin QSOP, SC70, MSOP, and SOT23 packages are not marked with the Pericom logo due to space limitations on the package.

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