CMPA801B030D

30 W, 8.0 - 11.0 GHz, GaN MMIC, Power Amplifier

Description

Cree's CMPA801B030D is a gallium nitride (GaN) High Electron Mobility Transistor (HEMT) based monolithic microwave integrated circuit (MMIC). GaN has superior properties compared to silicon or gallium arsenide, including higher breakdown voltage, higher saturated electron drift velocity and higher thermal conductivity. GaN HEMTs also offer greater power density and wider bandwidths compared to Si and GaAs transistors. This MMIC contains a two-stage reactively matched amplifier design approach enabling very wide bandwidths to be achieved.

Typical Performance Over 8.0-11.0 GHz (T_C = 85˚C)

Parameter	8.0 GHz	8.5 GHz	9.0 GHz	10.0 GHz	11.0 GHz	Units
Small Signal Gain	31	30	27	25	25	dB
P_{OUT} @ P_{IN} = 27 dBm, 100 µs @ 10%	37	39	37	28	29	W
Power Gain @ P_{IN} = 27 dBm, 100 µs @ 10%	19	19	19	18	18	dB
PAE @ P_{IN} = 27 dBm, 100 µs @ 10%	41	42	43	34	36	%

Features

- 28 dB Small Signal Gain
- 40 W Typical P_{SAT}
- Operation up to 28 V
- High Breakdown Voltage
- High Temperature Operation
- Size 0.142 x 0.188 x 0.004 inches

Applications

- Point to Point Radio
- Communications
- Test Instrumentation
- EMC Amplifiers
- Radar

RoHS COMPLIANT

Absolute Maximum Ratings (not simultaneous) at 25˚C

Parameter	Symbol	Rating	Units	Conditions
Drain source Voltage	V_{DSS}	84	V_{DC}	25˚C
Gate source Voltage	V_{GS}	-10, +2	V_{DC}	25˚C
Storage Temperature	T_{STG}	-55, +150	˚C	
Operating Junction Temperature	T_J	225	˚C	
Thermal Resistance, Junction to Case (packaged)[1]	$R_{\theta JC}$	1.22	˚C/W	Pulse Width = 100 µs, Duty Cycle = 10%, P_{DISS} = 77 W
Thermal Resistance, Junction to Case (packaged)[1]	$R_{\theta JC}$	1.80	˚C/W	CW, 85˚C, P_{DISS} = 77 W
Mounting Temperature (30 seconds)	T_S	320	˚C	P_{DISS} = 77 W

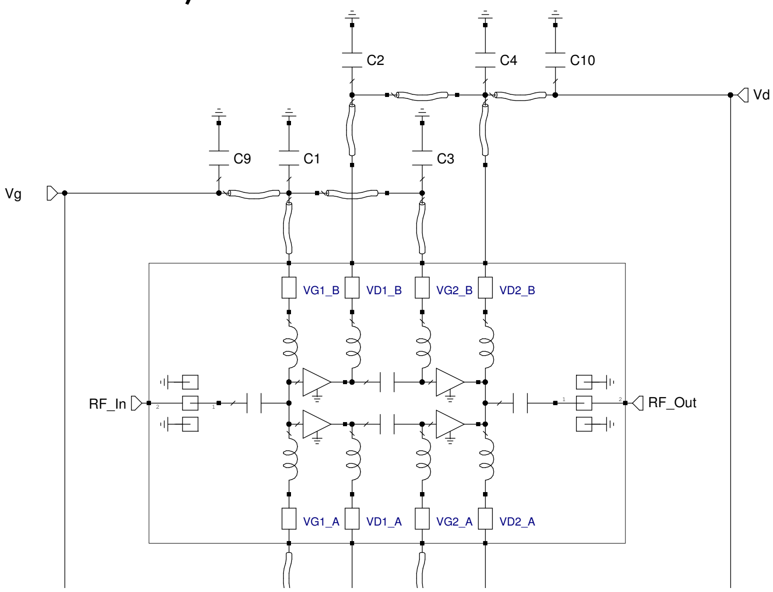
Note:
[1] Eutectic die attach using 80/20 AuSn mounted to a 40 mil thick CPC carrier

Electrical Characteristics (Frequency = 8 GHz to 11.0 GHz unless otherwise stated; T_C = 25˚C)

Characteristics	Symbol	Min.	Typ.	Max.	Units	Conditions
DC Characteristics						
Gate Threshold Voltage	$V_{GS(TH)}$	-3.8	-3.0	-2.3	V	V_{DS} = 10 V, I_D = 13.2 mA
Gate Quiescent Voltage	$V_{GS(Q)}$	–	-2.7	–	V	V_{DS} = 28 V, I_{DQ} = 1200 mA
Saturated Drain Current[1]	I_{DS}	9.5	12.9	–	A	V_{DS} = 6.0 V, V_{GS} = 2.0 V
Drain-Source Breakdown Voltage	V_{BD}	84	100	–	V	V_{GS} = -8 V, I_D = 13.2 mA
RF Characteristics[2]						
Small Signal Gain	S21	–	27.0	–	dB	V_{DD} = 28 V, I_{DQ} = 800 mA, Frequency 8.0 - 11.0 GHz
Input Return Loss	S11	–	-6.0	–	dB	V_{DD} = 28 V, I_{DQ} = 800 mA, Frequency 8.0 - 11.0 GHz
Output Return Loss	S21	–	-7.0	–	dB	V_{DD} = 28 V, I_{DQ} = 800 mA, Frequency 8.0 - 11.0 GHz
Power Output	P_{OUT}	–	41.9	–	W	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 8.0 GHz
Power Output	P_{OUT}	–	51.5	–	W	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 8.5 GHz
Power Output	P_{OUT}	–	45.4	–	W	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 9.0 GHz
Power Output	P_{OUT}	–	40.8	–	W	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 10.0 GHz
Power Output	P_{OUT}	–	38.8	–	W	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 11.0 GHz
Power Added Efficiency	PAE	–	42.7	–	%	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 8.0 GHz
Power Added Efficiency	PAE	–	47.9	–	%	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 8.5 GHz
Power Added Efficiency	PAE	–	50.2	–	%	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 9.0 GHz
Power Added Efficiency	PAE	–	40.7	–	%	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 10.0 GHz
Power Added Efficiency	PAE	–	41.0	–	%	V_{DD} = 28 V, I_{DQ} = 800 mA, P_{IN} = 27 dBm, Freq = 11.0 GHz
Output Mismatch Stress	VSWR	–	5 : 1	–	Ψ	No damage at all phase angles, V_{DD} = 28 V, I_{DQ} = 800 mA, P_{OUT} = 30 W

Notes:
[1] Scaled from PCM data
[2] All data pulse tested on-wafer with Pulse Width = 10 µs, Duty Cycle = 0.1%

DIE Dimensions (units in microns)



Overall die size 4780 x 3610 (+0/-50) microns, die thickness 100 (+/-10) micron.
All Gate and Drain pads must be wire bonded for electrical connection.

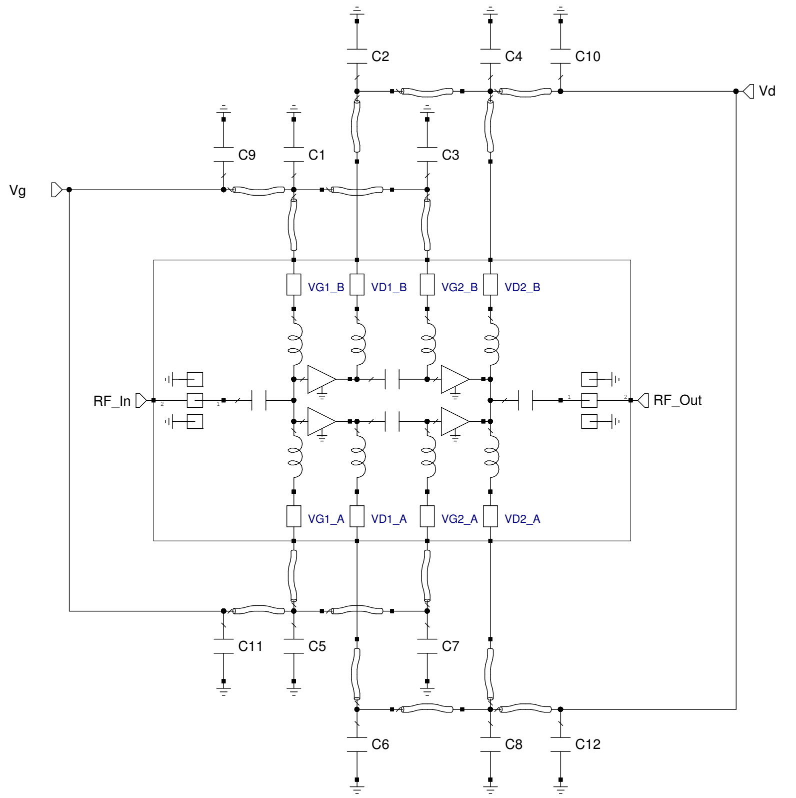
Pad	Function	Description	Pad Size (microns)	Note
1	RF-IN	RF-Input pad. Matched to 50 ohm.	190 x 165	4
2	VG1_A	Gate control for stage 1. V_G ~ 2.0 - 3.5 V.	110 x 110	1,2
3	VG1_B	Gate control for stage 1. V_G ~ 2.0 - 3.5 V.	110 x 110	1,2
4	VD1_A	Drain supply for stage 1. V_D = 28 V.	110 x 110	1
5	VD1_B	Drain supply for stage 1. V_D = 28 V.	110 x 110	1
6	VG2_A	Gate control for stage 2A. V_G ~ 2.0 - 3.5 V.	110 x 110	1,3
7	VG2_B	Gate control for stage 2A. V_G ~ 2.0 - 3.5 V.	110 x 110	1,3
8	VD2_A	Drain supply for stage 2A. V_D = 28 V.	274 x 140	1
9	VD2_B	Drain supply for stage 2B. V_D = 28 V.	274 x 140	1
10	RF-Out	RF-Output pad. Matched to 50 ohm.	150 x 150	4

Note 1: Attach bypass capacitor to pads 2-9 per application circuit
Note 2: VG1_A and VG1_B are connected internally so it would be enough to connect either one for proper operation
Note 3: VG2_A and VG2_B are connected internally so it would be enough to connect either one for proper operation
Note 4: The RF Input and Output pad have a ground-signal-ground with a nominal pitch of 1 mil (25 um). The RF ground pads are 110 x 110 microns

Die Assembly Notes:

- Recommended solder is AuSn (80/20) solder. Refer to Cree's website for the Eutectic Die Bond Procedure application note at http://www.cree.com/products/wireless_documents.asp
- Vacuum collet is the preferred method of pick-up
- The backside of the die is the Source (ground) contact
- Die back side gold plating is 5 microns thick minimum
- Thermosonic ball or wedge bonding are the preferred connection methods
- Gold wire must be used for connections
- Use the die label (XX-YY) for correct orientation

Block Diagram Showing Additional Capacitors for Operation Over 8.0 to 11.0 GHz



Designator	Description	Qty
C1,C2,C3,C4,C5,C6,C7,C8	CAP, 51pF, +/-10%, SINGLE LAYER, 0.035", Er 3300, 100V, Ni/Au TERMINATION	8
C9,C10,C11,C12	CAP, 680pF, +/-10%, SINGLE LAYER, 0.070", Er 3300, 100V, Ni/Au TERMINATION	4

Notes:
[1] The input, output and decoupling capacitors should be attached as close as possible to the die- typical distance is 5 to 10 mils with a maximum of 15 mils.
[2] The MMIC die and capacitors should be connected with 2 mil gold bond wires.

Typical Performance of the CMPA801B030D

Figure 1. Small Signal Gain vs Frequency
V_{DD} = 28 V, I_{DQ} = 0.8 A

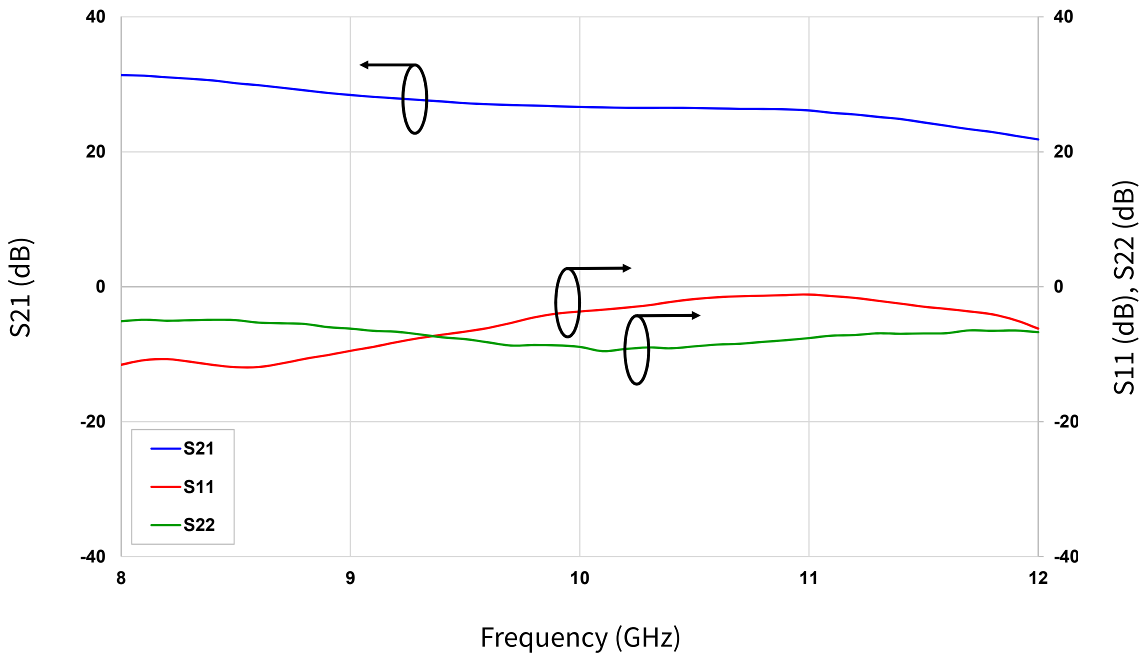
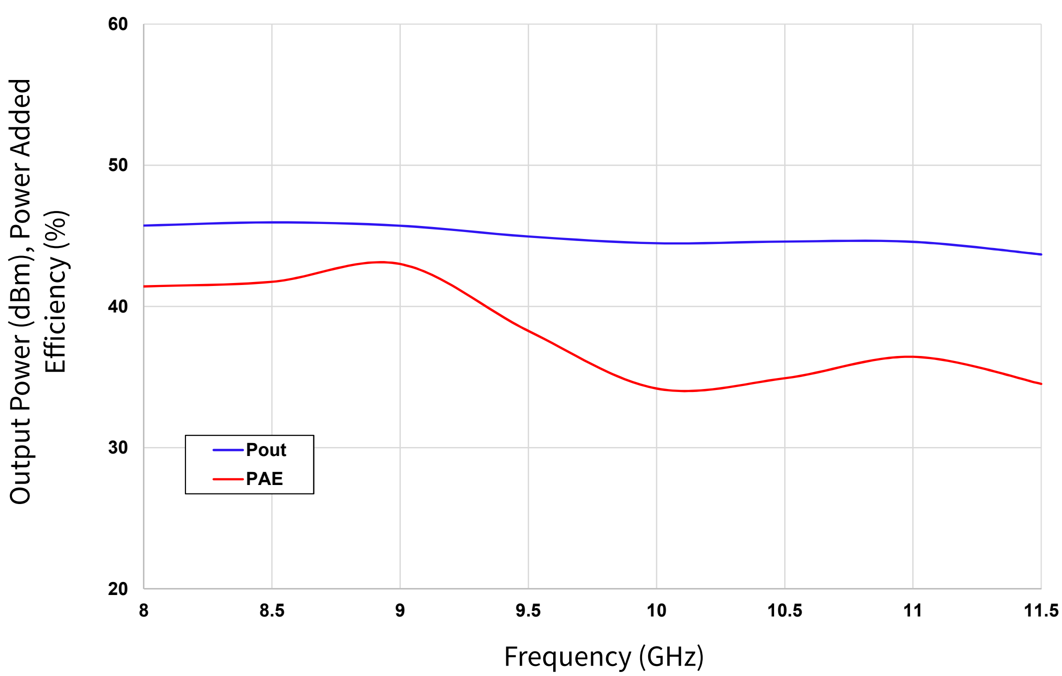
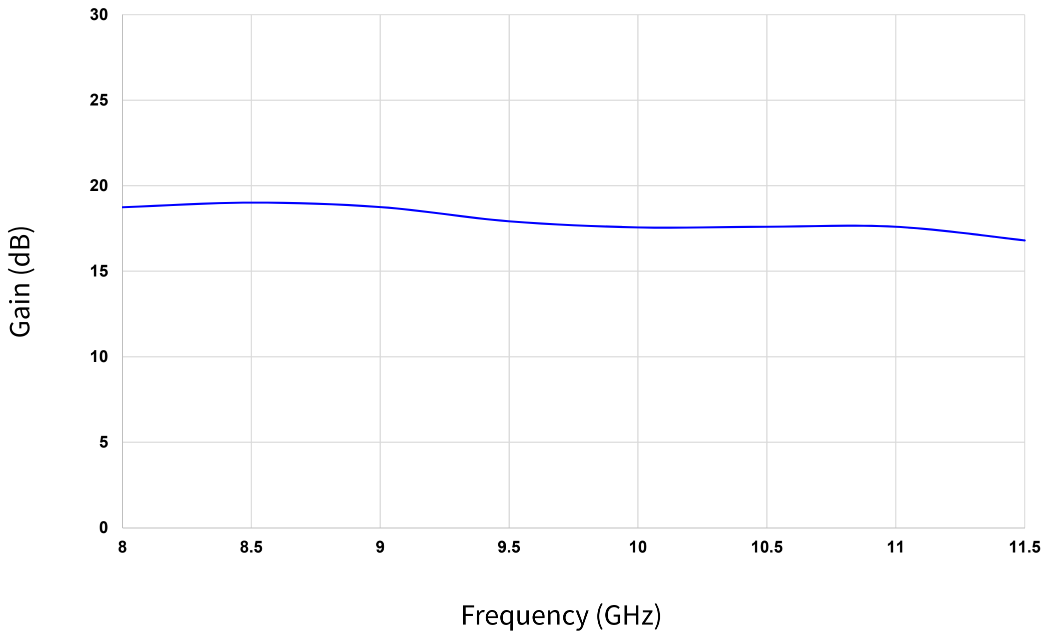


Figure 2. Output Power & Power Added Efficiency vs Frequency
V_{DD} = 28 V, I_{DQ} = 0.8 A, P_{IN} = 27 dBm, Pulse Width = 100µs, 10% Duty Cycle, Tc = 85°C



Typical Performance of the CMPA801B030D

Figure 3. Associated Gain vs Frequency
V_{DD} = 28 V, I_{DQ} = 0.8 A, P_{IN} = 27 dBm, Pulse Width = 100µs, 10% Duty Cycle, Tc = 85°C



Part Number System

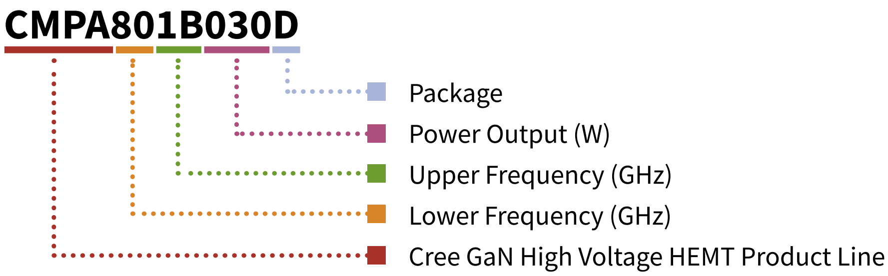


Table 1.

Parameter	Value	Units
Lower Frequency	8.0	GHz
Upper Frequency[1]	11.0	GHz
Power Output	30	W
Package	Bare Die	-

Note[1]: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Table 2.

Character Code	Code Value
A	0
B	1
C	2
D	3
E	4
F	5
G	6
H	7
J	8
K	9
Examples:	1A = 10.0 GHz 2H = 27.0 GHz

Product Ordering Information

Order Number	Description	Unit of Measure
CMPA801B030D	GaN MMIC Bare Die	Each

For more information, please contact:

4600 Silicon Drive
Durham, North Carolina, USA 27703
www.wolfspeed.com/RF

Sales Contact
RFSales@cree.com

Notes

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