

MGV High Current Molded SMT Power Inductors MGV0502 Series

FEATURES AND APPLICATIONS

Laird MGV series high current power inductors improve performance, reliability and power efficiency. A lower profile benefits consumer electronics and telecom design. Products feature extremely low DCR with greater efficiency and enable a large current in a small size. Inductors are of magnetic shielding and molded construction and perform in operating temperatures ranging from -40 C to 125 C including self-heating rise in temperature.

FEATURES

- Magnetic shielded structure
- Low DCR and high efficiency
- Low profile and miniaturization
- High reliability

APPLICATIONS

- DC-DC Converter and Power Suppliers
- LCD TV'S and Gaming Console
- Tablet, Notebooks, Servers and Printers
- Networking and Data storage
- GPS, Set-top-box and Base stations

MGV 0502 / R7

• Smart meters and Medical instruments

PART NUMBER EXPLANATION



	0 3 0 2			ТU
Product series code	Product size code	Inductance value code (i.e. 4R7: 4.7 µH)	Tolerance % (i.e. M: \pm 20%)	Standard Catalog P.N

Note: Automotive grade parts are also available, a specific P.N will be assigned upon request. Please contact laird local sales for details.

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ELECTRICAL SPECIFICATIONS

- Tolerance: M: ±20% or N: ±30%
- Inductance tested at 100KHz, 1.0V
- Heat Rated Current (Irms) is defined based on temperature rise approximate 40°C without core loss (ambient temperature 25±5°C)
- Saturation Current (Isat) is the DC current at which the inductance drops off approximately 30% from its value without current. (ambient temperature 25±5°C)
- Operating temperature range: -40°C~+125°C (including self-heating temperature rise)
- Storage temperature range (packaging conditions): -10°C~+40°C and RH 60%(MAX.)

Note: Heat Rated Current (Irms) is tested on a typical PCB and apply a constant current in still air.

The temperature rise is dependent on the application system condition including PCB PAD pattern, trace width and thickness and adjacent components etc. It's suggested to verify the temperature rise of the component under the real operation application conditions.



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				www.laird.com	MGV0502 Seri	es Rev: A
SPECIFICATI	ION					
PART NUMBER	INDUCTANCE (uH)	Irms(A) Typ.	Isat(A) Typ.	DCR(mΩ) Typ	DCR(mΩ) Max	REMARK
MGV0502R10N-10	0.10±30%	18.0	45.0	3.6	4.0	
MGV0502R47M-10	0.47±20%	11.5	18.0	7.3	8.6	
MGV0502R68M-10	0.68±20%	10.0	12.8	11.0	12.4	
MGV05021R0M-10	1.00±20%	7.0	13.7	17.5	20.0	
MGV05021R2M-10	1.20±20%	6.2	11.0	23.0	28.0	
MGV05021R5M-10	1.50±20%	5.5	9.8	26.5	30.5	
MGV05022R2M-10	2.20±20%	4.2	9.0	42.0	50.0	
MGV05023R3M-10	3.30±20%	3.3	7.3	66.0	76.0	
MGV05026R8M-10	6.80±20%	2.4	3.8	130.0	150.0	
MGV0502100M-10	10.00±20%	2.3	3.4	180.0	199.0	
ENERAL SPECI	FICATION:					
Inductance tested	d at 100KHz, 0.25V					
Heat Rated Curre	nt (Irms) is defined	based on tem	perature rise	approximate 40°	C without core lo	SS
(ambient tempera	ature 25±5°C)					
Saturation Currer	nt (Isat) is the DC cu	rrent at which	the inductan	ice drops off app	roximately 30% fi	rom
its value without	current. (ambient t	emperature 2	5±5°C)			
Operating temper	rature range: -40°C'	~+125°C (inclu	iding self-hea	ting temperature	rise)	
Storage temperat	ure range (packagi	ng conditions)	: -10°C~+40°C	and RH 60%(MA	X.)	



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Hand solder times: 1 time max

Laird Performance Materials



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Reliability and Te	stina Conditions / Pin Tvpe Po	wer Inductors					
SMD series(Consumer)							
ltem	Reference	Additional Requirements					
Operating temperature range	-55°C ~ +125°C (Including self-temperature rise)						
Storage temperature and humidity range	-10 $^\circ\!{\rm C}$ to +40 $^\circ\!{\rm C}$, 60% RH Max						
High Temperature Exposure (Storage)	MIL-STD-202 Method 108 85±2℃, 168+24hours						
Temperature Cycling	JESD22 Method JA-104	-40°C → +85, transforming interval:20s, 100cycles					
Operational Life	MIL-PRF-2	85±℃, 168+24hours Apply maximum rated voltage and current according part draw					
External Visual	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship. Elect Test not required.					
Physical Dimension	ESD22 Method JB-100 ESD22 Met						
Vibration	MIL-STD-202 Method 204	10~55Hz,1.5mm, 2 hours in each 3mutually perpendicular directions (total of 6 hours)					
Resistance to Soldering Heat	MIL-STD-202 Method 210	1. Max. 260±5 [°] C,10±1s, 2 times 2.Solder Composition: Sn/3Ag/0.5Cu					
Solderability	J-STD-002	245±5°C, 5±1sec, Solder: Sn/3.0Ag/0.5Cu					
Electrical Characterization	Print Spec	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max Operating temperatures					
Board Flex	AEC-Q200-005	2mm,30±1s					
Terminal Strength(SMD)	AEC-Q200-006	10N, 5S, X,Y direct					



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4. Allowable stacking condition of Packaging box: max height 1.5m or 5 boxes stacking