

Versal[™] Premium Series VPK120 Evaluation Kit

OVERVIEW

The VPK120 Evaluation Kit features the Versal Premium series VP1202 device, offering networked, power-optimized cores, including 600G Ethernet cores, 400G High-Speed Crypto Engines, and 100G multirate Ethernet cores.

The kit comes with a breadth of connectivity options, development tools, and example designs to accelerate prototyping the most demanding compute and networking systems across multiple markets.

HIGHLIGHTS

Versal Premium Series Capabilities

- > High I/O bandwidth and logic density
- > PCle[®] Gen5 with built-in DMA & CCIX
- > 600G Ethernet cores
- > 100G multirate Ethernet cores
- > 400G High-Speed Cryptographic (HSC) engines

Breadth of Onboard Connectivity Options

- > PCIe Gen5x8 edge connector
- > QSFP-DD ports supporting up to 112Gb/s over copper and optical
- > FPGA Mezzanine Card (FMC+) interface for I/O expansion
- > 1GbE USB3.0 MicroSD card reader
- > 12GB of onboard LPDDR4 memory

Development Tools and Enhanced Debug Methodology

- > Co-optimized with Vivado[™] ML Design Suite and the Vitis[™] unified software platform
- > Shipped with pre-built boot images



TARGET APPLICATIONS

Wired Communication

- > Metro Core Transport
- > Data Center Interconnect
- > Security Appliances

Data Center

- Search
- > Recommendation
- > Video Analytics

Test and Measurement

- > Network Testers
- > PCIe Protocol Analyzers

Aerospace and Defense

- > Avionics
- > Secure Communications

KIT CONTENTS



TAKE THE NEXT STEP

For more information, documents, and reference designs, or to purchase, visit xilinx.com/vpk120

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