

EVALUATION BOARD MANUAL

FOR RADIO MODULES

WE order code	Former order code	Marketing Name
260801102400x	AMB2621(-TR)	Proteus-I
260801112400x	AMB2621-1(-TR)	Proteus-I
260801102401x	AMB2623(-TR)	Proteus-II
260801112401x	AMB2623-1(-TR)	Proteus-II

VERSION 1.2

MARCH 5, 2019

Revision history

Manual version	HW version	Notes	Date
0.3	2.0	Initial version	November 2016
1.0	2.0	 New corporate design and structure 	February 2019
1.1	2.0	• Added chapter Regulatory compliance information	February 2019
1.2	2.0	Added Marketing name	March 2019

Abbreviations and abstract

Abbreviation	Name	Description
FSE	Field Sales Engineer	Support and sales contact person responsible for limited sales area
HIGH	High signal level	
LOW	Low signal level	
RF	Radio frequency	Describes everything relating to the wireless transmission.
UART	Universal Asynchronous Receiver Transmitter	Universal Asynchronous Receiver Transmitter allows communicating with the module of a specific interface.
VDD	Supply voltage	

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1 Supported radio modules

The evaluation board described in this manual can be used to evaluate the following products:

Order code	Former order code	Marketing Name
260801102400x	AMB2621(-TR)	Proteus-I
260801112400x	AMB2621-1(-TR)	Proteus-I
260801102401x	AMB2623(-TR)	Proteus-II
260801112401x	AMB2623-1(-TR)	Proteus-II

Order code	Description
260801102400x	Bluetooth Low Energy 4.2 radio module with integrated antenna
260801112400x	Bluetooth Low Energy 4.2 radio module with antenna pad
260801102401x	Bluetooth Low Energy 5.0 radio module with integrated antenna
260801112401x	Bluetooth Low Energy 5.0 radio module with antenna pad

Table 1: Compatibility



Figure 1: Product image

2 Functional description

The evaluation board offers the user the possibility to develop hard- and software for the compatible radio module. It can be connected to an USB port of a PC.

For the connection to a micro controller system the development board is equipped with a multi-pin connector which is connected to all pins of the RF module. Jumpers allow the module to be disconnected from components such as the USB interface which are not required.

Feel free to check our youtube channel:

www.youtube.com/user/WuerthElektronik/videos for video tutorials, hands-ons and webinars relating to our products.

2.1 Taking into operation

To run the evaluation board place the jumpers on default location. The corresponding FTDI driver package (*www.ftdichip.com/Drivers/VCP.htm*) has to be installed on your PC.

Connect the power jack or external power supply to the EV board and make sure the VCC is stable and able to reliably supply the module's static and peak current consumption as specified by the module manual.

The next step is to connect the evaluation board to the PC using a USB-cable. In that way a COM port can be detected and installed on your PC. Check the device manager to acquire the COM port name of the EV board. A typical name is "COM12" in Windows systems or "/dev/ttyUSB0" in Linux systems.

A terminal program (like hterm for Windows) has to be run and the corresponding COM port has to be opened using the default settings of the mounted radio module.

After the module is powered through the USB jack or an alternative power supply, the reset button should be pressed to ensure a clean start-up of the module.

Please refer to the module reference manual to get the detailed module specific quick start instructions.

3 Development board

3.1 Block diagram





3.2 Jumpers



Figure 3: Jumpers, defaults

JP2	Module Pin Function	Jumper set (default)
1,2	BOOT	Yes
3,4	LED3	Yes
5,6	LED2	Yes
7,8	UART TX	Yes
9,10	UART RX	Yes
11,12	Wakeup	Yes

JP3	Module Pin Function	Jumper set (default)
1,2	Connect BOOT pin to GND if set	No

JP4	Function	Jumper set (default)
1,2	Power bridge	Yes

JP5	CBUS Connections	Jumper set (default)
1,2	Reset	No
3,4	Wake-up	No
5,6	Boot	No

JP6	Function	Jumper set (default)
1,2	Connect SWCLK to GND if set	Yes

3.3 Connectors and pin headers



Figure 4: Connectors

USB	Function
	Mini-USB connector for host connection and VCC bus supply

P1	Function
1	P0.28 / AIN4
2	P0.04 / AIN2
3	P0.09 / NFC1
4	P0.10 / NFC2
5	VCC 3V
6	GND

P2	Function
1	VCC 3V
2	GND
3	VCC 5V

P3	Function
1-20	2×10 JTAG/SWD Connector

3.4 Buttons



Figure 5: Buttons

3.4.1 /RST button

Internally the active low reset input of the micro processor is connected via a RC combination with the power supply to ensure a proper startup of the module. Any module provides a */RESET* pin that is connected to this button so the module can be restarted properly. Most modules provide an internal pull-up resistor. Please refer to the module specific manual for detailed information.

3.4.2 Wake-up button

Some modules use the wake-up button connected to their *WAKE-UP* pin to exit from sleep mode of the module. Please refer to the module specific manual for detailed information.

3.5 Function blocks

3.5.1 Power supply

3.5.1.1 Bus powered, power supply through USB

The development board can be run via USB. The integrated voltage regulator regulates the connected USB voltage 5V down to 3V and supplies the remaining parts of the circuit. If the evaluation board is power sourced the power *LED1* lights up.

3.5.2 JP4 - Current measurement

By default, JP4 is bridged for normal operation. If a current meter is connected in place of the jumper, the power consumption of the radio module can be measured.

If the meter is not attached and the bridge is not set, the module will not receive a supply voltage. However, the power *LED1* may be active, as it is connected prior to the current measurement bridge in order not to distort the module's power consumption.

3.5.3 UART / USB

The UART of the module can be connected to the USB converter by setting the bridge JP2. In this case it is available on the USB jack. Using the FTDI-driver the PC will show a virtual COM-Port which can be used to communicate with the module.



The USB cable length must not exceed 3 meters.

3.5.4 UART direct

If a micro-controller is to be connected to the module, remove the bridges on JP5. The UART can be connected directly on the pin strip JP5 (all odd numbered pins). The module *RXD* line must be handled accordingly by your host (i.e. pulled up while inactive and during module boot-up).

Beware of IO level incompatibility. The host must obey the values stated in the module's manual. Especially the IO level restrictions must be implemented by a host system (i.e. using a level shifter to use the allowed IO levels).

3.5.5 LFXO crystal

For higher LFCLK accuracy (better than ±250ppm) a low frequency crystal oscillator of about 32.768kHz (LFXO) may be used. A crystal, 3.2 ×1.6mm package, for example CC7V-T1A 32.768kHz 12.5pF 10ppm TA QC, can be placed on the evaluation board to position Q2.

The needed load capacitance can be reached with capacitors C31 and C41, 0402 package. Depending on parasitic capacities a capacity of 6.8pF may be a good starting value for C31 and C41.

Furthermore, the LFXO Crystal avoids the regularly recalibration of the clock and therefore optimizes low power current consumption.

If a LFXO is mounted to the EV board the JP2.3-4 and JP2.5-6 jumpers shall not be connected, therefore the LED2 and LED3 function is no longer available.



Using standard firmware the external crystal is not needed. To enable use of the LFXO a custom firmware is required.



Figure 6: LFXO mounting

3.5.6 NFC



Using standard firmware NFC is not supported. To enable use of the NFC a custom firmware is required and an antenna and antenna matching network needs to be connected to the corresponding pins.

3.5.7 Programming interface

The evaluation board provides a 2×10 pin connector to connect directly to a JTAG flash adapter used for development. Please take care of the correct mounting of the flash adapter. The recommended flash adapter is one of the "Segger J-Link" family.

Jumper JP6 will prevent the module from starting in debug mode when no flash adapter is connected. To apply this, remove the flash adapter connection. Make sure a jumper at JP6 is placed. Unplug and replug the USB connection of the device. Press the reset button on the evaluation board.

P3 Pin	SWD Function
1	VCC
7	SWDIO
9	SWCLK
15	/RST
2, 3, 5, 7, 11, 13, 17, 19	n.c.
6, 8, 10, 12, 14, 16, 18, 20	GND

3.6 Schematic



Figure 7: Circuit diagram

3.7 Layout







Figure 9: Top & bottom Layer

4 Regulatory compliance information

Pursuant to Article 1 (2.) of the EU directive 2014/53/EU, Article 1 (2.) the directive does not apply to equipment listed in Annex I (4.): custom-built evaluation kits destined for professionals to be used solely at research and development facilities for such purposes.

4.1 Exemption clause

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more than you expect



Rudi-Schillings-Str. 31 54296 Trier Germany

Tel.: +49 651 99355-0 Fax.: +49 651 99355-69 www.we-online.com/wireless-connectivity