MSWSE-050-17 Silicon PIN Diode Switch Element









Description

The MSWSE-050-17 is a PIN diode SPST switch element designed for medium incident power applications, up to 40W C.W. It has low insertion loss.

Features

- Broadband performance up to 3.0 GHz
- Low Insertion Loss
- Cost effective choice for switch applications

Maximum Ratings

RATING	LIMITS	UNITS	
V_R	500	V	
I _F	500	mA	
θ^{1C}	10	°C / W	
T _J	+175	°C	
T _{STG}	-55 to +150	°C	
T _{SOLDER}	+260 °C per JEDEC STD-J-20C		

Electrical Characteristics, $T_C = +25 \text{ }^{\circ}\text{C}$

SYMBOL	TEST CONDITIONS			MIN	TYPICAL	MAX	UNITS
$V_{_{\mathrm{BR}}}$	$I_R = 10 \mu A$			500	_	-	V
$V_{_{\rm F}}$	I _F = 50 mA			-	850	950	mV
\mathbf{C}_{T}	$V_R = 50 \text{ V}$		F= 1 MHz	_	0.20	0.25	pF
R_s	$I_{\rm F} = 100 {\rm mA}$		F= 100 MHz	_	0.75	_	Ω
τ	$I_F = 10 \text{ mA}$	$I_R = 6 \text{ mA}$	measured at 50%	_	1600	_	ns
W	I-Layer			_	80	_	μ m
I _L	$I_F = 50 \text{ mA}$		F < 0.5 GHz	_	0.06	0.2	dB
			F < 2.0 GHz	_	0.1	0.2	dB
IRL / ORL	$I_F = 50 \text{ mA}$		F < 0.5 GHz	25	33	-	dB
			F < 2.0 GHz	20	25	_	dB
I _{so}	$V_R = 50 \text{ V}$		F < 0.5 GHz	22	24	_	dB
			F < 2.0 GHz	20	12	_	dB

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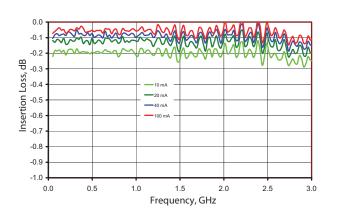


Revision Date: 2/21/2014

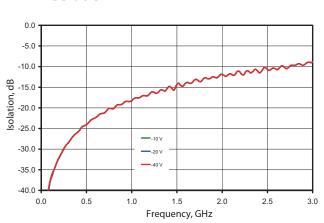




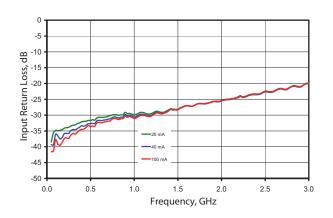
Insertion Loss



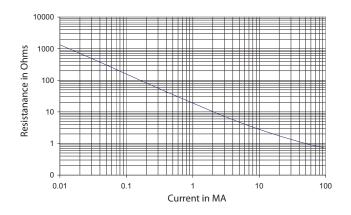
Isolation



Input Return Loss



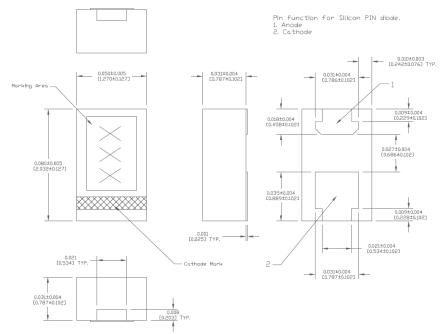
Series Resistance vs. Current



MSWSE-050-17

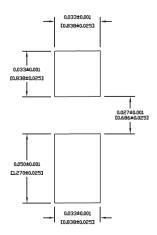


Package Outline

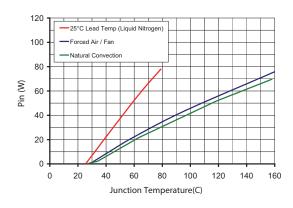


Dimensions: inches [mm] Finish: 300 micro inches matte Tin, annealed 1 hour 150° C

Printed Circuit Board Layout



Junction Temperature vs Power 20 Mil PCB Mounted on Heat Sink 25°C Ambient 1.3 GHz





MSWSE-050-17

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Our passion for performance is defined by three attributes represented by these three icons: solution-minded, performance-driven and customer-focused.