

#### Product Brief 2016

# CeraLink™

#### DC Link and Snubber Capacitors

Ceralink<sup>™</sup> is a family of very compact capacitors for stabilizing voltages in the DC link. They are therefore suitable for use as either snubber or DC link capacitors. These products based on PLZT ceramics are designed to provide engineers with compact components optimized for fast-switching converters, converters with very tight space requirements and converters that need to withstand high operating temperatures.

The basic component is a ceramic chip which either is manufactured with lead frames (LP series), or which can be combined modularly to form capacitors with higher capacitance values (SMD and SP types).

### The key benefits of the CeraLink™ technology are:

- High operating and peak temperatures
- Low ESL
- Low ESR
- High capacitance density and small size
- Low losses at high frequencies and high temperatures
- Supports fast switching semiconductors and high switching frequencies
- Supports further miniaturization of power electronics at the system level
- Reduction of total system cost
- Increased robustness against short circuit by internal series electrode design



## CeraLink™, LP, SMD and SP Series

Technical data										
Loss factor @ 0 V <sub>DC</sub> , 0.5 V <sub>RMS</sub> , 1 kHz, 25 °C					$tan \delta$			< 0.02		
Device temperature allowed for all operating time					T <sub>device</sub>			–40 °C 125 °C		
Device temperature allowed for 5% of operating time					T <sub>device, max</sub>			+125 °C 150 °C		
Ordering code	C <sub>nom, typ</sub> @ V <sub>op</sub> , 25 °C, quasistatic μF	V <sub>op</sub>	V <sub>R</sub> V	V <sub>ws</sub>	I <sub>op1</sub> @ 100 kHz, 85 °C A <sub>RMS</sub>	I <sub>ор2</sub> @ 100 kHz, 105 °С А <sub>RMS</sub>	ESL nH	Approx. weight g	Packing	Pcs. per packing unit
Low profile series – LP (L leads)										
B58031I5105M002 B58031I5105M062	> 1	400	500	650	7.5	5.2	2.5	1.3	cut tape taped on reel	100 1000
B5803117504M002 B5803117504M062	> 0.5	600	700	1000	5.4	4.4			cut tape taped on reel	100 1000
B58031I9254M002 B58031I9254M062	> 0.25	800	900	1300	3.6	2.9			cut tape taped on reel	100 1000
Low profile series – LP (J leads)										
B58031U5105M002 B58031U5105M062	> 1	400	500	650	7.5	5.2	2.5	1.2	cut tape taped on reel	100 1000
B58031U7504M002 B58031U7504M062	> 0.5	600	700	1000	5.4	4.4			cut tape taped on reel	100 1000
B58031U9254M002 B58031U9254M062	> 0.25	800	900	1300	3.6	2.9			cut tape taped on reel	100 1000
Surface mountable device series – SMD										
B58032I5505M062	> 5	400	500	650	15.3	11.7	2.5	6	taped on reel	250
B58032I7255M062	> 2.5	600	700	1000	14	10.5			taped on reel	250
B58032I9125M062	> 1.25	800	900	1300	10	7.8			taped on reel	250
Solder pin series – SP										
B58033I5206M001	> 20	400	500	650	31.5	24.5	3.5	31	tube	20
B58033I7106M001	> 10	600	700	1000	26	17.7			tube	20
B58033I9505M001	> 5	800	900	1300	19	13			tube	20

Note: Full product range to be released in 2016. Samples available. For details contact our locale sales office.

#### Main applications



#### Symbols and terms

 $\rm C_{nom},$  typ (@  $\rm V_{op},$  quasistatic, 25 °C): typical nominal capacitance

 $\rm V_R\!\!:$  rated voltage, reference DC voltage for the reliability tests

 $V_{\rm op}$ : operating voltage, optimized DC voltage in terms of capacitance value

 $V_{\text{WS}}$ : withstand voltage, 100% end of line test for  $\geq 7$  seconds

# CeraLink™, LP, SMD and SP Series



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Structure of ordering codes: The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of EPCOS, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes.

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