# BLF6G27L-50BN

## **Power LDMOS transistor**

**AMPLEON** 

Rev. 4 — 1 September 2015

Product data sheet

### 1. Product profile

#### 1.1 General description

50~W LDMOS power transistor for base station applications at frequencies from 2500~MHz to 2700~MHz.

#### Table 1. Typical performance

RF performance at  $T_{case} = 25$  °C in a common source class-AB production test circuit.

| Test signal      | f            | $I_{Dq}$ | V <sub>DS</sub> | P <sub>L(AV)</sub> | G <sub>p</sub> | $\eta_D$ | ACPR           |
|------------------|--------------|----------|-----------------|--------------------|----------------|----------|----------------|
|                  | (MHz)        | (mA)     | (V)             | (W)                | (dB)           | (%)      | (dBc)          |
| 2-carrier W-CDMA | 2500 to 2700 | 430      | 28              | 3                  | 16.5           | 14.5     | -47 <u>[1]</u> |

<sup>[1]</sup> Test signal: 3GPP; test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF per carrier; carrier spacing 5 MHz

#### 1.2 Features and benefits

- Easy power control
- Integrated ESD protection
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (2500 MHz to 2700 MHz)
- Internally matched for ease of use
- Integrated current sense
- Compliant to Restriction of Hazardous Substances (RoHS) Directive 2002/95/EC

### 1.3 Applications

RF power amplifiers for base stations and multi carrier applications in the 2500 MHz to 2700 MHz frequency range

## 2. Pinning information

Table 2. Pinning

| Pin  | Description |     | Simplified outline | Graphic symbol     |
|------|-------------|-----|--------------------|--------------------|
| 1    | drain       |     |                    |                    |
| 2    | gate        |     | 4 5<br>            | 1 4,5              |
| 3    | source      | [1] |                    |                    |
| 4, 5 | sense drain |     |                    | 2 6, 7<br>3 sym126 |
| 6, 7 | sense gate  |     | 3 6 7              | 3 Symres           |
|      |             |     | 0 7                |                    |

[1] Connected to flange.

## 3. Ordering information

Table 3. Ordering information

| Type number   | Packag | je   |          |
|---------------|--------|--|----------|
|               | Name   | Description  | Version  |
| BLF6G27L-50BN | -      | flanged ceramic package; 2 mounting holes; 6 leads | SOT1112A |

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol                 | Parameter                 | Conditions | Min  | Max  | Unit |
|------------------------|---------------------------|------------|------|------|------|
| $V_{DS}$               | drain-source voltage      |            | -    | 65   | V    |
| $V_{GS}$               | gate-source voltage       |            | -0.5 | +13  | V    |
| V <sub>GS(sense)</sub> | sense gate-source voltage |            | -0.5 | +9   | V    |
| T <sub>stg</sub>       | storage temperature       |            | -65  | +150 | °C   |
| Tj                     | junction temperature      |            | -    | 200  | °C   |

## 5. Thermal characteristics

Table 5. Thermal characteristics

| Symbol                  | Parameter                                | Conditions  | Тур | Unit |
|-------------------------|--|---|-----|------|
| R <sub>th(j-case)</sub> | thermal resistance from junction to case | T <sub>case</sub> = 80 °C; P <sub>L</sub> = 12.5 W (CW) | 1.3 | K/W  |

#### 6. Characteristics

Table 6. Characteristics

 $T_i = 25$  °C per section; unless otherwise specified

| Symbol               | Parameter                        | Conditions   | Min | Тур  | Max | Unit |
|----------------------|----------------------------------|--|-----|------|-----|------|
| V <sub>(BR)DSS</sub> | drain-source breakdown voltage   | $V_{GS} = 0 \text{ V}; I_D = 0.5 \text{ mA}$                       | 65  | -    | -   | V    |
| V <sub>GS(th)</sub>  | gate-source threshold voltage    | $V_{DS} = 10 \text{ V}; I_{D} = 72 \text{ mA}$                     | 1.4 | 1.9  | 2.4 | V    |
| I <sub>Dq</sub>      | quiescent drain current          | sense transistor:  | 380 | 430  | 480 | mA   |
|                      |                                  | I <sub>DS</sub> = 9.1 mA;<br>V <sub>DS</sub> = 26.5 V              |     |      |     |      |
|                      |                                  | main transistor:   |     |      |     |      |
|                      |                                  | V <sub>DS</sub> = 28 V   |     |      |     |      |
| I <sub>DSS</sub>     | drain leakage current            | V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 28 V                      | -   | -    | 1.5 | μΑ   |
| I <sub>DSX</sub>     | drain cut-off current            | $V_{GS} = V_{GS(th)} + 3.75 \text{ V};$<br>$V_{DS} = 10 \text{ V}$ | 10  | 12   | -   | Α    |
| I <sub>GSS</sub>     | gate leakage current             | V <sub>GS</sub> = 11 V; V <sub>DS</sub> = 0 V                      | -   | -    | 150 | nA   |
| g <sub>fs</sub>      | forward transconductance         | $V_{DS} = 10 \text{ V}; I_{D} = 3.6 \text{ A}$                     | -   | 5.0  | -   | S    |
| R <sub>DS(on)</sub>  | drain-source on-state resistance | $V_{GS} = V_{GS(th)} + 3.75 \text{ V};$<br>$I_D = 2.52 \text{ A}$  | -   | 0.25 | -   | Ω    |

## 7. Application information

#### Table 7. 2-carrier W-CDMA application information

All testing performed in Class-AB production test circuit; test signal 3GPP; test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF per carrier; carrier spacing 5 MHz;  $f_1 = 2500 \text{ MHz}$ ;  $f_2 = 2600 \text{ MHz}$ ;  $f_3 = 2700 \text{ MHz}$ ; RF performance at  $V_{DS} = 28 \text{ V}$ ;  $I_{Dq} = 430 \text{ mA}$ ;  $T_{case} = 25 \text{ °C}$ ; unless otherwise specified.

| Symbol     | Parameter                    | Conditions               | Min  | Тур  | Max | Unit |
|------------|------------------------------|--------------------------|------|------|-----|------|
| Gp         | power gain                   | P <sub>L(AV)</sub> = 3 W | 15.3 | 16.5 | -   | dB   |
| $\eta_{D}$ | drain efficiency             | P <sub>L(AV)</sub> = 3 W | 12.5 | 14.5 | -   | %    |
| ACPR       | adjacent channel power ratio | P <sub>L(AV)</sub> = 3 W | -    | -47  | -43 | dBc  |
| $I_{Dq}$   | quiescent drain current      | V <sub>DD</sub> = 28 V   | -    | 430  | -   | mA   |

#### Table 8. 1-carrier W-CDMA application information

All testing performed in Class-AB production test circuit; test signal 3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF per carrier; f = 2700 MHz; RF performance at  $V_{DS} = 28 \text{ V}$ ;  $I_{Dq} = 430 \text{ mA}$ ;  $T_{case} = 25 \text{ }^{\circ}\text{C}$ ; unless otherwise specified.

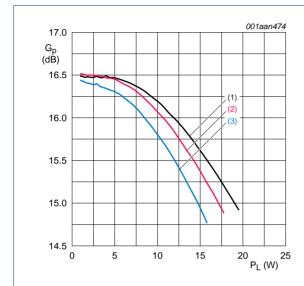
| Symbol | Parameter                    | Conditions                | Min | Тур | Max | Unit |
|--------|------------------------------|---------------------------|-----|-----|-----|------|
| PARO   | output peak-to-average ratio | P <sub>L(AV)</sub> = 16 W | 4.1 | 4.7 | 5.3 | dB   |

#### 7.1 Ruggedness in Class-AB operation

The BLF6G27L-50BN is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA;  $P_{L}$  = 40 W (CW); f = 2500 MHz.

### 7.2 Single carrier IS-95

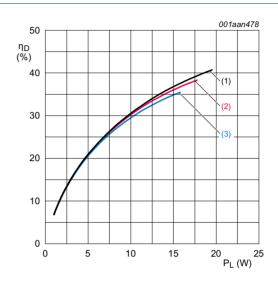
Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

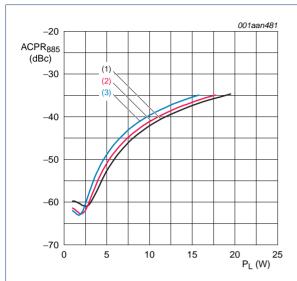
Fig 1. Single carrier IS-95 power gain as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

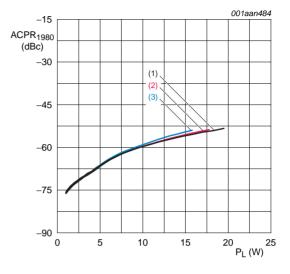
Fig 2. Single carrier IS-95 drain efficiency as a function of output power; typical values



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

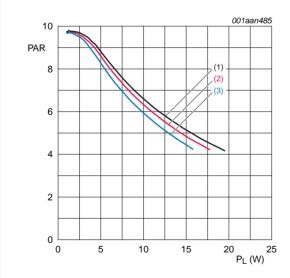
Fig 3. Single carrier IS-95 ACPR at 885 kHz as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

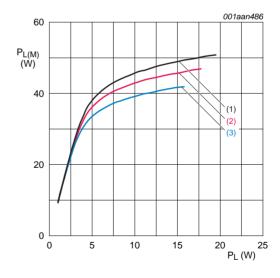
Fig 4. Single carrier IS-95 ACPR at 1980 kHz as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 5. Single carrier IS-95 peak-to-average power ratio as a function of output power; typical values

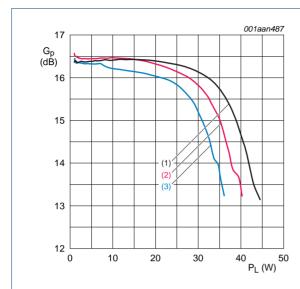


 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 6. Single carrier IS-95 peak power as a function of output power; typical values

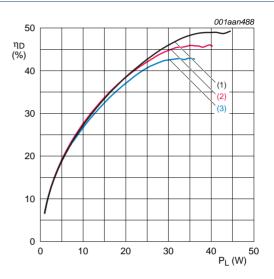
#### 7.3 Pulsed CW



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 7. Pulsed CW power gain as a function of output power; typical values



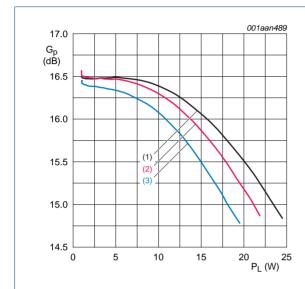
 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 8. Pulsed CW drain efficiency as a function of output power; typical values

#### 7.4 2-carrier W-CDMA

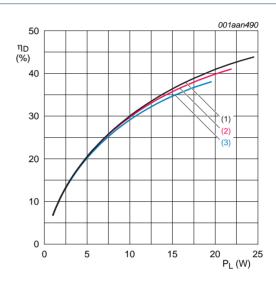
All testing performed in Class-AB production test circuit; test signal 3GPP; test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF per carrier; carrier spacing 5 MHz;  $f_1$  = 2500 MHz;  $f_2$  = 2600 MHz;  $f_3$  = 2700 MHz;  $T_{case}$  = 25 °C; unless otherwise specified.



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

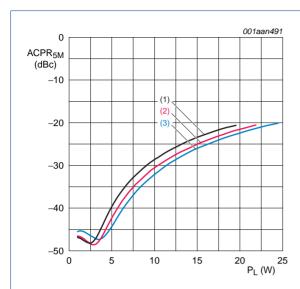
Fig 9. 2-carrier W-CDMA power gain as a function of output power; typical values



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

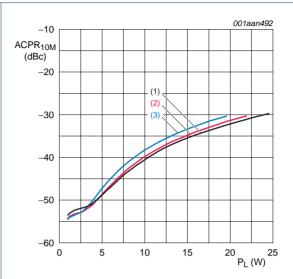
Fig 10. 2-carrier W-CDMA drain efficiency as a function of output power; typical values



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 11. 2-carrier W-CDMA ACPR at 5 MHz as a function of output power; typical values



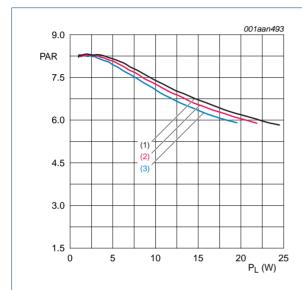
 $V_{DS}$  = 28 V;  $I_{Dq}$  = 430 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 12. 2-carrier W-CDMA ACPR at 10 MHz as a function of output power; typical values

### 7.5 Single carrier W-CDMA

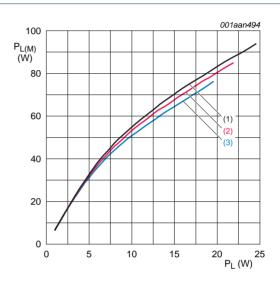
All testing performed in Class-AB production test circuit; test signal 3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF per carrier; f = 2700 MHz;  $T_{case} = 25$  °C; unless otherwise specified.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 13. Single carrier W-CDMA peak-to-average power ratio as a function of output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 430 \text{ mA}.$ 

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 14. Single carrier W-CDMA peak output power as a function of output power; typical values

## 8. Package outline

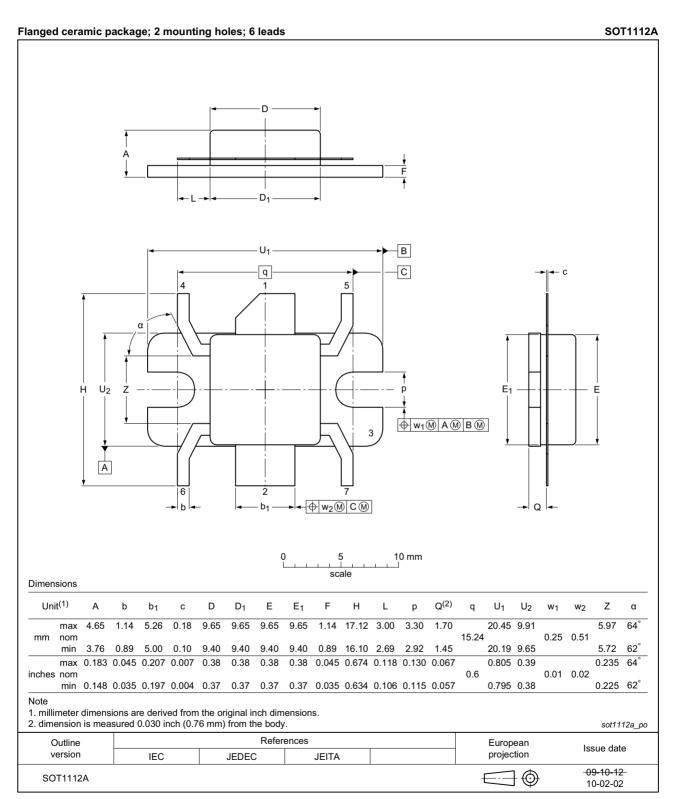


Fig 15. Package outline SOT1112A

## 9. Handling information

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

## 10. Abbreviations

Table 9. Abbreviations

| Acronym | Description                                    |
|---------|--|
| 3GPP    | 3rd Generation Partnership Project             |
| CCDF    | Complementary Cumulative Distribution Function |
| CW      | Continuous Wave                                |
| DPCH    | Dedicated Physical CHannel                     |
| ESD     | ElectroStatic Discharge                        |
| IS-95   | Interim Standard 95                            |
| LDMOS   | Laterally Diffused Metal-Oxide Semiconductor   |
| PAR     | Peak-to-Average Ratio                          |
| VSWR    | Voltage Standing-Wave Ratio                    |
| W-CDMA  | Wideband Code Division Multiple Access         |

## 11. Revision history

Table 10. Revision history

| Document ID                   | Release date   | Data sheet status        | Change notice   | Supersedes                        |  |
|-------------------------------|--|--------------------------|-----------------|-----------------------------------|--|
| BLF6G27L-50BN#4               | 20150901   | Product data sheet       | -               | BLF6G27L-50BN v.3                 |  |
| Modifications:                | The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. |                          |                 |                                   |  |
|                               | <ul> <li>Legal texts I</li> </ul>  | nave been adapted to the | new company nam | e where appropriate.              |  |
| BLF6G27L-50BN v.3             | 20141008   | Product data sheet       | -               | BLF6G27L-50BN_<br>6G27LS-50BN v.2 |  |
| BLF6G27L-50BN_6G27LS-50BN v.2 | 20110407   | Product data sheet       | -               | BLF6G27L-50BN_<br>6G27LS-50BN v.1 |  |
| BLF6G27L-50BN_6G27LS-50BN v.1 | 20100916   | Objective data sheet     | -               | -                                 |  |

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| Document status[1][2]          | Product status[3] | Definition  |
|--------------------------------|-------------------|---|
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| Preliminary [short] data sheet | Qualification     | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production        | This document contains the product specification.                                     |

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## **AMPLEON**

# BLF6G27L-50BN

#### **Power LDMOS transistor**

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