

## CGHV38375F

400 W, 2.75 - 3.75 GHz, Internally-Matched, GaN-on-Silicon Carbide Transistor (IM-FET)

## Description

Wolfspeed's CGHV38375F is a packaged, 400 W HPA matched to 50 ohms at both input and output ports. The CGHV38375F operates from 2.75 - 3.75 GHz providing coverage over the entire S-Band radar band. This highpower amplifier provides >10 dB of large signal gain and 40% power-added efficiency and is ideally suited as a high-power building block supporting both pulsed and CW radar applications.



Package Type: 440226 PN: CGHV38375F

## Typical Performance Over 2.75 - 3.75 GHz ( $T_c = 25^{\circ}C$ )

Parameter	2.75 GHz	2.9 GHz	3.3 GHz	3.5 GHz	3.75 GHz	Units
Small Signal Gain <sup>1,2</sup>	10.0	12.5	12.6	12.6	13.5	dB
Output Power <sup>1,3</sup>	55.9	57.4	57.5	57.7	56.8	dBm
Power Gain <sup>1,3</sup>	9.9	11.4	11.5	11.7	10.8	dB
Drain Efficiency <sup>1,3</sup>	50	67	62	60	60	%

Note:

 $1 V_{DD} = 50 V$ ,  $I_{DQ} = 500 mA$ 

2 Measured at  $P_{IN}$  = -10 dBm

3 Measured at  $P_{\text{IN}}$  = 46 dBm and 100  $\mu s;$  Duty Cycle = 10%

#### Features

- Full S-Band Radar Coverge
- 400 W Typical P<sub>SAT</sub>
- 55% Typical Drain Efficiency
- >10 dB Large Signal Gain
- Pulsed and CW Operation

Note: Features are typical performance across frequency under 25°C, pulsed operation. Please reference performance charts for additional details.

## Applications

• Civil and Military, Pulsed and CW S-Band Radar



Figure 1.



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## Absolute Maximum Ratings (not simultaneous) at 25°C

Parameter	Symbol	Rating	Units	Conditions
Drain-source Voltage	V <sub>DSS</sub>	150	N	ar°o
Gate-source Voltage	V <sub>GS</sub>	-10, +2	V <sub>DC</sub>	25°C
Storage Temperature	T <sub>STG</sub>	-55, +150	°C	
Maximum Forward Gate Current	l <sub>G</sub>	80	mA	25°C
Maximum Drain Current	I <sub>DMAX</sub>	24	A	
Soldering Temperature	Ts	260	96	
Junction Temperature	TJ	225	°C	MTTF > 1e6 Hours

## Electrical Characteristics (Frequency = 2.75 GHz to 3.75 GHz unless otherwise stated; $T_c = 25^{\circ}C$ )

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
DC Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-3.8	-3.0	-2.3	V	$V_{DS} = 10 \text{ V}, I_{D} = 83.6 \text{ mA}$
Gate Quiescent Voltage	$V_{GS(Q)}$	_	-2.7	—	V <sub>DC</sub>	$V_{DD} = 28 \text{ V}, I_{DQ} = 500 \text{ mA}$
Saturated Drain Current <sup>1</sup>	I <sub>DS</sub>	54.4	77.7	_	А	$V_{DS} = 6.0 \text{ V}, V_{GS} = 2.0 \text{ V}$
Drain-Source Breakdown Voltage	V <sub>BD</sub>	125	_	_	V	$V_{GS} = -8 V$ , $I_{D} = 83.6 mA$
<b>RF Characteristics<sup>2</sup></b>						
Small Signal Gain	S211	_	12.5	—	dB	P <sub>IN</sub> = -10 dBm
Output Power at 2.75 GHz	P <sub>OUT1</sub>	-	55.9	—		
Output Power at 2.9 GHz	P <sub>OUT2</sub>	_	57.4	—	]	
Output Power at 3.3 GHz	P <sub>OUT3</sub>	_	57.5	_	dBm	
Output Power at 3.5 GHz	P <sub>OUT4</sub>	_	57.7	_		
Output Power at 3.75 GHz	P <sub>OUT5</sub>	—	56.8	—		
Drain Efficiency at 2.75 GHz	$DE_1$	—	50	_		
Drain Efficiency at 2.9 GHz	DE <sub>2</sub>	—	67	_		
Drain Efficiency at 3.3 GHz	DE <sub>3</sub>	-	62	—	%	$V_{DD} = 50 \text{ V}, I_{DQ} = 500 \text{ mA}, P_{IN} = 46 \text{ dBm}$
Drain Efficiency at 3.5 GHz	$DE_4$	_	60	—		
Drain Efficiency at 3.75 GHz	DE₅	_	60	—		
Power Gain at 2.75 GHz	G <sub>P2</sub>	_	9.9	_		
Power Gain at 2.9 GHz	G <sub>P3</sub>	_	11.4	_	]	
Power Gain at 3.3 GHz	G <sub>P4</sub>	_	11.5	_	dB	
Power Gain at 3.5 GHz	G <sub>P5</sub>	_	11.7	_	]	
Power Gain at 3.75 GHz	G <sub>P6</sub>	-	10.8	_		



## Electrical Characteristics (Frequency = 2.75 GHz to 3.75 GHz unless otherwise stated; $T_c = 25^{\circ}C$ )

Characteristics	Symbol	Тур.	Max.	Units	Conditions
RF Characteristics <sup>2</sup>					
Input Return Loss	S11	-6	-	- D	
Output Return Loss	S22	-6	_	dB	$P_{IN} = -10 \text{ dBm}$
Output Mismatch Stress	VSWR	-	5:1	Ψ	No damage at all phase angles

Notes:

<sup>1</sup> Scaled from PCM data

 $^2$  Unless otherwise noted: Pulse Width = 100  $\mu s,$  Duty Cycle = 10%

## **Thermal Characteristics**

Parameter	Symbol	Rating	Units	Conditions	
Operating Junction Temperature	TJ	177	°C	Pulse Width = 100 $\mu$ s, Duty Cycle =10%, P <sub>DISS</sub> = 418 W, T <sub>CASE</sub> = 85°C	
Thermal Resistance, Junction to Case	R <sub>ejc</sub>	0.22	°C/W		
Operating Junction Temperature	ΤJ	185	°C		
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	0.5	°C/W	CW, $P_{\text{DISS}} = 200 \text{ W}$ , $T_{\text{CASE}} = 85^{\circ}\text{C}$	



Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 500 mA, Pulse Width = 100 µs, Duty Cycle = 10%, P<sub>IN</sub> = 46 dBm, T<sub>BASE</sub> = +25°C



Figure 1. Output Power vs Frequency as a Function of Temperature



Figure 3. Drain Eff. vs Frequency as a Function of Temperature







Figure 2. Output Power vs Frequency as a Function of Input Power









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Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 500 mA, Pulse Width = 100 µs, Duty Cycle = 10%, P<sub>IN</sub> = 46 dBm, T<sub>BASE</sub> = +25°C



Figure 7. Output Power vs Frequency as a Function of  $V_D$ 



Figure 9. Drain Eff. vs Frequency as a Function of  $V_D$ 



Figure 11. Drain Current vs Frequency as a Function of  $V_D$ 



Figure 8. Output Power vs Frequency as a Function of  $I_{DQ}$ 



**Figure 10.** Drain Eff. vs Frequency as a Function of  $I_{DO}$ 





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Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 500 mA, Pulse Width = 100 µs, Duty Cycle = 10%, P<sub>IN</sub> = 46 dBm, T<sub>BASE</sub> = +25°C



Figure 13. Output Power vs Input Power as a Function of Frequency



Figure 15. Large Signal Gain vs Input Power as a Function of Frequency





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**Figure 14.** Drain Eff. vs Input Power as a Function of Frequency



Figure 16. Drain Current vs Input Power as a Function of Frequency



Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 500 mA, Pulse Width = 100 µs, Duty Cycle = 10%, P<sub>IN</sub> = 46 dBm, T<sub>BASE</sub> = +25°C



Figure 18. Output Power vs Input Power as a Function of Temperature



Figure 20. Large Signal Gain vs Input Power as a Function of Temperature



Figure 22. Gate Current vs Input Power as a Function of Temperature

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**Figure 19.** Drain Eff. vs Input Power as a Function of Temperature



Figure 21. Drain Current vs Input Power as a Function of Temperature



Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 500 mA, Pulse Width = 100 µs, Duty Cycle = 10%, P<sub>IN</sub> = 46 dBm, T<sub>BASE</sub> = +25°C



Figure 23. Output Power vs Input Power as a Function of  $I_{\text{DQ}}$ 



Figure 25. Large Signal Gain vs Input Power as a Function of  $I_{\mbox{\scriptsize DQ}}$ 



Figure 27. Gate Current vs Input Power as a Function of  $I_{\text{DQ}}$ 

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Figure 24. Drain Eff. vs Input Power as a Function of  $I_{DQ}$ 



**Figure 26.** Drain Current vs Input Power as a Function of  $I_{DO}$ 



Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ , CW,  $P_{IN} = 43 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 28. Output Power vs Frequency as a Function of Temperature



Figure 30. Drain Eff. vs Frequency as a Function of Temperature







Figure 29. Output Power vs Frequency as a Function of Input Power



Figure 31. Drain Eff. vs Frequency as a Function of Input Power





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Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ , CW,  $P_{IN} = 43 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 34. Output Power vs Frequency as a Function of Voltage



Figure 36. Drain Eff. vs Frequency as a Function of Voltage







**Figure 35.** Output Power vs Frequency as a Function of  $I_{DQ}$ 



**Figure 37.** Drain Eff. vs Frequency as a Function of  $I_{DQ}$ 





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Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ , CW,  $P_{IN} = 43 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 40. Output Power vs Input Power as a Function of Frequency



Figure 42. Large Signal Gain vs Input Power as a Function of Frequency





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Figure 41. Drain Eff. vs Input Power as a Function of Frequency



Figure 43. Drain Current vs Input Power as a Function of Frequency



Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ , CW,  $P_{IN} = 43 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 45. Output Power vs Input Power as a Function of Temperature



Figure 47. Large Signal Gain vs Input Power as a Function of Temperature





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Figure 46. Drain Eff. vs Input Power as a Function of Frequency



Figure 48. Drain Current vs Input Power as a Function of Temperature



Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ , CW,  $P_{IN} = 43 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 50. Output Power vs Input Power as a Function of  $I_{DQ}$ 



Figure 52. Large Signal Gain vs Input Power as a Function of  $I_{\mbox{\scriptsize DQ}}$ 



Figure 54. Gate Current vs Input Power as a Function of  $I_{DQ}$ 

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Figure 51. Drain Eff. vs Input Power as a Function of  $I_{DQ}$ 



**Figure 53.** Drain Current vs Input Power as a Function of  $I_{DO}$ 



Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 500 mA, Pulse Width = 100 µs, Duty Cycle = 10%, P<sub>IN</sub> = 46 dBm, T<sub>BASE</sub> = +25°C



Figure 55. 2nd Harmonic vs Frequency as a Function of Temperature



Figure 57. 2nd Harmonic vs Output Power as a Function of Frequency







Figure 56. 3rd Harmonic vs Frequency as a Function of Temperature



Figure 58. 3rd Harmonic vs Output Power as a Function of Frequency





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Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ ,  $P_{IN} = -10 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 61. Gain vs Frequency as a Function of Temperature



Figure 63. Input RL vs Frequency as a Function of Temperature







Figure 62. Gain vs Frequency as a Function of Temperature



Figure 64. Input RL vs Frequency as a Function of Temperature





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Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ ,  $P_{IN} = -10 \text{ dBm}$ ,  $T_{BASE} = +25^{\circ}\text{C}$ 



Figure 67. Gain vs Frequency as a Function of Voltage



Figure 69. Input RL vs Frequency as a Function of Voltage



Figure 71. Output RL vs Frequency as a Function of Voltage



Figure 68. Gain vs Frequency as a Function of  $I_{DQ}$ 



**Figure 70.** Input RL vs Frequency as a Function of  $I_{DQ}$ 



Figure 72. Output RL vs Frequency as a Function of  $I_{DQ}$ 

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## CGHV38375F-AMP Evaluation Board Schematic



## CGHV38375F-AMP Evaluation Board Outline



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## CGHV38375F-AMP Evaluation Board Bill of Materials

Designator	Description	Qty
R1	RES, 511 OHM, +/- 1%, 1/16W,0603	1
R2, R4	RES, 5.1,OHM, +/- 1%, 1/16W,0603	2
R3	RES, 4.7 OHM, 1%, 1/4W, 1206	1
C1	CAP, 6.8pF, +/- 0.25pF, 250V, 0603	1
C2,C7,C8	CAP, 10pF, +/- 1%, 250V, 0805	3
С3	CAP, 10.0pF, +/-5%,250V, 0603,	1
C4,C9	CAP, 470pF, 5%, 100V, 0603, X	2
C5	CAP, 33000pF, 0805, 100V, X7R	1
C6	CAP, 10μF, 16V, TANTALUM	1
C10	CAP, 1.0μF, 100V, 10%, X7R, 1210	1
C11	CAP, 33µF, 20%, G CASE	1
C12	CAP, 3300μF, +/-20%, 100V, ELECTROLYTIC	1
C13	CAP, 1µF, 0805, 100V, X7S	1
J1,J2	CONN, SMA, PANEL MOUNT JACK, FL	2
J3	HEADER RT>PLZ .1CEN LK 9POS	1
J4	CONNECTOR ; SMB, Straight, JACK, SMD	1
W1	CABLE, 18 AWG, 4.2	1
	PCB, RF35-TC, 2.5 X 4.0 X 0.030	1
	BASEPLATE, AL, 4.0 X 2.5 X 0.5	1
	2-56 SOC HD SCREW 1/4 SS	4
	#2 SPLIT LOCKWASHER SS	4
Q1	Transistor CGHV38375F	1

## **Electrostatic Discharge (ESD) Classifications**

Parameter	Symbol	Class	<b>Classification Level</b>	Test Methodology
Human Body Model	НВМ	1B	ANSI/ESDA/JEDEC JS-001 Table 3	JEDEC JESD22 A114-D
Charge Device Model	CDM	С3	ANSI/ESDA/JEDEC JS-002 Table 3	JEDEC JESD22 C101-C

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## CGHV38375F



## Product Dimensions CGHV38375F (Package 440226)

NOTES: (UNLESS OTHERWISE SPECIFIED)

- 1. INTERPRET DRAWING IN ACCORDANCE WITH ANSI Y14.5M-2009
- 2. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF .020 BEYOND EDGE OF LID
- 3. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF .008 IN ANY DIRECTION
- 4. ALL PLATED SURFACES ARE GOLD DVER NICKEL





PIN	DESC.
1	RF <sub>IN</sub>
2	RFout
3	SOURCE/FLANGE

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## Part Number System



## Table 1.

Parameter	Value	Units
Lower Frequency	2.75	GHz
Upper Frequency <sup>1</sup>	3.75	GHZ
Power Output	375	W
Package	Flange	_

Note:

<sup>1</sup> Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

## Table 2.

Character Code	Code Value
A	0
В	1
C	2
D	3
E	4
F	5
G	6
Н	7
J	8
К	9
Examples	1A = 10.0 GHz 2H = 27.0 GHz

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## **Product Ordering Information**

Order Number	Description	Unit of Measure	Image
CGHV38375F	GaN HEMT	Each	and the second sec
CGHV38375F-AMP	Test board with GaN HEMT installed	Each	





#### For more information, please contact:

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