# **BLP0408H9S30**

Power LDMOS transistor

Rev. 3 — 12 January 2023

# 1. Product profile

## 1.1 General description

A 30 W LDMOS driver transistor for broadcast, class-AB transmitter and industrial applications. The excellent ruggedness of this device makes it ideal for digital and analog transmitter applications in the frequency range from 400 MHz to 860 MHz.

#### Table 1. Typical performance

RF performance at  $I_{Dq} = 60$  mA in an application circuit.

Test signal	f	V <sub>DS</sub>	PL	G <sub>p</sub>	η <sub>D</sub>
	(MHz)	(V)	(W)	(dB)	(%)
pulsed CW [1]	714	50	30	20.2	62
DVB-T (8k OFDM)	714	50	6	20.0	32

[1] Measured at  $\delta$  = 20 %;  $t_p$  = 100  $\mu s.$ 

## 1.2 Features and benefits

- Designed for broadband operation
- High efficiency
- Integrated dual sided ESD protection
- Excellent ruggedness
- High power gain
- Excellent reliability
- Easy power control
- Excellent stability
- For RoHS compliance see the product details on the Ampleon website

## **1.3 Applications**

- Broadcast transmitter applications in the UHF band
- Digital and analog broadcasting
- Industrial, scientific and medical applications
- Applicable at frequencies from 400 MHz to 860 MHz

# 2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	drain		
2	gate		1 لــــا
3	source		

[1] Connected to flange.

# 3. Ordering information

#### Table 3. Ordering information

Package name	Orderable part number	12NC	Packing description	Min. orderable quantity (pieces)
TO-270-2F-1	BLP0408H9S30Z	9349 602 54515	TR13; 500-fold; 24 mm; dry pack	500
	BLP0408H9S30XY	9349 602 54538	TR7; 100-fold; 24 mm; dry pack	100

## 4. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage		-	108	V
V <sub>GS</sub>	gate-source voltage		-6	+11	V
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature	[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

# 5. Thermal characteristics

#### Table 5.Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-c)</sub>	thermal resistance from junction to case	T <sub>case</sub> = 80 °C	1.7	K/W

# 6. Characteristics

#### Table 6. DC characteristics

 $T_i = 25 \ ^{\circ}C$ ; per section unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; \text{ I}_{D} = 0.18 \text{ mA}$	108	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 18 mA	1.5	1.9	2.5	V
V <sub>GSq</sub>	gate-source quiescent voltage	$V_{DS} = 50 \text{ V}; \text{ I}_{D} = 60 \text{ mA}$	1.6	2.0	2.6	V
I <sub>DSS</sub>	drain leakage current	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 50 V	-	-	1.4	μΑ
I <sub>DSX</sub>	drain cut-off current	$\label{eq:VGS} \begin{array}{l} V_{\mathrm{GS}} = V_{\mathrm{GS(th)}} + 3.75 \; V; \\ V_{\mathrm{DS}} = 10 \; V \end{array}$	-	3.1	-	A
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 11 V; V <sub>DS</sub> = 0 V	-	-	140	nA
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ I <sub>D</sub> = 0.63 A	-	1.2	-	Ω

#### Table 7.AC characteristics

 $T_i = 25 \ ^{\circ}C$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	input capacitance	$V_{GS} = 0 V; V_{DS} = 50 V; f = 1 MHz$	-	112	-	pF
C <sub>oss</sub>	output capacitance	$V_{GS} = 0 V; V_{DS} = 50 V; f = 1 MHz$	-	9.1	-	pF
C <sub>rss</sub>	reverse transfer capacitance	$V_{GS} = 0 V; V_{DS} = 50 V; f = 1 MHz$	-	0.24	-	pF

#### Table 8. RF characteristics

Test signal: pulsed CW;  $t_p = 100 \ \mu s$ ;  $\delta = 20 \ \%$ ;  $f = 714 \ MHz$ ; RF performance at  $V_{DS} = 50 \ V$ ;  $I_{Dq} = 60 \ mA$ ;  $T_{case} = 25 \ ^{\circ}C$ ; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G <sub>p</sub>	power gain	P <sub>L</sub> = 30 W	18.5	20.2	-	dB
RL <sub>in</sub>	input return loss	P <sub>L</sub> = 30 W	-	-12	-9	dB
η <sub>D</sub>	drain efficiency	P <sub>L</sub> = 30 W	59	62	-	%

#### **Power LDMOS transistor**



# 7. Test information

## 7.1 Ruggedness in class-AB operation

The BLP0408H9S30 is capable of withstanding a load mismatch corresponding to VSWR = 30 : 1 through all phases under the following conditions:  $V_{DS} = 50$  V;  $I_{Dq} = 60$  mA;  $P_L = 30$  W; f = 714 MHz; pulsed CW ( $t_p = 100 \ \mu$ s;  $\delta = 20 \ \%$ ).

## 7.2 Test circuit



### Table 9. List of components

For test circuit see Figure 2.

Component	Description	Value		Remarks
C1, C3, C5, C6	multilayer ceramic chip capacitor	100 pF	[1]	
C2, C7	multilayer ceramic chip capacitor	4.7 μF, 100 V		
C4	multilayer ceramic chip capacitor	3.6 pF	[1]	
C8	electrolytic capacitor	470 μF, 64 V		
L1	inductor	9 nH		Coilcraft: 1508-9N0GLB
R1	chip resistor	4.7 kΩ		SMD 1206
R2	chip resistor	10 Ω		SMD 1206
R3	shunt resistor	10 mΩ		current monitoring

[1] American Technical Ceramics type 800A or capacitor of same quality.

## 7.3 Graphical data



### 7.3.1 Pulsed CW performance measured in production RF test circuit

# AMPLEON

# **BLP0408H9S30**

#### **Power LDMOS transistor**



Product data sheet

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### 7.3.2 DVB-T performance measured in production RF test circuit



PAR (of output signal) at 0.01 % probability on CCDF; PAR of input signal = 9.5 dB at 0.01 % probability on CCDF.

# 8. Package outline



Fig 11. Package outline TO-270-2F-1 (sheet 1 of 2)

# BLP0408H9S30 Power LDMOS transistor

#### TO-270-2F-1

	teme Description					
Items			Description			
(1)		•	ision. The mold protrusion is maximum 0.7			
			0.55 mm. max. in length and 0.3 mm. ma			
(2)	The lead dambar (n	netal) protrusions a	are not included. Add 0.14 mm max to the	total lead dimension at the dambar location.		
(3)	The leads and expo	osed heatsink are p	plated with matte Tin (Sn).			
(4)	Dimensions (Heatsi	imensions (Heatsink ears) 10,67 and 1,78 do not include mouldprotrusion. Overall Max. dimensions incl. mould				
( )	protrusions is 10.92	orotrusions is 10.92 mm. (max.) and 2.03 mm. (max.).				
(5)	Lead coplanarity over the leads is 0,1 mm. maximum.					
(6)	Surfaces may rema	Surfaces may remain unplated (not solderable surfaces).				
ALC	X	6				
	B	Lead Dar	(0.3 max.) (0.5 max.) mbar protrusion (#2)	DETAIL B SCALE 50 : 1		
Package o	utline drawing:	Lead Dar	nbar protrusion (#2)	SCALE 50 : 1		

#### Fig 12. Package outline TO-270-2F-1 (sheet 2 of 2)

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# 9. Handling information

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

#### Table 10.ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2B 🗓
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

[1] CDM classification C2B is granted to any part that passes after exposure to an ESD pulse of 750 V.

[2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

## **10. Abbreviations**

Table 11. Abbre	Fable 11.         Abbreviations				
Acronym	Description				
CCDF	Complementary Cumulative Distribution Function				
CW	Continuous Wave				
DVB-T	Digital Video Broadcast - Terrestrial				
ESD	ElectroStatic Discharge				
LDMOS	Laterally Diffused Metal-Oxide Semiconductor				
MTF	Median Time to Failure				
OFDM	Orthogonal Frequency Division Multiplexing				
PAR	Peak-to-Average Ratio				
RoHS	Restriction of Hazardous Substances				
SMD	Surface Mounted Device				
UHF	Ultra High Frequency				
VSWR	Voltage Standing Wave Ratio				

# 11. Revision history

#### Table 12.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BLP0408H9S30 v.3	20230112	Product data sheet		BLP0408H9S30 v.2	
Modifications:	<u>Table 3 on page 2</u> : package name changed from SOT1482-1 to TO-270-2F-1				
	• <u>Table 5 on page 2</u> : value changed from 2.30 K/W to 1.7 K/W				
	• Section 8 on page 9: package outline drawing changed from SOT1482-1 to TO-270-2F-1				
	<u>Section 12 on page 12</u> : updated section				
BLP0408H9S30 v.2	20210716	Product data sheet		BLP0408H9S30 v.1	
BLP0408H9S30 v.1	20191205	Product data sheet		-	

# 12. Legal information

## 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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