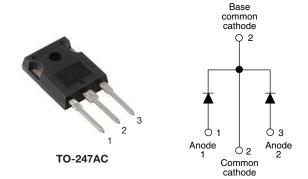


Vishay Semiconductors

Ultrafast Rectifier, 2 x 15 A FRED Pt®



PRODUCT SUMMARY							
Package	TO-247AC						
I _{F(AV)}	2 x 15 A						
V_{R}	200 V						
V _F at I _F	0.85 V						
t _{rr} typ.	See Recovery table						
T _J max.	175 °C						
Diode variation	Common cathode						

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS
COMPLIANT
HALOGEN
FREE
Available

DESCRIPTION / APPLICATIONS

VS-MUR3020WT... is the state of the art ultrafast recovery rectifier specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER Peak repetitive reverse voltage		SYMBOL	TEST CONDITIONS	MAX.	UNITS				
		V_{RRM}		200	V				
Average restified forward current				15					
Average rectified forward current total	total device	I _{F(AV)}	Rated V _R , T _C = 150 °C	30	۸				
Non-repetitive peak surge current per leg		I _{FSM}		200	А				
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 150 °C	30					
Operating junction and storage temperatures		T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	200	-	-					
Forward voltage	V_{F}	I _F = 15 A	-	-	1.05	V				
		I _F = 15 A, T _J = 150 °C	-	-	0.85					
Devene leekene euwent	I _R	$V_R = V_R$ rated	-	-	10					
Reverse leakage current		T _J = 150 °C, V _R = V _R rated	-	-	500	μA				
Junction capacitance	C _T	V _R = 200 V	-	55	-	pF				
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	12	-	nH				



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A, } dI_F/dt =$	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	35			
Reverse recovery time	t _{rr}	T _J = 25 °C		-	22	-	ns		
		T _J = 125 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	39	-			
Peak recovery current	I _{RRM}	T _J = 25 °C		-	1.6	-	A		
		T _J = 125 °C		-	4.1	-			
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	19	-	nC		
		T _J = 125 °C		-	90	-			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C			
Thermal resistance, junction to case per leg	R_{thJC}		-	-	1.5				
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	40	°C/W			
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-				
Majaht			-	6.0	-	g			
Weight			-	0.21	-	OZ.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-247AC	MUR3020WT						



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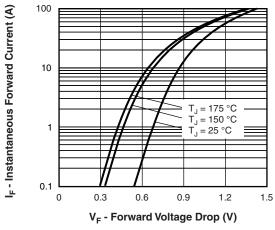


Fig. 1 - Typical Forward Voltage Drop Characteristics

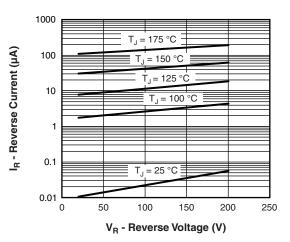


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

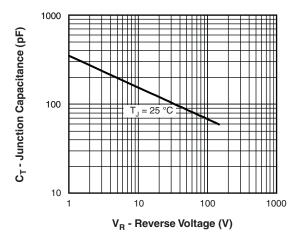


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

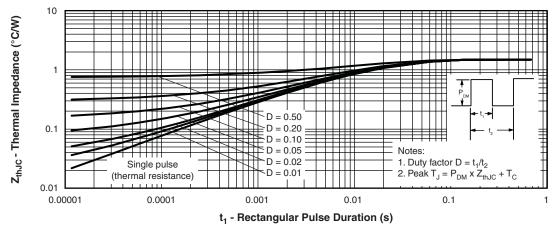


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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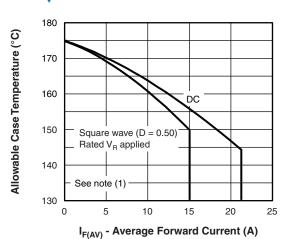


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

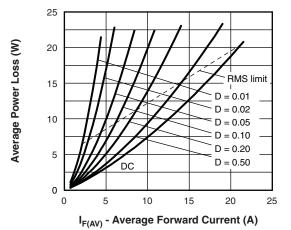


Fig. 6 - Forward Power Loss Characteristics

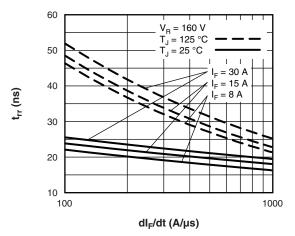


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

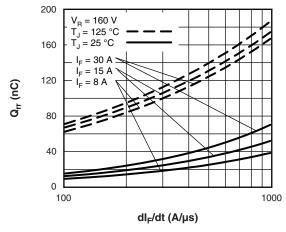


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at } V_{R1} = \text{Rated } V_R \\ \end{array}$

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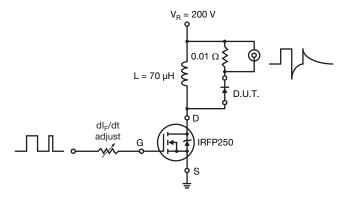
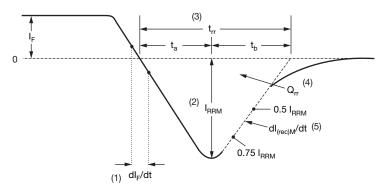


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

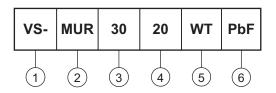
(5) dI_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Ultrafast MUR series (TO-247AC)

3 - Current rating (30 = 30 A)

4 - Voltage rating (20 = 200 V)

5 - WT = center tap (dual) TO-247

6 - Environmental digit:

PbF = lead (Pb)-free and RoHS-compliant

-N3 = halogen-free, RoHS-compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-MUR3020WTPbF	25	500	Antistatic plastic tube						
VS-MUR3020WT-N3	25	500	Antistatic plastic tube						

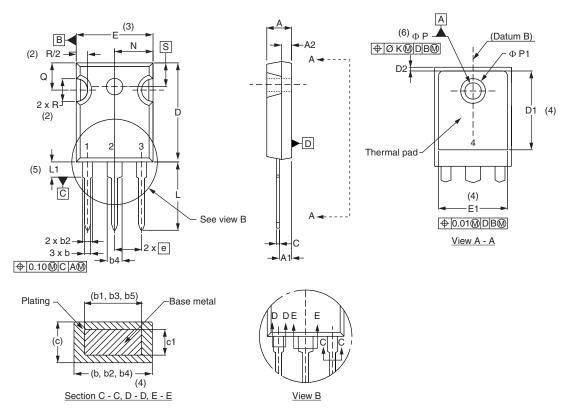
LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95542						
Part marking information TO-247ACI	PbF www.vishay.com/doc?95226						
TO-247AC	N3 www.vishay.com/doc?95007						



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TO-247 - 50 mils L/F

DIMENSIONS in millimeters and inches



CVMPOL	MILLIMETERS INCHES NOTES	CVMPOL	MILLIMETERS		INCHES		NOTES				
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209		D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102		E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054		E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055		е	5.46 BSC		BSC 0.215 BSC		
b1	0.99	1.35	0.039	0.053		ØK	0.254		0.010		
b2	1.65	2.39	0.065	0.094		L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092		L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135		N	7.62 BSC		62 BSC 0.3		
b5	2.59	3.38	0.102	0.133		ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035		Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4	S	5.51 BSC		0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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