

PRODUCT / PROCESS CHANGE NOTIFICATION PCN-000458 Date: 260CT2017 P1/1

Semtech Corporation, 200 Flynn Road, Camarillo CA 93012					
	Semtech Canada Corporation, 4281 Harvester Road, Burlington, Ontario L7L 5M4 Canada				
	Semtech Irvine, 5141 California Ave., Suite 100, Irvine CA 92617				
		CH-2000 Neuchatel Switzerla			
		ourt, Great Park Road, Bristol			
		rros Edificio 7, Reynosa, Tam	aulipas, Mexico 88780		
Semtech Plano, 1101 Re	esource Drive, Suite 121, Plar	no TX 75074			
	Change	Details			
Part Number(s) Affected: Customer Part Number(s) Affected: N/A EClamp2357NQ Customer Part Number(s) Affected: N/A					
Description, Purpose and Effect of Change: Description: This is an Ordering Part Number change in the data sheet. Added: EClamp2357NQTLT; available in 12 mm wide carrier tape. Removed: EClamp2357NQTCT Purpose:					
The purpose of the change is improved tape and reel quality and compliance with EIA-481 – Tape and Reel Spec. This spec calls for EClamp2357NQ to be taped into a 12 mm carrier tape.					
Change Classification	🖾 Major 🗌 Minor	Impact to Form, Fit, Function	🗌 Yes 🛛 No		
Impact to Data Sheet	🖂 Yes 🛛 No	New Revision or Date	16OCT2017 🗌 N/A		
Impact to Performance, Characteristics or Reliability: There is no impact to Form, Fit, Function or Reliability performance of the part. This is a datasheet change to identify a new ordering Part Number.					
Implementation Date	27OCT2017	Work Week	ТВА		
Last Time Ship (LTS) Of unchanged product	No longer available	Affecting Lot No. / Serial No. (SN)	N/A		
Sample Availability	27OCT2017	Qualification Report Availability	27OCT2017		
Supporting Documents for Change Validation/Attachments: • New Datasheet for EClamp2357NQ					
Issuing Authority					
Semtech Business Unit					
Semtech Contact Info:	Les Fang Yuen Semtech Corporation				
FOR FURTHER INFORMATION &	WORLDWIDE SALES COVERAGE:	http://www.semtech.com/contact/ii	ndex.html#support		





EMIClamp® EMI Filter and ESD Protection For SD Card Interfaces

PROTECTION PRODUCTS

Description

The EClamp[°]2357NQ is a low pass filter array with integrated TVS diodes. It is designed to suppress unwanted EMI/RFI signals and provide electrostatic discharge (ESD) protection in portable electronic equipment. They have been optimized for protection of touch screen displays, secure digital (SD) card interfaces, and color LCD panels in cellular phones and other portable electronics.

The device consists of six π filter circuits comprised of TVS diodes for ESD protection, and a resistor capacitor network for EMI/RFI filtering. A series resistor value of 100Ω and a component capacitance value of 10pF are used to achieve 20dB minimum attenuation from 1.0GHz to 3GHz. The device also includes 4 discrete TVS diodes for dedicated ESD protection. All of the TVS diodes provide effective suppression of ESD voltages in excess of ± 20 kV (air discharge) and ± 12 kV (contact discharge) per IEC 61000-4-2, level 4.

The EClamp2357NQ is gualified to AEC-Q100 Grade 1 for automotive use.

Features

- EMI/RFI filter with integrated ESD protection •
- ESD protection to IEC 61000-4-2 (ESD) Level 4, ±20kV (air), ±12kV (contact)
- Filter performance: >30dB attenuation at 1.8GHz
- TVS working voltage: 2.5V
- Resistor: 100Ω +/- 15% •
- Capacitance: 10pF typ. ($V_{R} = 0V$ for non filter pins) •
- EMI & ESD protection for six lines •
- Dedicated ESD protection for four lines •
- Solid-state technology
- AEC-Q100 Grade 1 Qualified

Mechanical Characteristics

- SLP3030P16 package
- Pb-Free, Halogen Free, RoHS/WEEE compliant •
- Nominal Dimensions: 3.0 x 3.0 x 0.6 mm
- Lead Pitch: 0.5mm •
- Lead Finish: Matte Tin
- Marking: Marking Code
- Packaging: Tape and Reel per EIA 481

Applications

- **Touch Screen Display Interfaces**
- Secure Digital (SD) Memory Card Interfaces •
- Multimedia Card Interfaces (MCI) .
- **Color LCD Panel Protection**
- **Cell Phone Handsets and Accessories**
- **Automotive Applications**

Schematic & Pin Configuration



EClamp2357NQ **Final Datasheet** Sep 20, 2017

Rev 2.0

Package Dimensions

Absolute Maximum Ratings

Rating	Symbol	Value	Units
ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V	±12	
ESD per IEC 61000-4-2 (Air) ⁽¹⁾	V _{ESD}	±20	kV
Junction Temperature	T,	125	°C
Operating Temperature	T _{op}	-40 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}	-40°C to 125°C, any I/O to GND				2.5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA, any I/O to GND	-40°C to 125°C	6	8	10	V
Reverse Leakage Current	I _R	$V_{RWM} = 2.5V$, any I/O to GND	-40°C to 125°C			0.5	μA
Total Series Resistance	R	Each line, T = -40 $^{\circ}$ C to 125 $^{\circ}$ C		85	100	115	Ω
Total Conscitones	C	Innutto Cod coch Ling f 1 Mile	$V_{R} = 0V$		20	22	۳Ľ
Total Capacitance		Input to Gnd, each Line, $f = 1 \text{ MHz}$	$V_{R} = 2.5V$		12	15	pF

Notes: (1): ESD Gun return path to Ground Reference Plane (GRP)

Typical Characteristics

Typical Insertion Loss -S21 (Each Filter)











Analog Crosstalk (Each Line)



ESD Clamping (-8kV Contact per IEC 61000-4-2)







Typical Characteristics (Continued)

Breakdown Voltage vs. Temperature



TLP Characteristics (Positive)



Leakage Current vs. Temperature



Capacitance vs. Reverse Voltage



TLP Characteristics (Negative)



Application Information

Device Connection

The EClamp2357NQ is comprised of six circuits each consisting of a low pass filter for EMI/RFI suppression and dual TVS diodes for ESD protection. It also includes 4 lines of TVS diodes for ESD protection of power lines or high speed I/O lines. The device is housed in a 16-pin Quad Flat No-Lead (QFN) package. Electrical connection is made via 16 pins located at the bottom of the device. A center tab serves as the ground connection. Pin connections are noted in the table to the right. The device is designed for easy PCB routing as shown in the application examples. All path lengths should be kept as short as possible to minimize the effects of parasitic inductance in the board traces.

Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

Pin Identification and Configuration (Top Side View)



Pin	Identification
3, 4, 5, 7, 9, 10	Input EMI/ESD Protected Lines
1, 2, 11, 12, 14, 16	Output EMI/ESD Protected Lines
6, 8, 13, 15	Input/Output ESD Protected Lines
Center Tab	Ground

Pin Configuration and Schematic



Application Information



EClamp2357NQ SD Memory





Outline Drawing - SLP3030P16



Land Pattern - SLP3030P16



Marking Code



Notes:

1. YYWW = Alphanumeric character Date Code

2. Pin 1 indicated by "Mouse Bite" on the ground pad

Tape and Reel Specification - Plastic Tape



Ordering Information

Part Number	Qty per Reel	Reel Size
EClamp2357NQTLT	3000	13 Inch
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Contact Information

Semtech Corporation 200 Flynn Road, Camarillo, CA 93012 Phone: (805) 498-2111, Fax: (805) 498-3804 www.semtech.com