# High Voltage / High Speed Opto-Isolator

### **OPI1268S**

### Features:

- 20 kV dc Isolation
- 2 Mbit/s transfer rate
- $t_{PHL}$ - $t_{PLH} \le 50$  ns typical
- Creepage path: 24 mm
- TTL Compatible
- 6 Axis / 10 G<sub>RMS</sub> load rating

### **Certifications:**

- UL File E58730
- ATEX Certification Exia IIc Ga EN 60079-0:2012/A11:2013 EN 60079-11:2012 (IEC 60079-11:2011 Edition 6)

[27.94] 1.100

IP65 Rated



6.35

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**Electronics** 

### **Description:**

The **OPI1268S** is a high voltage isolator with a digital output that is capable of high speed data transmission. The input of the OPI1268 consists of a high-efficiency GaAlAs LED with a peak wavelength of 850 nm, which is optically coupled to the output optical IC. A photologic device in the output IC detects the incoming modulated light and converts it to a proportionate current. This current is fed into a high-gain linear amplifier which is temperature, current and voltage compensated. The result is a highly stable digital output with an open collector inverter configuration. This device produces DC and AC voltage isolation between the input and output circuitry while providing TTL signal integrity.

#### **Applications:**

- Transportation Systems
- PC Board Power Systems
- Hybrid Vehicle Systems
- Medical Systems
- Control Systems

-SYMBOLIZATION PER COVERSHEET .025 [0.64] X 45 [0.51±0.10] .020±.004 [8.89] .350 [1.01±0.13] .040±.005 [24.89] [3.61] .980 .142 NOM [0.76] 5X .030 [0.51±0.13] [3.81] .020±.005 .150 4 5 [2.54] .100 [2.54] [1.91±0.13] .100 .075±.005 PINS [7.62] 1 CATHODE 2 ANODE 3 VCC 4 OUTPUT .300 5 GROUND

NOTE:

- DIMENSIONS ARE ± .010 [.25] UNLESS OTHERWISE NOTED. 1.
- DIMENSIONS ARE IN INCHES [MM]. 2.

	Ordering Information										
Part Number	LED Peak Wavelength	Sensor Photologic <sup>®</sup>	Isolation Voltage (kV)DC	<b>t<sub>PLH</sub> / t<sub>PHL</sub></b> Max (ns)	<b>I<sub>F</sub> (mA)</b> Typ / Max	V <sub>ce</sub> (V) Max	Lead Length (mm)	Lead Spac- ing (mm)			
OPI1268S	850 nm	Open Collector	20	100	10 / 50	18	3.6	2.0			

General Note

**Pb-Free** (RoHS)

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### **Electrical Specifications**

#### Absolute Maximum Ratings (T<sub>A</sub> = 25° C unless otherwise noted)

Storage Temperature	-50° C to +100° C	
Operating Temperature	-50° C to +100° C	
Input-to-Output Isolation Voltage <sup>(2)</sup>	20 kVDC	
Lead Soldering Temperature (1/16" (1.6 mm) from case for 5 seconds with soldering iron) <sup>(3)</sup>	260° C	
Input Diode		
Continuous Forward Current	30 mA	
Peak Forward current (1 μs pulse width, 300 pps)	3.0 A	
Reverse Voltage	3.0 V	
Power Dissipation <sup>(1)</sup>	100 mW	
Output IC		
Maximum Supply Voltage	7 V	
Power Dissipation <sup>(4)</sup>	100 mW	
Maximum Output Voltage	18 V	
Maximum Output Current	25 mA	

#### Electrical Characteristics (T<sub>A</sub> = 0° C to 70° C unless otherwise noted)

SYMBOL	PARAMETER	MIN	ТҮР	MAX	UNITS	TEST CONDITIONS			
Input Diode									
V <sub>F</sub>	Forward Voltage		1.4	1.8	V	I <sub>F</sub> = 20 mA			
I <sub>R</sub>	Reverse Current		0.1	100	μA	V <sub>R</sub> = 2.0 V			
Output IC (V <sub>CC</sub> = 4.5 V to 5.25 V) (See OPL550 for additional information—for reference only.)									
I <sub>ОН</sub>	High Level Output Current		0.20	25	μΑ	$I_F$ = 0.0 mA, $V_{OH}$ = 18.0 V, $V_{CC}$ = 5.25 V			
V <sub>OL</sub>	Low Level Output Voltage		0.35	0.55	V	$I_{\text{F}}$ = 10.0 mA, $I_{\text{OL}}$ = 8.0 mA, $V_{\text{CC}}$ = 4.5 V			
I <sub>CCH</sub>	High Level Supply Current	-	5.5	7		I <sub>F</sub> = 0, V <sub>CC</sub> = 5.25 V			
I <sub>CCL</sub>	Low Level Supply Current	-	7.5	10	mA	I <sub>F</sub> = 10.0 mA, V <sub>CC</sub> = 5.25 V			
Coupled Characteristics ( $V_{CC}$ = 5 V, $I_F$ =30 mA, $R_L$ =560 $\Omega$ )									
CIO	Coupling Capacitance		-	2	pF	Input and output leads shorted.			
$t_{\text{PLH}}$	Propagation Delay to Low Output Level	-	50	100		See Figure 1			
t <sub>PHL</sub>	Propagation Delay to High Output Level	-	50	100	ns				
I <sub>ISO</sub>	Isolation Leakage Current <sup>(5)</sup>		-	20	μA	V <sub>ISO</sub> = 19.2 kV dc			
I <sub>F</sub> +	LED Positive Going Threshold Current		1.7	5.0	mA	V <sub>CC</sub> = 5 V, I <sub>OL</sub> = 8.0 mA			
dv/dt	v/dt Voltage Spike Immunity		30	-	kV/μs				

Notes:

(1) Derate LED linearly 1.33 mW/° C above 25° C.

(2) UL recognition is for 16 kV dc for one minute.

(3) RMA flux is recommended.

(4) Derate linearly 1.33 mW/° C above 25° C.

(5) Measured with input leads shorted together and output leads shorted together in air with a maximum relative humidity of 50 %.

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### **Typical Performance Curves**

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### **CIRCUIT VALUES**

Condition #1:  $V_{cc}$  = 5.0V,  $I_F$  = 30mA,  $R_L$  = 560 Ohms







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