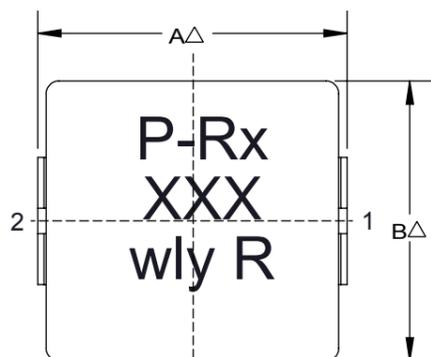
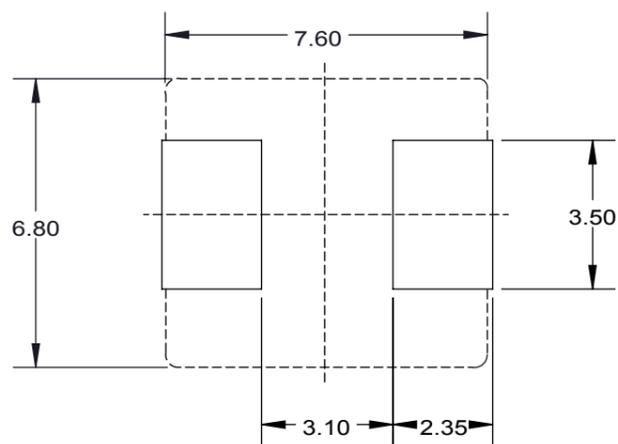


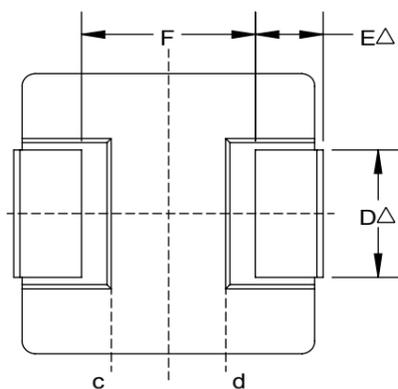
TABLE A						
Item	A	B	C	D	E	F
	Max	$\pm 0.20$	$\pm 0.20$	$\pm 0.30$	$\pm 0.30$	TYP
Dim	7.60	6.60	3.80	3.00	1.60	3.75

### RECOMMENDED PCB LAYOUT

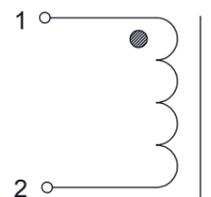


DCR test at point a

DCR test at point b

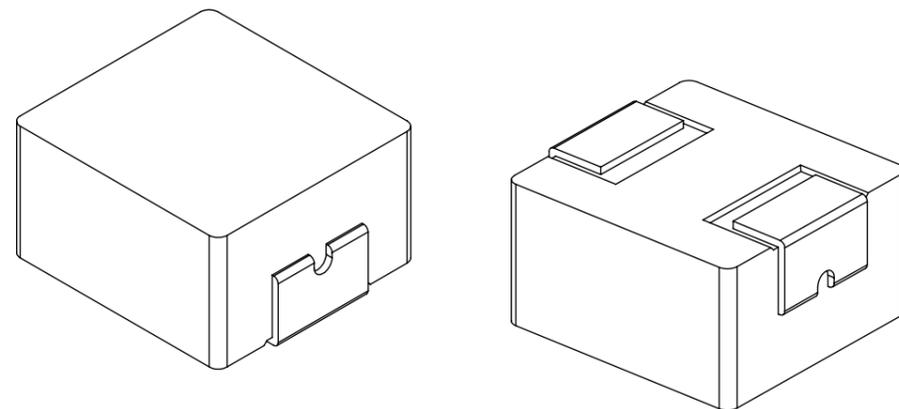


### SCHEMATIC



#### Notes:

- Parts shall be fully coated with gray epoxy, except the terminals.
- Minimum value is defined for dimension E. However, two terminals can not exceed lines c and d individually and must stay in the terminal slot of bottom core surface.
- All soldering surfaces to be coplanar within 0.10 millimeters.
- Tolerances are +/- 0.3 millimeters unless stated otherwise.
- Marking: Inkjet marking in black.  
 P stands for HCMP  
 (XXX) The 3 characters = Inductance in uH. R=decimal point.  
 If no R is present third character = # of zeros.  
 wly = (Date Code, w is bi-weekly letter code, l is manufacturing location code, y is the last number of year)  
 R= (Revision Level)
- Environmental compliance requirement (Refer Division files: F306B-003-03 & CBA309A-001):  
 (a) RoHS, (b) REACH, (c) PFOS & PFOA, (d) Halogen free, Sb2O3 and Red Phosphorus



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