



Parameter	Ratings	Units
Blocking Voltage	350	V_p
Load Current	120	mA_{rms} / mA_{DC}
On-Resistance (max)	35	Ω

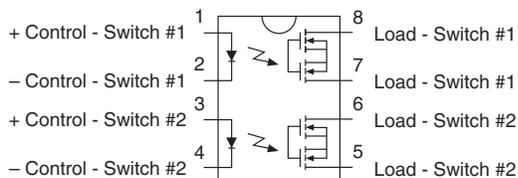
Features

- 3750V_{rms} Input/Output Isolation
- Current Limiting Device
- Low Drive Power Requirements
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Flammability Rating UL 94 V-0
- Surface Mount and Tape & Reel Versions Available
- Small 8-Pin Package

Applications

- Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Pin Configuration



Description

LAA110L is a dual normally open (1-Form-A) Solid State Relay that has two independently controlled, optically coupled MOSFET switches with an additional current limiting circuit. The MOSFET switches and photovoltaic die use optically coupled MOSFET technology to provide 3750V_{rms} of input/output isolation.

The optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

This dual switch OptoMOS relay provides a more compact design solution than discrete single-pole relays in a variety of applications, and saves board space by incorporating both switches in a single 8-Pin package.

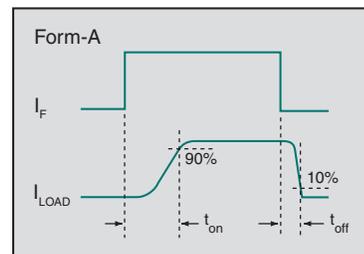
Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- EN/IEC 60950-1 Certified Component:
Certificate available on our website

Ordering Information

Part #	Description
LAA110L	8-Pin DIP (50/Tube)
LAA110LS	8-Pin Surface Mount (50/Tube)
LAA110LSTR	8-Pin Surface Mount (1,000/Reel)
LAA110PL	8-Pin Flat Pack (50/Tube)
LAA110PLTR	8-Pin Flat Pack (1,000/Reel)

Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	350	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ 25°C

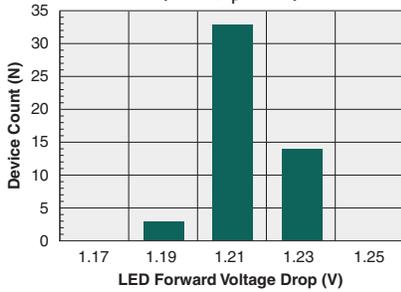
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current, Continuous ¹	-	I _L	-	-	120	mA _{rms} / mA _{DC}
Load Current Limiting	V _L =8V, I _F =5mA	I _{CL}	±130	±170	±210	mA
On-Resistance ²	I _L =120mA	R _{ON}	-	30	35	Ω
Off-State Leakage Current	V _L =350V _P	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	-	3	ms
Turn-Off		t _{off}	-	-	3	ms
Output Capacitance	I _F =0mA, V _L =50V, f=1MHz	C _{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =120mA	I _F	-	-	5	mA
Input Control Current to Deactivate	-	-	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	V _{IO} =0V, f=1MHz	C _{IO}	-	3	-	pF

¹ If both poles operate, then the load current must be derated so that it does not exceed the package power dissipation value.

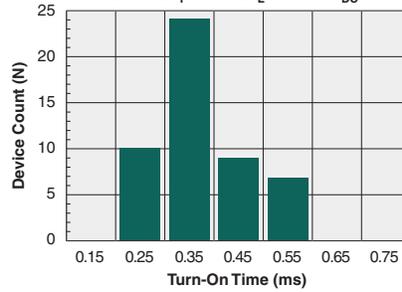
² Measurement taken within one second of on-time.

PERFORMANCE DATA*

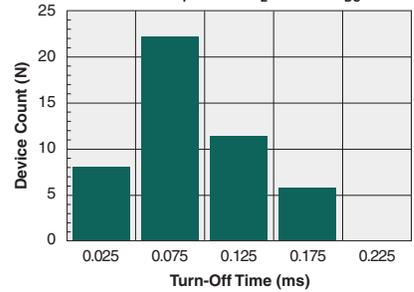
Typical LED Forward Voltage Drop
(N=50, $I_F=5mA$)



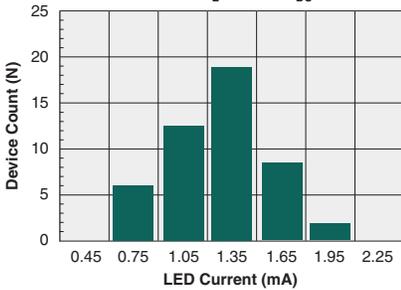
Typical Turn-On Time
(N=50, $I_F=5mA$, $I_L=120mA_{DC}$)



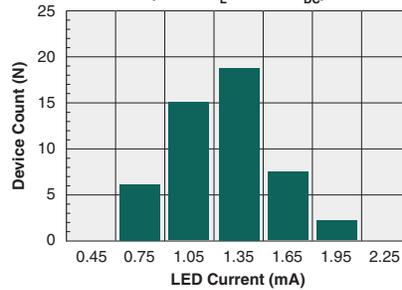
Typical Turn-Off Time
(N=50, $I_F=5mA$, $I_L=120mA_{DC}$)



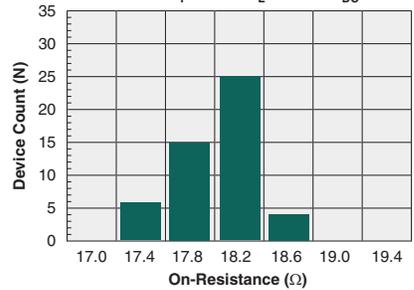
Typical I_F for Switch Operation
(N=50, $I_L=120mA_{DC}$)



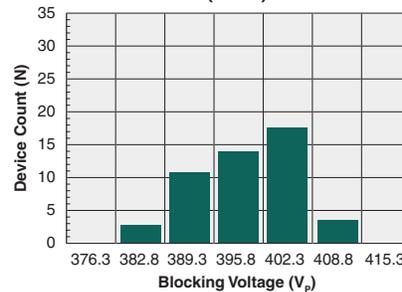
Typical I_F for Switch Dropout
(N=50, $I_L=120mA_{DC}$)



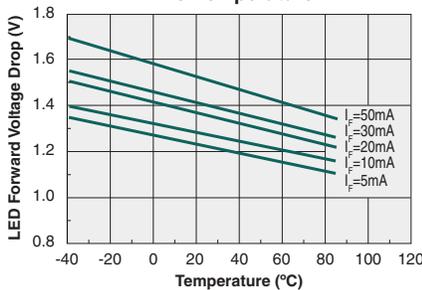
Typical On-Resistance Distribution
(N=50, $I_F=5mA$, $I_L=120mA_{DC}$)



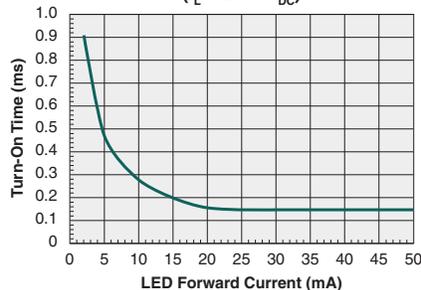
Typical Blocking Voltage Distribution
(N=50)



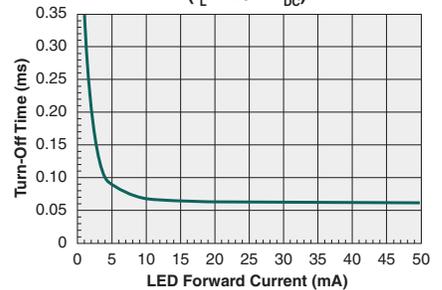
Typical LED Forward Voltage Drop vs. Temperature



Typical Turn-On Time vs. LED Forward Current
($I_L=120mA_{DC}$)



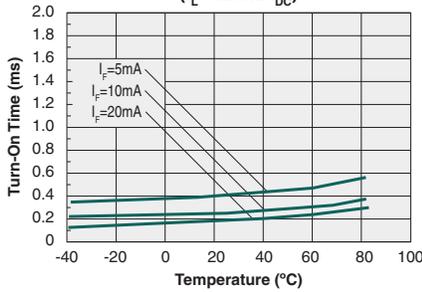
Typical Turn-Off Time vs. LED Forward Current
($I_L=120mA_{DC}$)



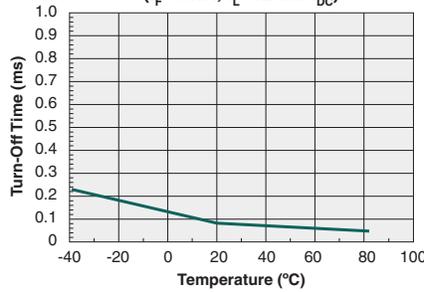
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*

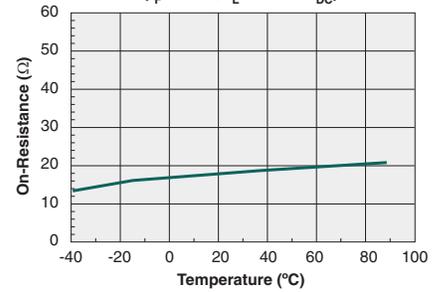
Typical Turn-On Time vs. Temperature
($I_L=120mA_{DC}$)



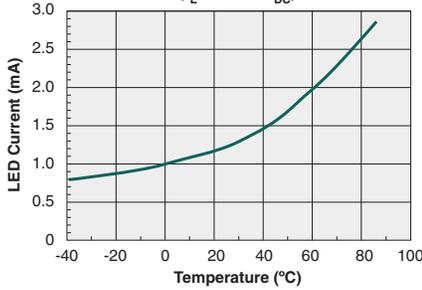
Typical Turn-Off Time vs. Temperature
($I_F=5mA, I_L=120mA_{DC}$)



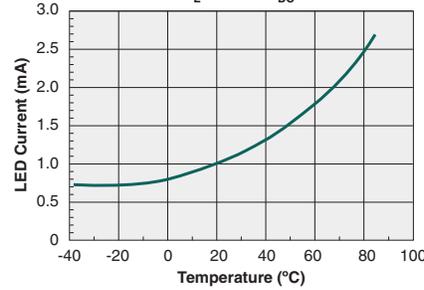
Typical On-Resistance vs. Temperature
($I_F=5mA, I_L=120mA_{DC}$)



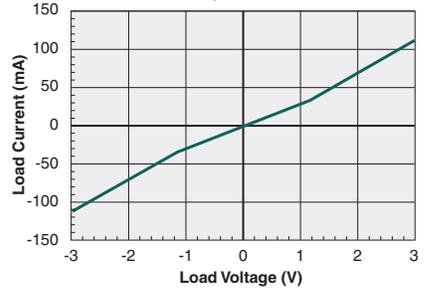
Typical I_F for Switch Operation vs. Temperature
($I_L=120mA_{DC}$)



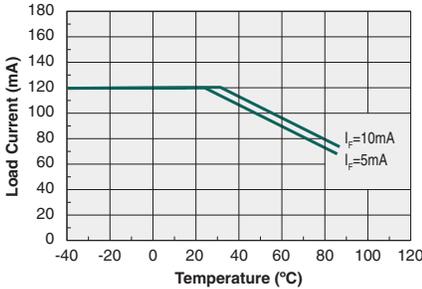
Typical I_F for Switch Dropout vs. Temperature
($I_L=120mA_{DC}$)



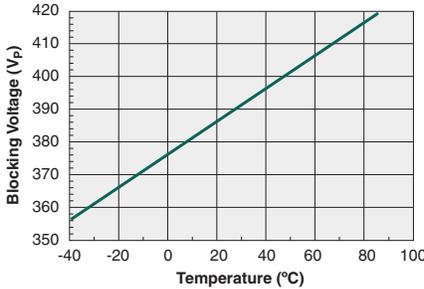
Typical Load Current vs. Load Voltage
($I_F=5mA$)



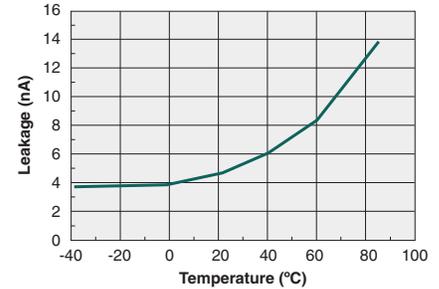
Maximum Load Current vs. Temperature



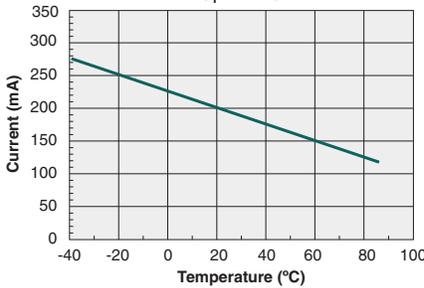
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature Measured across Pins 5&6, 7&8



Typical Current Limiting vs. Temperature
($I_F=5mA$)



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LAA110L / LAA110LS / LAA110PL	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be ($T_C - 5$)°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
LAA110L	250°C	30 seconds	1
LAA110LS	250°C	30 seconds	3
LAA110PL	260°C	30 seconds	3

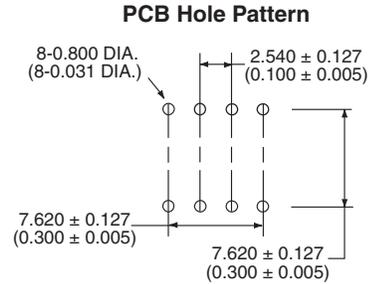
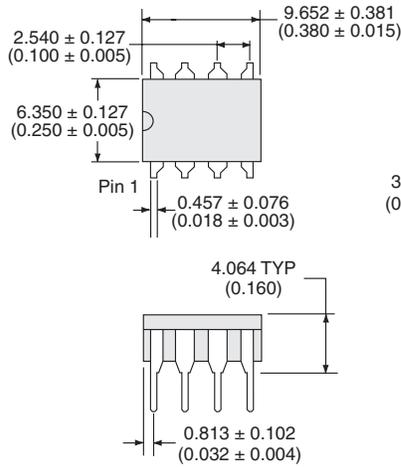
Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.



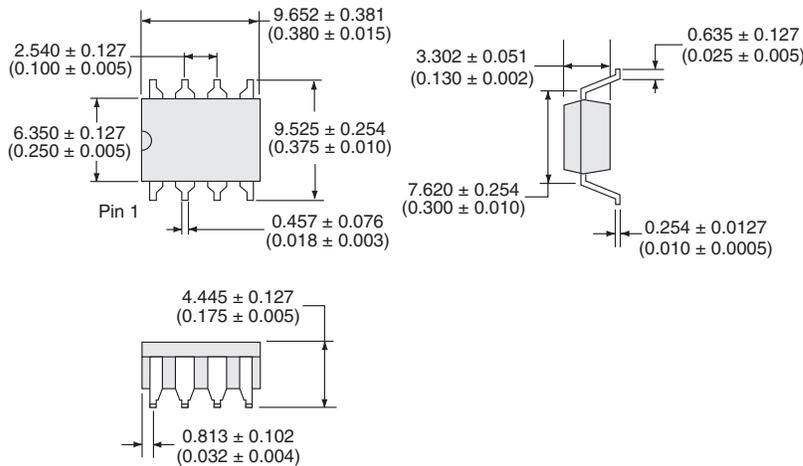
MECHANICAL DIMENSIONS

LAA110L

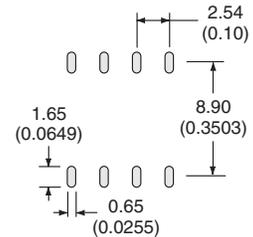


Dimensions
mm
(inches)

LAA110LS

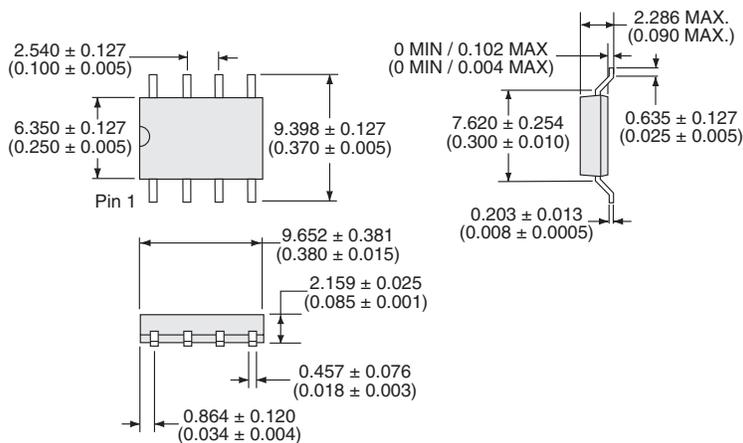


PCB Land Pattern

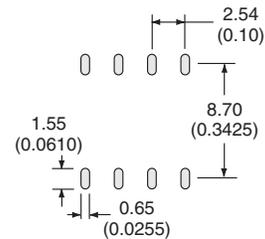


Dimensions
mm
(inches)

LAA110PL



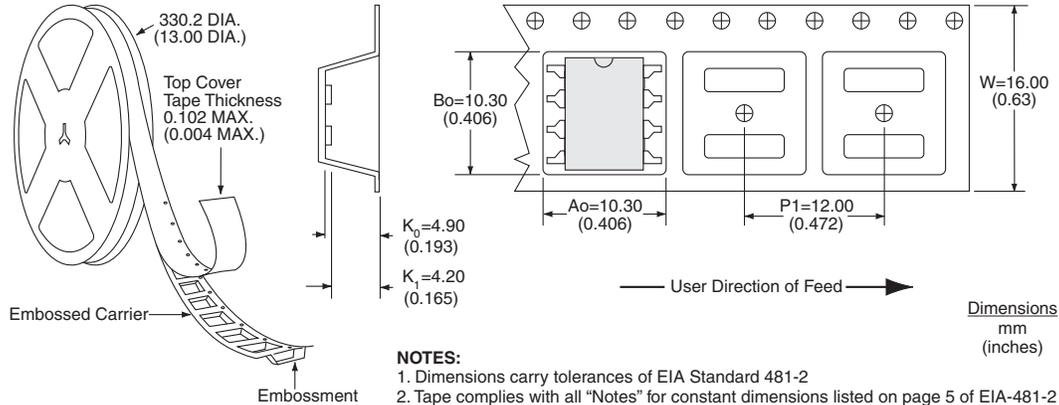
PCB Land Pattern



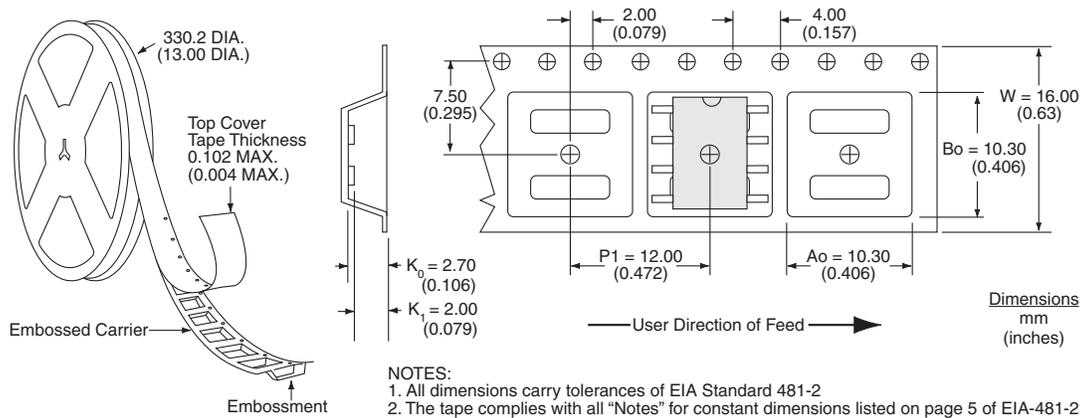
Dimensions
mm
(inches)

MECHANICAL DIMENSIONS

LAA110LSTR Tape & Reel



LAA110PLTR Tape & Reel



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