Displaytech a seacomp company

TFT LCD Module Product Specification

DT043BTFT-PTS1 4.3" (480(RGB) x 272 PIXELS) TFT Module with Capacitive Touch Panel

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1. Scope

This data sheet is to introduce the specification of DT043BTFT-PTS1 active matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC, capacitive touch panel and a backlight unit. The 4.3" display area contains 480 (RGB) x 272 pixels.

2. Application

Digital equipments which need color display, mobile phone, mobile navigator/video systems.

3. General Information

| Item | Contents | Unit |
|-------------------------------|------------------------|------|
| Size | 4.3 | inch |
| Resolution | 480(RGB) x 272 | / |
| Interface | RGB | / |
| Technology Type | a-Si TFT | / |
| Pixel Configuration | R.G.B. Vertical Stripe | / |
| Outline Dimension (W x H x D) | 105.50 x 67.20 x 4.7 | mm |
| Active Area | 95.04 x 53.86 | mm |
| Display Mode | Transmissive | / |
| Backlight Type | LED | / |
| Driver IC | HX8257A01 | / |
| Driver IC for PCAP | FT5346 | / |
| Weight | TBD | g |

4. Outline Drawing

| DISFLAT HTFE: 4.31F1, INANAMISSIVE VIEWING DIRECTION: U/L/D/R 55/65/65 TFT Driver IC: HX8257A01 CTP Driver IC: FT5346 Top: -20° C ~ 70° C, Tst : -30° C ~ 80° C GENERAL TOLERANCE: ±0.2 LCM Luminance:LED/480cd/m² (Typical) RoHS comliance | DTED: TED AVECTOR LEVE ALL STATUS ST |
|---|--|
| DRAWN BY: CHECKED BY: APPROVED BY: CONFIRMED BY: | $\begin{array}{c} 1.20 \text{ MX} \\ + 1.33 \\ + 1.35 \text{ MX} \\ $ |
| isplaytech TITLE: DT043BTFT-PTS1 DWG NO: DWG NAME: SHE | CTP: PIN DEFINE 1 SCL 2 SDA 4 VAKE 6 GND |
| S1 | PIN FUCTION 1 CND 2 CND 3 VBDL 4 VRESET 5 R0 3 VBD 4 VRESET 3 VENC 4 VENC |

5. Interface Signals

Recommended connector: FH12-45S-0.5SH

| Pin No. | Symbol | I/O | Function | |
|---------|---------|-----|---|--|
| 1-2 | GND | Р | Ground | |
| 3 | VDD | Р | Power supply | |
| 4 | RESET | I | Reset the display, active low | |
| 5-12 | R0 ~ R7 | Ι | Red data bus | |
| 13-20 | G0 ~ G7 | I | Green data bus | |
| 21-28 | B0 ~ B7 | | Blue data bus | |
| 29 | PCLK | | Data clock | |
| 30 | DISP | | Display ON/OFF control | |
| 31 | HSYNC | Ι | Horizontal synchronous signal input | |
| 32 | VSYNC | Ι | Vertical synchronous signal input | |
| 33 | ENABLE | Ι | Data enable pin | |
| 34 | U/D | Ι | Select up or down scanning direction | |
| 35 | L/R | I | Select left or right scanning direction | |
| 36 | CS | Ι | Chip select signal | |
| 37 | SCL | I | Serial clock signal | |
| 38 | SDI | | Serial in signal | |
| 39 | SDO | 0 | Serial out signal | |
| 40 | XR (NC) | 0 | Touch panel control pin (NC) | |
| 41 | YD (NC) | 0 | Touch panel control pin (NC) | |
| 42 | XL (NC) | 0 | Touch panel control pin (NC) | |
| 43 | YU (NC) | 0 | Touch panel control pin (NC) | |
| 44 | К | Р | LED backlight (Cathode) | |
| 45 | А | Р | LED backlight (Anode) | |

CTP signal interface

| Pin No. | Symbol | Function |
|---------|--------|--------------------------|
| 1 | SCL | Clock for the data input |
| 2 | SDA | Data input |
| 3 | VDD | Power supply |
| 4 | WAKE | |
| 5 | INT | Interrupt output pin |
| 6 | GND | Ground |

6. Absolute Maximum Ratings

6.1 Electrical absolute maximum ratings

| Parameter | Symbol | MIN | MAX | Unit | Remark |
|----------------------|--------|------|-----|------|--------|
| Power Supply Voltage | VCC | -0.3 | 3.6 | V | |

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

2. VCC > VSS must be maintained.

6.2 Environment conditions

| Parameter | Symbol | MIN | MAX | Unit | Remark |
|-----------------------|--------|-----|-----|------|--------|
| Operating Temperature | TOPR | -20 | +70 | °C | |
| Storage Temperature | TSTG | -30 | +80 | О° | |

Note:

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40°C:85%RH MAX.

Ta>=40°C : Absolute humidity must be lower than the humidity of 85%RH at 40°C .

7. Electrical Specifications

7.1 Electrical characteristics

GND=0V, Ta=25°C Symbol Condition Item MIN TYP MAX Unit Remark Power Supply VDD Ta=25°C 3.0 3.3 3.6 V VIH VDD=3.3V 0.8VDD -VDD V Н Input Voltage 0.2VDD VIL VDD=3.3V V -L 0 Current ICC1 Normal mode -20 30 mΑ Consumption ICC2 Sleep mode 0.05 0.1 mΑ -**Clock Frequency** fCLK 9 12 MHz -

Note: Tested in 1×1 chessboard pattern.

7.2 LED backlight

| | | | | | | Ta=25°C |
|-----------------|--------|-----|--------|-----|------|---------|
| ltem | Symbol | MIN | ТҮР | MAX | Unit | Remark |
| Forward current | IF | - | 40 | - | mA | |
| Forward voltage | VF | - | 15 | - | V | |
| LED life time | - | - | 20,000 | - | Hr | Note |

Note 1: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL =40mA. The LED lifetime could be decreased if operating IL is larger than40mA.

Note 2: Backlight should be driven in constant current mode. Use a supply voltage of 17 Volts or higher with a series resistor to limit the current to 40 mA (e.g. (17V - (5 x 3.2V))/0.040 = 25 ohms). Alternatively, the backlight can be driven from lower voltages with a boost LED driver such as the ON Semiconductor CAT4139.

8. Optical Specification

| | | | | | | | | Ta=25°C |
|----------------|--------|--------|------------|-----|-------|-----|-------------------|-----------|
| ltem | | Symbol | Condition | MIN | TYP | MAX | Unit | Remark |
| Contrast ratio | | CR | θ=0° | 350 | 500 | - | | Note 1, 2 |
| Response tim | 0 | Tr | 25°C | - | 10 | - | ma | Note 1, 3 |
| Response um | e | Tf | 25'0 | - | 10 | - | ms | Note 1, 5 |
| | | ΘΤ | | - | 55 | - | | |
| View angles | | ΘB | CR≧10 | - | 65 | - | Degree | Note 4 |
| view angles | | ΘL | | - | 65 | - | Degree | NOLE 4 |
| | | ΘR | | - | 65 | - | - | |
| | White | х | | - | 0.285 | - | | |
| | vvnite | У | - | - | 0.331 | - | | |
| | Red | х | | - | TBD | - | | |
| Chromaticity | Neu | У | Brightness | - | TBD | - | | Note 1, 5 |
| Chromaticity | Green | х | is on | - | TBD | - | 1 | NOLE 1, 5 |
| | Gleen | У | | - | TBD | - | | |
| | Blue | х | | - | TBD | - | | |
| | Diue | У | | - | TBD | - | | |
| NTSC Ratio | | S | | 50 | 60 | | % | Note 1, 5 |
| Luminance | | L | | 350 | 48/0 | - | cd/m ² | Note 1, 6 |
| Uniformity | | U | | - | 80 | - | % | Note 1, 7 |

Note 1: Definition of optical measurement system.

Temperature = $25^{\circ}C(\pm 3^{\circ}C)$

LED back-light: ON, Environment brightness < 150 lx



Note 2: Contrast ratio is defined as follow:

Contrast Ratio = $\frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$

Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise time, Tr) and from white to black (Decay Time, Tf).







Note 5: Color chromaticity is defined as follow (CIE1931) Color coordinates measured at center point of LCD.



Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels "White" at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig.2). Every measuring point is placed at the center of each measuring area.

Uniformity (U) = $\frac{\text{Minimum Luminance(brightness) in 9 points}}{\text{Maximum Luminance(brightness) in 9 points}}$



Fig. 2 Definition of uniformity

9. Environmental / Reliability Tests

| No | Test Item | Condition | Judgment Criteria |
|----|--------------------------------------|---|---|
| 1 | High Temp Operation | Ta=+70°C, 96hrs | Per table below |
| 2 | Low Temp Operation | Ta=-20°C, 96hrs | Per table below |
| 3 | High Temp Storage | Ta=+80°C, 96hrs | Per table below |
| 4 | Low Temp Storage | Ta=-30°C, 96hrs | Per table below |
| 5 | High Temp & High Humidity Storage | Ta=+60°C, 90% RH, 96hrs | Per table below (polarizer discoloration is excluded) |
| 6 | Thermal Shock (Non-operation) | -30°C 30 min~+80°C 30 min, Change time:5min, 5 Cycles | Per table below |
| 7 | ESD (Operation) | C=150pF, R=330Ω, 5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times; | Per table below |
| 8 | Vibration (Non-operation) | 10Hz~150Hz, 100m/s ² , 120min | Per table below |
| 9 | Shock (Non-operation) | Half- sine wave,300m/s ² ,11ms | Per table below |
| 10 | Package Drop Test | Height: 80cm, 1 corner, 3 edges, 6 surfaces | Per table below |

| Inspection | Criterion (after test) |
|--|---|
| Appearance No crack on the FPC, on the LCD panel | |
| Alignment of LCD panel | No bubbles in the LCD panel No other defects of alignment in active area |
| Electrical current | Within device specifications |
| Function / Display | No broken circuit, no short circuit or no black line No other defects of display |

10. Precautions for Use of LCD Modules

10.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

10.2 Handling

- a. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- b. Do not handle the product by holding the flexible pattern portion in order to assure the reliability.
- c. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.
- d. Provide a space so that the panel does not come into contact with other components.
- e. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.
- f. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.
- g. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.

h. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

10.3 Static electricity

- a. Ground soldering iron tips, tools and testers when they are in operation.
- b. Ground your body when handling the products.
- c. Power on the LCD module before applying the voltage to the input terminals.
- d. Do not apply voltage which exceeds the absolute maximum rating.
- e. Store the products in an anti-electrostatic bag or container.

10.4 Storage

- a. Store the products in a dark place at +25°C±10°C with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.
- b. Storage in a clean environment, free from dust, active gas, and solvent.

10.5 Cleaning

- a. Do not wipe the touch panel with dry cloth, as it may cause scratch.
- b. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

10.6 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

