



74HC138

3 TO 8 LINE DECODER DEMULTIPLEXER

Description

The 74HC138 is a high speed CMOS device.

The device accepts a three bit binary weighted address on input pins A0, A1 and A2 and when enabled will produce one active low output with the remaing seven being high.

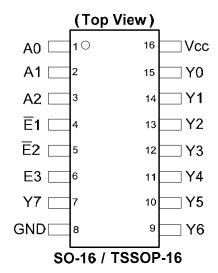
There are two active LOW enable inputs $\overline{E}1$ and $\overline{E}2$, and one active HIGH enable input E3. The disabled device state results in all outputs being high. The enable state occurs with $\overline{E}1$ and $\overline{E}2$ asserted low and E3 asserted high.

The multiple enable lines allow for the parallel expansion of decoders to create 4-to-16 line versions with no additional parts and 5-to-32 versions with the addition of a single inverter.

Features

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or sources 8 mA at V_{CC} = 4.5V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- Inputs Accept up to 6.0V
- ESD Protection Tested per JESD 22
- Exceeds 200-V Machine Model (A115-A)
- Exceeds 2000-V Human Body Model (A114-A)
- Exceeds 1000-V Charged Device Model (C101C)
 Latch-Up Exceeds 100mA per JESD 78D, Class II
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- Memory chip select decoding
- Demultiplexing
- Single line peripheral control
- Allow simple serial bit streams from a microcontroller to control as many peripheral lines as needed

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Click here for ordering information, located at the end of datasheet



Pin Descriptions

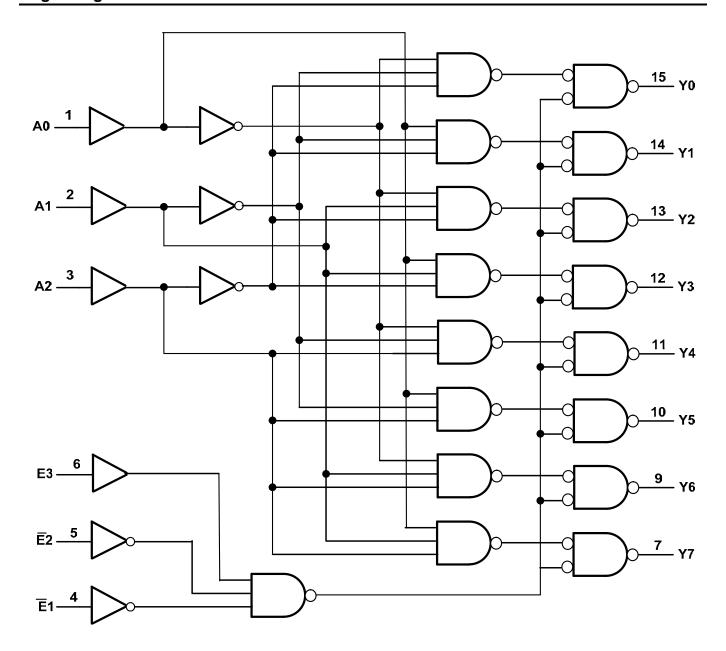
Pin Number	Pin Name	Description
1	A0	Address Input 0
2	A1	Address Input 1
3	A2	Address Input 2
4	E ₁	Enable Input 1 (active LOW)
5	E ₂	Enable Input 2 (active LOW)
6	E3	Enable Input 3 (active HIGH)
7	Y7	Output 7 (active LOW)
8	GND	Ground
9	Y6	Output 6 (active LOW)
10	Y5	Output 5 (active LOW)
11	Y4	Output 4 (active LOW)
12	Y3	Output 3 (active LOW)
13	Y2	Output 2 (active LOW)
14	Y1	Output 1 (active LOW)
15	Y0	Output o (active LOW)
16	Vcc	Supply Voltage

Function Table Diagram

	Control			Input		Output							
E1	E2	E3	A2	A 1	A0	Y 7	Y 6	Y 5	<u>7</u> 4	7 3	<u> 7</u> 2	<u>Y</u> 1	7 0
Н	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н
Х	Н	Х	_	_	_	_	_	_	_	_	_	_	-
Х	Х	L	=	=	-	=	=	-	_	-	=	-	-
L	L	Н	=	=	_	=	=	=	_	=	=	=	-
_	_	_	L	L	L	Н	Н	Н	Н	Н	Н	Н	L
_	_	_	L	L	Н	Н	Н	Н	Н	Н	Н	L	Н
-	-	=	L	Н	L	Н	Н	Н	Н	Н	L	Н	Н
=	=	=	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
=	-	_	Н	L	L	Н	Н	Н	L	Н	Н	Н	Н
-	_	_	Н	L	Н	Н	Н	L	Н	Н	Н	Н	Н
=	-	=	Н	Н	L	Н	L	Н	Н	Н	Н	Н	Н
-	-	-	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н



Logic Diagram





Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range	-0.5 to +7.0	V
Vo	Voltage applied to output in high or low state	-0.3 to V _{CC} +0.5	V
lık	Input Clamp Current V _I < -0.5V	-20	mA
I _{IK}	Input Clamp Current VI > V _{CC} +0.5V	20	mA
I _{OK}	Output Clamp Current Vo<-0.5V	-20	mA
I _{OK}	Output Clamp Current Vo> Vcc + 0.5V	20	mA
lo	Continuous output current	±25	mA
Icc	Continuous current through V _{CC}	50	mA
I _{GND}	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Note:

Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage	_	2.0	6.0	V
V_{I}	Input Voltage	_	0	V_{CC}	V
Vo	Output Voltage	Active Mode	0	V_{CC}	V
		V _{CC} = 2.0V	-	1000	ns/V
Δt/ΔV	Input transition Rise or Fall Rate	V _{CC} = 4.5V	-	500	115/ V
		V _{CC} = 6.0V	-	400	_
T _A	Operating Free-Air Temperature	_	-40	+125	°C

Note:

5. Unused inputs should be held at V_{CC} or Ground.

^{4.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Cumbal	Parameter	Test Conditions	V	T _A	= +25°C		T _A = -40°C	to +85°C	T _A = -40°	C to +125°C	Unit	
Symbol	Parameter	rest Conditions	V _{CC}	Min	Тур	Max	Min	Max	Min	Max	Onic	
		-	2.0V	1.5	1.2	_	1.5	-	1.5	-		
VIH	High-Level Input Voltage	-	4.5V	3.15	2.4	_	3.15	_	3.15	-	V	
	Input voltage	-	6.0V	4.2	3.2	_	4.2	-	4.2	-		
		-	2.0V	-	0.8	0.5	-	0.5	-	0.5		
V_{IL}	Low-Level Input Voltage	-	4.5V	-	2.1	1.35	-	1.35	-	1.35	V	
	Input voltage	-	6.0V	_	2.8	1.8	-	1.8	-	1.8		
			2.0V	1.9	2.0	_	1.9	-	1.9	-		
		$I_{OH} = -20 \mu\text{A}$ All outputs	4.5V	4.4	4.5	_	4.4	-	4.4	-		
Voh	High-Level Output Voltage	All outputs	6.0V	5.9	6.0	-	5.9	_	5.9	-	V	
	Output Voltage	Output Voltage	$I_{OH} = -4 \text{ mA}$	4.5V	3.98	4.32	-	3.84	-	3.7	=	
		I _{OH} = -5.2 mA	6.0V	5.48	5.81	-	5.34	=	5.2	=		
			2.0V	=	0	0.1	=	0.1	=	0.1		
			$I_{OL} = 20 \mu A$	4.5V	=	0	0.1	=	0.1	=	0.1	
VoL	Low-Level Output Voltage	All outputs	6.0V	=	0	0.1	=	0.1	=	0.1	V	
	Output voltage	I _{OL} = 4 mA	4.5V	-	0.15	0.26	=	0.33	-	0.4		
		I _{OL} = 5.2 mA	6.0V	-	0.16	0.26	-	0.33	-	0.4		
II	Input Current	V _I =GND or 6.0V	6.0V	-	-	±0.1	-	± 1	-	± 1	μΑ	
I _{CC}	Supply Current	$V_I = GND \text{ or } V_{CC}$ $I_O = 0$	6.0V	-	_	8.0	-	80	-	160	μΑ	
Ci	Input Capacitance	V _i = V _{CC} or GND	6.0V	-	4	10	=	10	=	10	pF	

Switching Characteristics

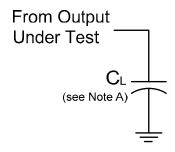
Symbol /	Pins	Test Conditions	V	Т	A = +25°	С	-40°C to	+85°C	-40°C to	+125°C	Unit
Parameter	FIIIS	rest Conditions	V _{CC}	Min	Тур	Max	Min	Max	Min	Max	Oilit
			2.0V	-	41	150	-	190	_	225	
	A = 4 = V =	Figure 1	4.5V	-	15	30	-	38	_	45	
	An to ₹n	rigule i	5.0V	-	12	-	İ	ı	_	-	
			6.0V	-	12	26	-	33	_	38	
t _{PLH} ,			2.0V	-	47	150	-	190	_	225	
t _{PLH}	E3 to \overline{Y} n	Figure 1	4.5V	-	17	30	-	38	_	45	ns
Propagation			5.0V	-	14	_	-	-	_	-	
Delay			6.0V	-	14	26	-	33	_	38	
		Fig. 4	2.0V	-	47	150	-	190	_	225	
	En to Yn		4.5V	-	17	30	-	38	_	45	
	En to Yn	Figure 1	5.0V	-	14	_	-	-	_	-	
			6.0V	-	14	26	-	33	_	38	
t _{TLH,} t _{THL}			2.0V	-	19	75	-	95	_	110	
	₹n	Figure 1	5.0V	_	7	15	_	19	_	22	ns
Transition Time		Ç	6.0V	=	6	13	=	16	=	19	



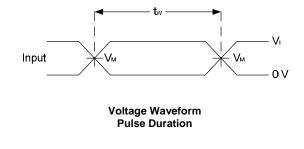
Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

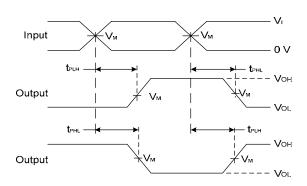
	Parameter	Test Conditions	V _{CC} = 5V Typ	Unit
$C_{\sf pd}$	Power dissipation capacitance	f = 1 MHz all outputs switching-no load	19	pF

Parameter Measurement Information



Vcc	Inj	outs	V	•		
VCC	VI	t _r /t _f	V _M	C _L		
2.0V -6.0V	V _{CC}	6 ns	V _{CC} /2	50pF		
5.0V	Vcc	6 ns	V _{CC} /2	15pF used for 5V typical test		





Voltage Waveform Propagation Delay Times **Inverting and Non Inverting Outputs**

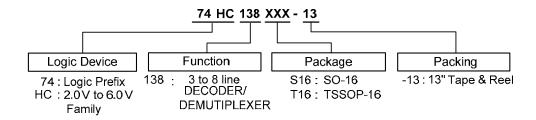
A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
 C. Inputs are measured separately one transition per measurement
- D. t_{PLH} and t_{PHL} are the same as t_{PD}

Figure 1 Load Circuit and Voltage Waveforms



Ordering Information

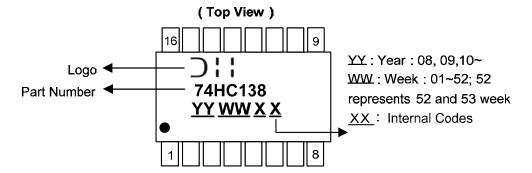


Part Number	Backage Code	Dockoning	7" Tape an	nd Reel (Note 6)
Fait Number	Package Code	Packaging	Quantity	Part Number Suffix
74HC138S16-13	S16	SO-16	2500/Tape & Reel	-13
74HC138T16-13	T16	TSSOP-16	2500/Tape & Reel	-13

Notes: 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf

Marking Information

(1) SO-16, TSSOP16



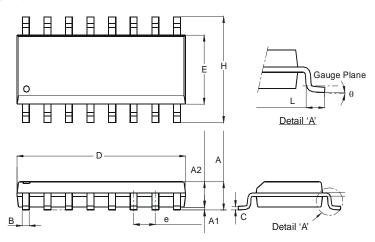
Part Number	Package
74HC138S16	SO-16
74HC138T16	TSSOP-16



Package Outline Dimensions (All dimensions in mm.)

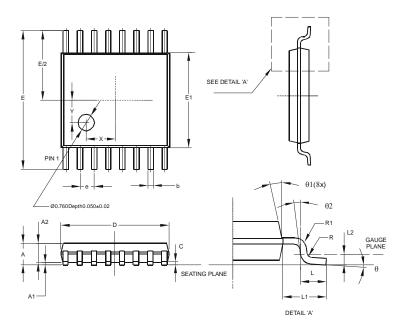
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-16



	SO-16							
Dim	Min	Max						
Α	1.40	1.75						
A1	0.10	0.25						
A2	1.30	1.50						
В	0.33	0.51						
С	0.19	0.25						
D	9.80	10.00						
Е	3.80	4.00						
е	1.27	Тур						
Н	5.80	6.20						
L	0.38	1.27						
Θ	0°	8°						
All Dimensions in mm								

Package Type: TSSOP-16



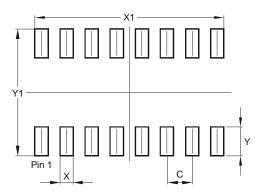
TSSOP-16							
Dim	Min	Max	Тур				
Α	-	1.08	-				
A1	0.05	0.15	-				
A2	0.80	0.93	-				
b	0.19	0.30	-				
C	0.09	0.20	-				
D	4.90	5.10	-				
Е	6	.40 BS	SC Sc				
E1	4.30	4.50	-				
е	C	.65 BS	SC SC				
L	0.45	0.75	-				
L1	1	.00 R	EF				
L2	C	.25 BS	SC Sc				
R	0.09	-	-				
R1	0.09	-	-				
Х	-	-	1.350				
Υ	-	-	1.050				
Θ	0°	8°	-				
Θ1	5°	15°	-				
Θ2	0°	-	-				
All Dimensions in mm							



Suggested Pad Layout

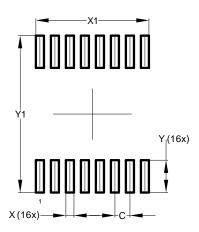
 $Please see AP02001 \ at \ http://www.diodes.com/datasheets/ap02001.pdf \ for \ the \ latest \ version.$

Package Type: SO-16



Dimensions	Value (in mm)
С	1.270
Х	0.670
X1	9.560
Y	1.450
Y1	6.400

Package Type: TSSOP-16



Dimensions	Value (in mm)
С	0.650
Х	0.350
X1	4.900
Y	1.400
Y1	6.800



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