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User's Manual

QB-78K0RLX3

In-Circuit Emulator

Target Devices 78K0R/LF3 78K0R/LG3 78K0R/LH3

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General Precautions for Handling This Product

1. Circumstances not covered by product guarantee

- If the product was disassembled, altered, or repaired by the customer
- If it was dropped, broken, or given another strong shock
- Use at overvoltage, use outside guaranteed temperature range, storing outside guaranteed temperature range
- If power was turned on while connection to the AC adapter, USB interface cable, or target system was in an unsatisfactory state
- If the cable of the AC adapter, the USB interface cable, the emulation probe, or the like was bent or pulled excessively
- If an AC adapter other than the supplied product was used
- If the product got wet
- If this product is connected to the target system when there is a potential difference between the GND of this product and GND of the target system.
- If the connectors or cables are plugged/unplugged while this product is in the power-on state.
- If excessive load is applied to the connectors or sockets (As for handling, please see 2.5 Mounting and Connecting Connectors).
- If a metal part of the power switch, cooling fan, or another such part comes in contact with an electrostatic charge.
- If the product is used or stored in an environment where an electrostatic or electrical noise is likely to occur.

2. Safety precautions

- If used for a long time, the product may become hot (50°C to 60°C). Be careful of low temperature burns and other dangers due to the product becoming hot.
- Be careful of electrical shock. There is a danger of electrical shock if the product is used as described above in 1. Circumstances not covered by product guarantee.
- The AC adapter supplied with the product is exclusively for this product, so do not use it with other products.

INTRODUCTION

Readers This manual is intended for users who wish to perform debugging using the QB-

78K0RLX3. The readers of this manual are assumed to be familiar with the device

functions and usage, and to have knowledge of debuggers.

Purpose This manual is intended to give users an understanding of the basic specifications and

correct usage of the QB-78K0RLX3.

Organization This manual is divided into the following sections.

General

- Setup procedure
- · Settings at product shipment
- Cautions

How to Read This Manual

It is assumed that the readers of this manual have general knowledge in the fields of electrical engineering, logic circuits, and microcontrollers.

This manual describes the basic setup procedures and how to set switches.

To understand the overall functions and usages of the QB-78K0RLX3

→ Read this manual in the order of the CONTENTS. The mark <R> shows major revised points. The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

To know the manipulations, command functions, and other software-related settings of the QB-78K0RLX3

ightarrow See the user's manual of the debugger (supplied with the QB-78K0RLX3) to be used.

Conventions Note: Footnote for item marked with Note in the text

Caution: Information requiring particular attention

Remark: Supplementary information Numeric representation: Binary ... xxxx or xxxxB

Decimal ... xxxx

Hexadecimal ... xxxxH

Prefix indicating power of 2 (address space, memory

capacity): $K \text{ (kilo): } 2^{10} = 1,024$

M (mega): $2^{20} = 1,024^2$

Terminology

The meanings of the terms used in this manual are described in the table below.

| Term | Meaning |
|---------------|--|
| Target device | This is the device to be emulated. |
| Target system | This is the system to be debugged. This includes the target program and the hardware provided by the user. |
| 78K0R/Lx3 | Generic name indicating 78K0R/LF3, 78K0R/LG3 and 78K0R/LH3. |
| IECUBE™ | Generic name for NEC Electronics' high-performance/compact in-circuit emulator. |

Related Documents

Please use the following documents in conjunction with this manual.

The related documents listed below may include preliminary versions. However, preliminary versions are not marked as such.

Documents Related to Development Tools (User's Manuals)

| Document Name | | Document Number |
|---|-------------|-----------------|
| QB-78K0RLX3 In-Circuit Emulator | This manual | |
| RA78K0R Ver. 1.20 Assembler Package Operation | | U18547E |
| | Language | U18546E |
| CC78K0R Ver. 2.00 C Compiler | Operation | U18549E |
| | Language | U18548E |
| ID78K0R-QB Ver. 3.20 Integrated Debugger | Operation | U17839E |
| PM+ Ver. 6.31 Project Manager | | U18416E |

Caution The related documents listed above are subject to change without notice. Be sure to use the latest version of each document for designing, etc.

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CHAPTER 1 GENERAL

The QB-78K0RLX3 is an in-circuit emulator for emulating the 78K0R/Lx3.

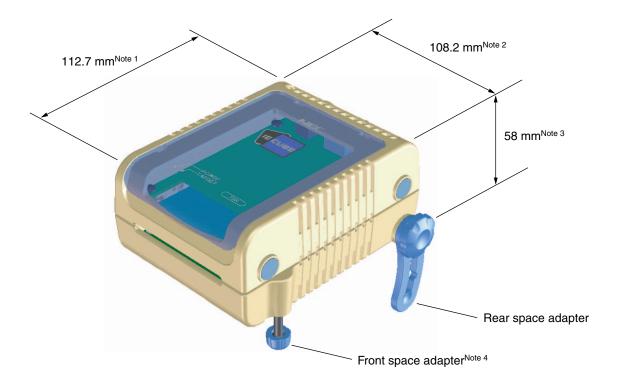
Hardware and software can be debugged efficiently in the development of systems in which the 78K0R/Lx3 is used. This manual descries basic setup procedures, hardware specifications, system specifications, and how to set switches.

1.1 Hardware Specifications

Table 1-1. QB-78K0RLX3 Hardware Specifications

| Parameter | | Specification | | |
|-------------------------------------|----------------------------|--|---|--|
| Target device | | 78K0R/LF3, 78K0R/LG3, 78K0R/LH3 | | |
| Operating voltage | | 1.8 to 5.5 V | | |
| Operating frequency ^{Note} | | High-speed system clock | $2.7 \text{ V} \le \text{V}_{DD} \le 5.5 \text{ V}$: 2 to 20 MHz $1.8 \text{ V} \le \text{V}_{DD} \le 2.7 \text{ V}$: 2 to 5 MHz | |
| | | Internal high-speed oscillation clock | $2.7~V \le V_{DD} \le 5.5~V$: An 8 MHz clock is supplied from the oscillator in the QB-78K0RLX3 | |
| | | | $1.8~V \le V_{DD} < 2.7~V$: An 1 MHz clock is supplied from the oscillator in the QB-78K0RLX3 | |
| | | Subsystem clock | A 32.768 kHz clock is supplied from the oscillator in the QB-78K0RLX3 | |
| Operating temp | perature range | 0 to 40°C (No condensation) | | |
| Storage tempe | rature range | −15 to 60°C (No condensation) | | |
| External dimen | sions | See figure below | | |
| Power consumption | AC adapter for QB-78K0RLX3 | Output: DC15 V, 1 A Input: AC100 to 240 V | | |
| Target system power supply | | Voltage: 1.8 to 5.5 V Current: approx. 10 mA MAX. | | |
| Weight | | Approx. 480 g | | |
| Host interface | | USB interface (1.1, 2.0) | | |

<R> Note Errors are within ±0.5%. However, this does not apply to errors of the oscillator or clock system on the target board.



Notes 1. Does not include projection of power switch

- 2. Includes projection of screw that fixes rear space adapter
- 3. Rear space adapter can adjust the height from 30 mm (longest) to 0 mm (shortest)
- 4. Front space adapter can adjust the height from 20 mm (longest) to 5 mm (shortest)

1.2 System Specifications

This section shows the QB-78K0RLX3 system specifications.

Table 1-2. QB-78K0RLX3 System Specifications

| Parameter | | Specification | | |
|-----------------------------|----------------------------------|--|--|--|
| Emulation memory capacity | Internal ROM | 512 KB (MAX.) | | |
| | Internal RAM | 61.75 KB (MAX.) | | |
| Program execution functions | Real-time execution function | Go, Start from Here, Come Here, Restart, Return Out, Ignore break points and Go | | |
| | Non-real-time execution function | Step In, Next Over, Slowmotion, Go & Go | | |
| Memory manipulation | | Available (initialize, copy, compare) | | |
| Register manipulation | | Available (general-purpose registers, control registers, SFRs) | | |
| Disassemble function | | Available | | |
| Local variable view | | Local variables | | |
| Watch data view | | Local variables, global variables, or else | | |
| Stack trace view | | Available | | |
| Break functions | Event break | Execution: 8 points Access: 8 points | | |
| | Software break | 2000 points | | |
| | Pre-execution break | 4 to 8 points ^{Note} | | |
| | Fail-safe break | Non-map, write protect, SFR illegal access, stack overflow, or els | | |
| | Other | Forcible break, trace full break, trace delay break, timeout break, timer overflow break | | |
| Trace functions | Trace data types | Program address, program data, access address, access data, status, time tag | | |
| | Trace modes | Unconditional trace, section trace, qualify trace, delay trigger trace | | |
| | Trace functions | Non-stop, full stop, full break, delay trigger stop, delay trigger break | | |
| | Memory capacity | 128K frames | | |
| Real-time RAM monitoring fu | inction | All internal RAM spaces | | |
| Time measurement | Measurement clock | 60 MHz | | |
| functions | Measurement objects | Start through end of program execution Start event through end event | | |
| | Maximum measurement time | Approx. 40 hours and 43 minutes (Resolution: 17 ns) | | |
| | Number of timers for measurement | Start through end of program execution: 1 Start event through end event: 2 | | |
| | Measurement results | Execution time (start through end of execution) Maximum, minimum, average, total, pass count (between events) | | |
| | Other | Timer overflow break function, timeout break function | | |
| Other functions | | Command functions set in the console, mapping function, event function, coverage function, snapshot function, DMM function, power-off emulation function, pin mask function, flash self programming emulation function | | |

Note The number of breaks that can be set varies depending on the location where the break is set.

1.3 System Configuration

This section shows the system configuration when using the QB-78K0RLX3 connected to a PC (Windows TM PC (Windows 2000 and Windows XP), PC/AT compatible). Connection is possible even without optional products.

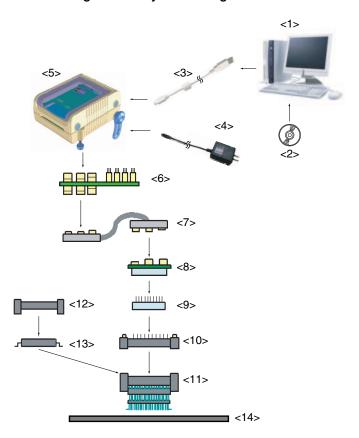


Figure 1-1. System Configuration

- <1> Host machine
- <2> ID78K0R-QB Disk/Accessory Disk
- <3> USB interface cable
- <4> AC adapter
- <5> QB-78K0RLX3

<R>

- <6> Check pin adapter (optional)
- <7> Emulation probe
- <8> Exchange adapter
- <9> Space adapter (optional)
- <10> YQ connector
- <11> Target connector
- <12> Mount adapter (optional)
- <13> Device
- <14> Target system

- : Windows PC (Windows 2000 and Windows XP), IBM PC/AT compatible can be used
- : Debugger, USB drivers, manual, etc.
- : Cable connecting QB-78K0RLX3 to host machine
- : AC adapters classified by region
- : This product
- : Adapter used for monitoring waveforms with oscilloscope
- : High-characteristic FPC type emulation probe
- : Adapter that performs pin conversion
- : Adapter used for height adjustment
- : Connector that connects exchange adapter to target connector
- : Connector soldered to target system
- : Adapter used for mounting target device into socket
- : Target device

Remarks 1. Obtain device files from the NEC Electronics website. http://www.necel.com/micro/en/ods/

<R>

<R> <R>

- 2. Refer to 1.5 Package Contents for the purchase forms of the above products.
- 3. As for handling of connectors, refer to 2.5 Mounting and Connecting Connectors.
- **4.** The part number of <4> differs depending on the region of use. See **Table 1-5 Part Numbers of AC Adapter for IECUBE Classified by Region** for the part numbers. The IECUBE requires an AC adapter that must be purchased separately.
- 5. See Table 1-4 Common Probe and Adapter for the part numbers of <6> and <7>.
- 6. The combination of <8>, <9>, <10>, <11>, and <12> varies depending on the emulation device.See Table 1-3 Adapters and Connectors for Each Target Device for the combinations.

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1.4 System Configuration for Each Target Device

The following table lists the system configuration for each target device of the QB-78K0RLX3.

Table 1-3. Adapters and Connectors for Each Target Device

| Target Device | Package | Exchange Adaptor | Space Adaptor | YQ Connector | Target Connector | Mount Adaptor |
|------------------|---------|------------------|-----------------|-----------------|------------------|-----------------|
| 78K0R/LF3 | 80GC | QB-80GC-EA-09T | QB-80GC-YS-01T | QB-80GC-YQ-01T | QB-80GC-NQ-01T | QB-80GC-HQ-01T |
| | 80GK | QB-80GK-EA-08T | QB-80GK-YS-01T | QB-80GK-YQ-01T | QB-80GK-NQ-01T | QB-80GK-HQ-01T |
| 78K0R/LG3 | 100GC | QB-100GC-EA-08T | QB-100GC-YS-01T | QB-100GC-YQ-01T | QB-100GC-NQ-01T | QB-100GC-HQ-01T |
| 78K0R/LH3 | 128GF | QB-128GF-EA-01T | QB-128GF-YS-01T | QB-128GF-YQ-01T | QB-128GF-NQ-01T | QB-128GF-HQ-01T |

Table 1-4. Common Probe and Adapter

| Name | Part Number |
|-------------------|---------------|
| Check pin adapter | QB-144-CA-01 |
| Emulation probe | QB-144-EP-02S |

The adapter and connector for each device, and common probe and adapter are sold separately. An exchange adapter, a YQ connector, a target connector, and an emulation probe are included, depending on the order product name. For details, refer to **1.5 Package Contents.**

Remark For the package drawings of the connector, adapter, and probe, refer to the following URL. http://www.necel.com/micro/en/development/asia/Emulator/IE/iecube.html

<R>

1.5 Package Contents

The included products are described for each order product name.

Products supplied with QB-78K0RLX3-ZZZ

- 1: QB-78K0RLX3
- 2: AC adapter
- 3: USB interface cable (2 meters)
- 4: Probe holder
- 5: Online user registration card (warranty card and software contract in one)
- 6: ID78K0R-QB Disk (CD-ROM)
- 7: Accessory Disk (CD-ROM)
- 8: IECUBE Setup Manual (Japanese/English)
- 9: Packing list
- 10: QB-MINI2

Products supplied with QB-78K0RLX3-T128GF

1 to 10

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-128GF-EA-01T
- 13: YQ connector QB-128GF-YQ-01T
- 14: Target connector QB-128GF-NQ-01T

Products supplied with QB-78K0RLX3-T100GC

1 to 10

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-100GC-EA-08T
- 13: YQ connector QB-100GC-YQ-01T
- 14: Target connector QB-100GC-NQ-01T

Products supplied with QB-78K0RLX3-T80GC

1 to 10

<R>

<R>

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-80GC-EA-09T
- 13: YQ connector QB-80GC-YQ-01T
- 14: Target connector QB-80GC-NQ-01T

Products supplied with QB-78K0RLX3-T80GK

1 to 10

- 11: Emulation probe QB-144-EP-02S
- 12: Exchange adapter QB-80GK-EA-08T
 - 13: YQ connector QB-80GK-YQ-01T
 - 14: Target connector QB-80GK-NQ-01T

<R>> 1.6 AC Adapter for IECUBE

The specifications of the AC adapter for IECUBE differ depending on the region of use. Be sure to use an AC adapter corresponding to the region of use.

The IECUBE is not provided with an AC adapter. It must be purchased separately.

Table 1-5. Part Numbers of AC Adapter for IECUBE Classified by Region

| Product | Destination (Region)Notes 1, 2 | Part Number ^{Note 3} |
|-------------------|--------------------------------|-------------------------------|
| AC adapter | Japan | QB-COMMON-PW-JP |
| (sold separately) | USA | QB-COMMON-PW-EA |
| | China | QB-COMMON-PW-CN |
| | Hong Kong | QB-COMMON-PW-HK |
| | South Korea | QB-COMMON-PW-KR |
| | Singapore | QB-COMMON-PW-SG |
| | Taiwan | QB-COMMON-PW-TW |

Notes 1. Products are shipped only on order from each region.

- 2. Contact a distributor or an NEC Electronics sales representative for information on regions other than the above.
- 3. Only the AC adapter usable in each region can be ordered.

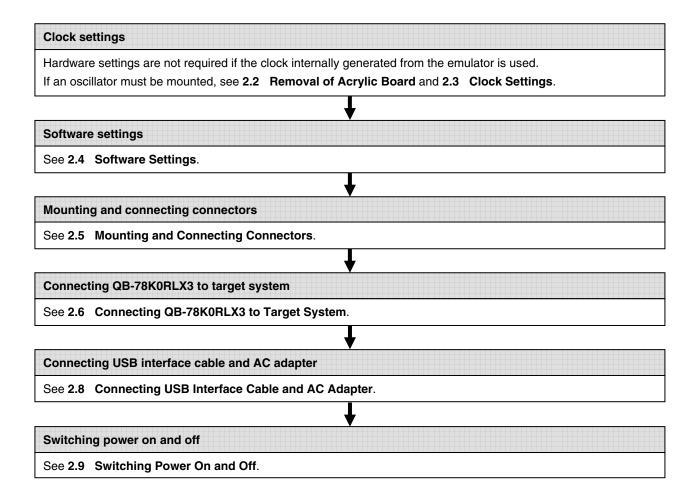
CHAPTER 2 SETUP PROCEDURE

This chapter explains the QB-78K0RLX3 setup procedure.

Setup can be completed by performing installation setup in the order in which it appears in this chapter.

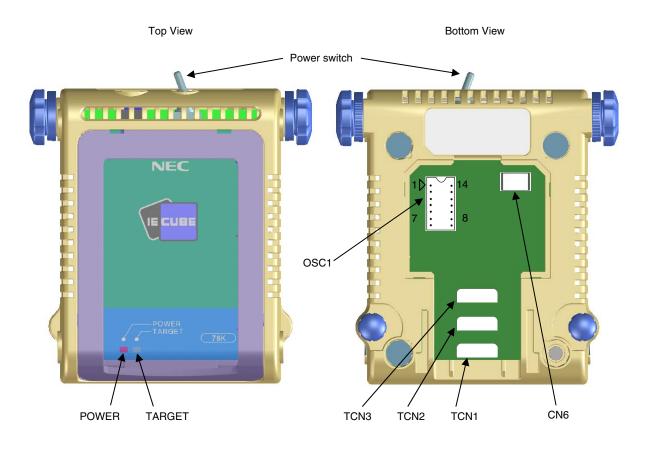
Perform setup along the lines of the following procedure.

See 2.1 Names and Functions of Hardware for clock positions.

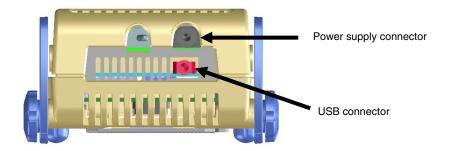


2. 1 Names and Functions of Hardware

Figure 2-1. Names of Parts of QB-78K0RLX3



Side View



(1) TCN1, TCN2, TCN3

These are connectors for connecting a check pin adapter or emulation probe.

(2) OSC1

This is a socket for mounting the oscillator.

(3) CN6

This is a connector for the shipment inspection. It is not something that the user will need.

(4) POWER (Red LED)

This is an LED that shows whether the power supply of the QB-78K0RLX3 is switched on.

| LED State | QB-78K0RLX3 State | | | |
|-----------|---|--|--|--|
| Lit | Power switch ON | | | |
| Not lit | Power switch OFF or AC adapter not connected to QB-78K0RLX3 | | | |
| Blinking | Internal error occurred (Contact an NEC Electronics sales representative or | | | |
| | distributor) | | | |

(5) TARGET (Green LED)

This is an LED that shows whether the power supply of the target system is switched on.

| LED State | Target System State |
|-----------|---|
| Lit | Target system power supply ON |
| Not lit | Target system power supply OFF or target system not connected |

(6) Power switch

This is the power switch of the QB-78K0RLX3.

It is OFF at shipment.

2. 2 Removal of Acrylic Board

To modify the clock setting, the acrylic board on the bottom of the QB-78K0RLX3 must be removed. The acrylic board can be removed by lifting it up.

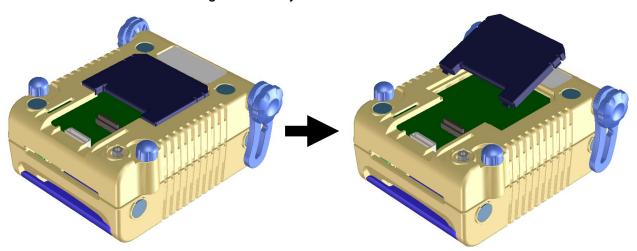


Figure 2-2. Acrylic Board Removal Method

2. 3 Clock Settings

The following four types of clock settings are available.

Each clock setting is listed below.

| Clock Used | Clock Supply | Debugger Setting (in Configuration Dialog) |
|---|--|--|
| (1) High-speed system clock Note 1 | (a) When the clock generated within the emulator is used | System |
| (X1 oscillator or External | (b) When the clock is supplied from the target system | External |
| input) | (c) When the oscillator (OSC1) mounted onto the emulator is used | Clock Socket |
| (2) Internal high-speed oscillation clock | Uses the clock internally generated from the emulator | _ |
| (3) Subsystem clock Note 2 | (a) When the clock generated within the emulator is used | System |
| (XT1 oscillator) | (b) When the clock is supplied from the target system | External |

Notes 1. First, select "System" in the debugger settings (refer to (a) When the clock generated within the emulator is used, in (1) High-speed system clock).

If there is no clock that can be selected, follow the descriptions below.

- If the target system clock can supply a square wave for the emulator: Select "External" in the debugger settings (refer to (b) When the clock is supplied from the target system, in (1) High-speed system clock).
- If the target system clock cannot supply a square wave for the emulator:

 Mount onto the emulator the oscillator of the clock to be used and select "Clock Socket" in the debugger settings (refer to (c) When the oscillator (OSC1) mounted onto the emulator is used, in (1) High-speed system clock).
- 2. First, select "System" in the debugger settings (refer to (a) When the clock generated within the emulator is used, in (3) Subsystem clock).
 - If there is no clock that can be selected, it can be supplied from the target system clock. A square wave, however, must be supplied (refer to (b) When the clock is supplied from the target system, in (3) Subsystem clock).

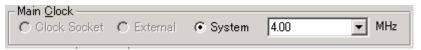
Oscillation with the resonator on the target system is not supported. Therefore, the in-circuit emulator cannot emulate the oscillation operation of the clock on the target system.

(1) High-speed system clock

The clock settings are listed below.

Table 2-1. Settings for High-Speed System Clock

| Type of Clock to Be Used | OSC1 | Debugger Setting |
|--|--------------------|------------------|
| (a) When the clock generated within the emulator is used | - | System |
| (b) When the clock is supplied from the target system Note | _ | External |
| (c) When the oscillator (OSC1) mounted onto the emulator is used | Oscillator mounted | Clock Socket |



Note This setting is not possible when TARGET LED is not lit.

Remarks 1. Settings other than the above are prohibited.

2. Selection of (a) or (b) is possible regardless of whether the oscillator is not mounted in the OSC1socket.

(a) When the clock generated within the emulator is used

Select the "System" in the debugger and select the desired frequency from the drop-down list.

The following frequencies are selectable.

2.00, 3.00, 3.57, 4.00, 4.19, 4.91, 5.00, 6.00, 8.00, 8.38, 10.00, 12.00, 16.00, 20.00 [MHz]

(b) When the clock is supplied from the target system

Select the "External" in the debugger. The clock input from the target system is then used.

Oscillation with the resonator on the target system is not supported. To input a clock from the target system, input to the clock pin (X2) the square-wave signal with the same voltage potential as that of the target device supply voltage (VDD). Inputting the inverted signal to X1 is not necessary.

The selectable frequencies are same as those of the target device.

(c) When the oscillator (OSC1) mounted onto the emulator is used

Mount an oscillator in the OSC1 socket in the emulator and then select the "Clock socket" in the debugger. The clock generated from the oscillator mounted on the emulator is used.

The selectable frequencies are same as those of the target device.

As an oscillator^{Note} to be mounted in the OSC1 socket in the emulator, use the one that satisfies the following specifications.

- Supply voltage: 5 V

- Output level: CMOS

Note An oscillator that uses a resonator cannot be used.







Figure 2-3. Oscillator Shape

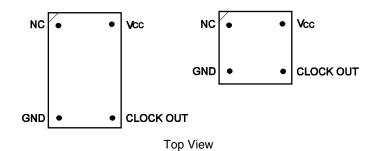
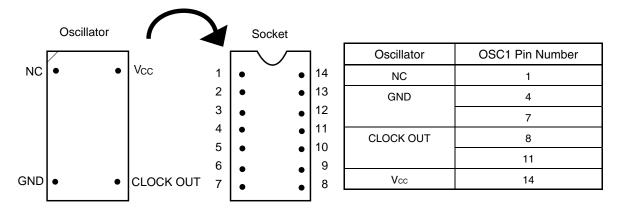


Figure 2-4. Mapping of Oscillator to Socket



Remark Insert the oscillator into the socket, take care for the pin 1 position.

(2) Internal high-speed oscillation clock

The debugger setting is not necessary.

The use of the internal high-speed oscillation clock can be specified in the user program.

(3) Subsystem clock

The clock settings are listed below.

Table 2-2. Settings for Subsystem Clock

| Type of Clock to Be Used | Debugger Setting |
|--|------------------|
| (a) When the clock generated within the emulator is used | System |
| (b) When the clock is supplied from the target system Note | External |



Note This setting is not possible when TARGET LED is not lit.

Remark Settings other than above are prohibited.

(a) When the clock generated within the emulator is used

Select the "System" in the debugger and select "32.768" [kHz] as the frequency from the drop-down list.

32.768 [kHz]

Remark "38.400" [kHz] can also be selected from the list, but do not select this frequency; it is not supported by the device.

(b) When the clock is supplied from the target system

Select the "External" in the debugger. The clock input from the target system is then used.

Oscillation with the resonator on the target system is not supported. To input a clock from the target system, input to the clock pin (XT2) the square-wave signal with the same voltage potential as that of the target device supply voltage (VDD). Inputting the inverted signal to XT1 is not necessary.

The selectable frequencies are same as those of the target device.

2. 4 Software Settings

For details, see the ID78K0R-QB Ver. 3.20 Integrated Debugger Operation User's Manual (U17839E).

2. 5 Mounting and Connecting Connectors

This section describes the methods of connecting the QB-78K0RLX3 and target system.

Make connections with both the QB-78K0RLX3 and target system powered OFF.

The following abbreviations are used in this section:

- TC: Target connector
- YQ: YQ connector
- EA: Exchange adapter
- MA: Mount adapter
- CA: Check pin adapter
- SA: Space adapter

2.5.1 Mounting TC to target system

- (1) Thinly apply a two-component epoxy adhesive (hardening time at least 30 minutes) to the ends of the four projections on the base of the TC and adhere the TC to the user board (clean the surface of the target system board using alcohol or the like). If alignment of target system pads to TC leads is difficult, align them as in (2).
- (2) Align by inserting the guide pins for alignment for the TC (NQGUIDE) through the pin holes on the top of the TC. Accessory holes are φ1.0 mm non-through holes in two or three places.

(For hole positions, see the particular TC drawing.)

- (3) Solder after fitting the MA to the TC. This is to prevent troubles such as flux or solder splashing and adhering to the TC contact pins when soldering.
 - Soldering conditions Solder reflow 260°C × 10 seconds or less

Manual soldering $350^{\circ}\text{C} \times 5 \text{ seconds or less (1 pin)}$

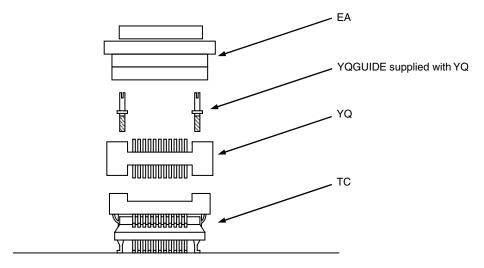
Caution Do not perform washing by flux immersion or vapor.

(4) Take away the guide pins.

2.5.2 Mounting YQ to TC

- (1) After confirming that there are no broken or bent YQ contact pins, fit the YQ in the TC and fasten it using the supplied YQGUIDE (for the fastening method, see the next step, (2)). If repeatedly inserting and removing, be sure to inspect the YQ pins before fitting. If pins are bent, correct them using something thin and flat such as the edge of a knife.
- (2) Fasten YQ to the TC on the target system using the supplied YQGUIDE. Fasten the screws equally in the four corners using the supplied flat-blade screwdriver or a torque driver. The tightening torque of the YQGUIDE is 0.054 Nm (MAX.). Too great tightening causes bad connections.

However, four screws for fitting to the TC (M2 x 10 mm / 4 units) are included with the YQ.



Target system

2.5.3 Plugging EA into YQ

Match the pin 1 position of the YQ or SA (corner cuts match in both) to the pin 1 position of the EA and plug in

- When plugging or unplugging, press on the TC, YQ, and SA with a finger so that there is no force on the TC.
- When plugging or unplugging, be careful of the direction of wiggling.

As a tool when unplugging, insert some kind of thin non-conductive material such as a wooden stick between the YQ (SA) and EA and wiggle while slowly unplugging. Be careful since the connector will be damaged if this is done in the wrong direction.

2.5.4 Precautions for handling TC, YQ, SA, and CA

- (1) When taking the TC from the box, press down on the body and take out the sponge first.
- (2) Since the pins of the YQ are thin and easily bent, be careful. When inserting it in the TC, confirm that there are no bent pins.
- (3) When screwing a YQ soldered to a board to the TC, fasten the screws in four places in turn using a #0 or #1 Phillips precision screwdriver or torque driver after tentatively tightening them. Fix the torque at 0.054 Nm (MAX.).
 - If just one place is overtightened, it may cause poor contact. Moreover, a board being connected to the YQ must have accessory holes in prescribed positions (four places: ϕ 2.3 mm or ϕ 3.3 mm). The ϕ 3.8 mm or ϕ 4.3 mm that is the screw head size is an area where wiring is prohibited.
- (4) In YQ and SA removal, since there is a danger of YQ pins being bent or broken when prying and wiggling, remove them gradually using a flatbladed screwdriver from four directions. Moreover, to connect and use the YQ and SA, screw the YQ to the TC according to the YQGUIDE (included with the YQ) using a 2.3 mm flatbladed screwdriver and then connect it to the SA. Fix the torque at 0.054 Nm (MAX.). If even one place is overtightened, it may cause poor contact.
- (5) For the TC, YQ, and SA, since there is a danger that washing fluid on the structure will remain in the connector, do not perform washing.
- (6) TC, IC, and YQ cannot be used in combination.
- (7) A TC/YQ system cannot be used in an environment of vibrations or shocks.
- (8) It is assumed that this product will be used in system development and evaluation. Moreover, when used in Japan, Electrical Appliance and Material Control Law and electromagnetic disturbance countermeasures have not been applied.
- (9) Since there are rare cases of shape change if the box is left for a long time in a place where it is 50°C or higher, for safekeeping, store it in a place where it is no higher than 40°C and direct sunlight does not hit it.
- (10) For details about handling the TC, YQ, and SA, see the NQPACK series technical materials at the website of Tokyo Eletech Corporation.

URL: http://www.tetc.co.jp/

(11) CA

The CA is an optional product for IECUBE, and can be used to measure the waveform between IECUBE and the target system.

Since the pins on the CA do not correspond to the pin layout in each device, the pin header cover must be mounted according to the device to be used. For mounting methods of the pin header cover, refer to [Related Content] on the following URL.

http://www.necel.com/micro/english/iecube/index.html

2.5.5 Precautions for mounting IC using TC and MA

- (1) Confirm that there is no weld flash in the resin (sealant part) of the IC. If there is weld flash, remove it using a knife or the like.
- (2) Confirm that there is no weld flash breaking or bending of IC leads. In particular, confirm the planarity of IC leads. If there is abnormality in the planarity, correct that portion.
- (3) Viewing the TC contact pins from the top, if there are foreign bodies on them, remove them using a brush or the like.
 - After confirming (1) to (3), fit the IC to the TC. Also fit the MA.
- (4) Put the supplied M2 x 6 mm screws in the four accessory holes of the MA and fasten the screws in opposite corners. At that time, use either the dedicated screwdriver that is supplied or a torque driver to fasten them equally in turn with a tightening torque of 0.054 Nm (MAX.). Since the contact is poor if tightening is too great, once you have lightly fastened the MA screws, tighten them again.
- (5) Depending on the use environment, when starting up a device that has been left for a long time, starting it may be difficult. In this case, loosen the screws slightly and then retighten them.
- (6) If startup still is difficult after (5) above, check (1) to (3) again.
- (7) Tightening the screws of the MA too much may give rise to cracks in the molded part of the MA (plastic part) and bend the mold into a bowed shape, making contact poor.
- (8) After soldering the TC, do not perform cleaning by flux immersion or vapor.

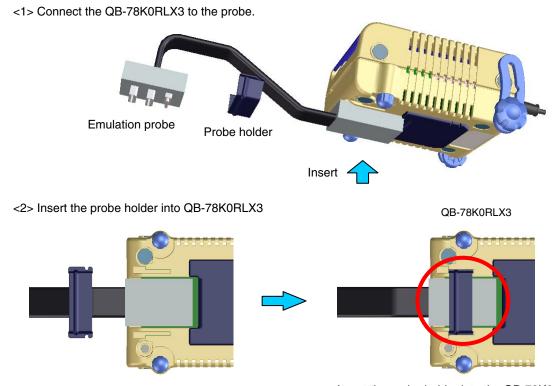
2. 6 Connecting QB-78K0RLX3 to Target System

If connecting the emulation probe (QB-144-EP-02S), connect it to the QB-78K0RLX3 and the target system by the following procedure.

(a) Connecting probe holder

Use the probe holder (supplied with the QB-78K0RLX3) to connect the emulation probe to the QB-78K0RLX3, as shown below.

Figure 2-5. Using Probe Holder



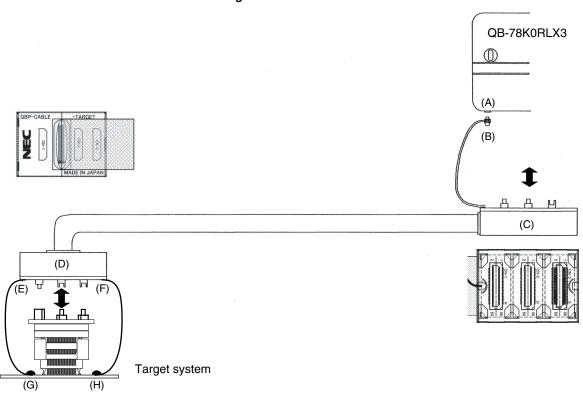
Insert the probe holder into the QB-78K0RLX3 until you hear a click (note the direction).

(b) Connection of emulation probe GND wire

There are three GND wires in the emulation probe. Connect them to the QB-78K0RLX3 and target system.

- <1> Fasten the GND wire on the QB-78K0RLX3 side of the emulation probe to the nut on the bottom of the QB-78K0RLX3 using a #0 or #1 Phillips precision screwdriver (connection of B to A in **Figure 2-6**).
- <2> Next insert the connector on the top of the emulation probe into the connector at the opening on the bottom of the QB-78K0RLX3 from below being careful of the insertion direction (connection of C in Figure 2-6 to QB-78K0RLX3).

Figure 2-6. GND Wire



- <3> Connect the exchange adapter and emulation probe to the target connector.
- <4> Connect the two GND wires on the target system side of the emulation probe to the target system GND. If a pin or screw is fastened to the target system GND, remove the transparent terminal cover on the end of the GND wire and fasten the Y terminal of the GND wire to the target system (G in Figure 2-6). If the GND on the target system is an exposed pad, likewise fasten the Y terminal to the pad on the target system by soldering (H in Figure 2-6) (recommended soldering iron temperature setting: 300°C).
- <5> If the target system has only one GND, connect only one of the GND wires of the emulation probe. Cut off the other GND wires with a nipper or leave it as is without removing the pin cover.
- <6> Since the length of the GND wire below the head (insulated part) is approximately 60 mm, there must be at least a GND to which it can be connected to within the range of the two approximately 60 mm radius sections of the target system for connecting the emulation probe, as shown in **Figure 2-7**. The GND wire of the emulation probe is soldered to positions J and K in **Figure 2-7**.

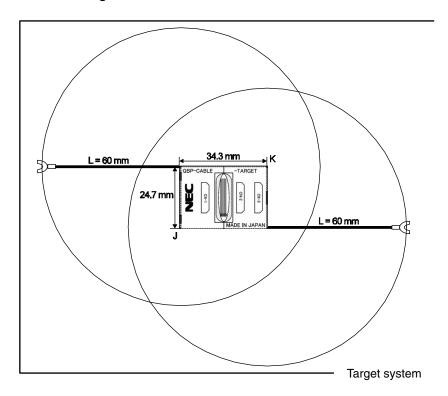


Figure 2-7. Where GND Wire Can Be Connected

(c) Ensuring isolation

When connecting the target system to the QB-78K0RLX3 using an emulation probe, perform height regulation using the front spacer or rear spacer of the QB-78K0RLX3 and ensure isolation from the target system.

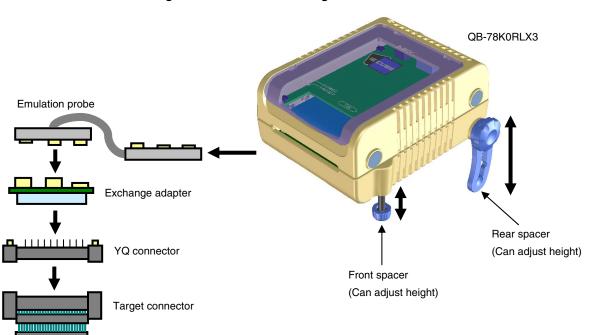


Figure 2-8. Connection Using Emulation Probe

(d) Precautions related to emulation probe

- <1> Be careful that stress of the emulation probe is not placed on the target connector. Moreover, when removing the emulation probe, remove it slowly while pressing down on the exchange adapter with a finger so that there is no stress on the target connector.
- <2> Be sure to connect the GND wire of the emulation probe to the QB-78K0RLX3 and the target system. If it cannot be connected, the impedance of the cable is unstable and could bring about lowering of signal transmission characteristics or distortion of the output waveform for an input waveform.

2. 7 Notes on Power Supply and GND Pin Connection

For power supplies and GND pins of the target device, be sure to connect all pins to each power supply or GND.

2. 8 Connecting USB Interface Cable and AC Adapter

Plug the USB interface cable supplied with the QB-78K0RLX3 into the USB connector of the host machine, and plug the other side into the USB connector on the rear of the QB-78K0RLX3.

Plug the AC adapter supplied with the QB-78K0RLX3 into a receptacle and plug the other side into the power supply connector on the rear of the QB-78K0RLX3.

For QB-78K0RLX3 connector positions, see Figure 2-9.

Power supply connector (Connect AC adapter)

USB connector (Connect USB interface cable)

Figure 2-9. Connector Positions

2. 9 Switching Power On and Off

Be sure to switch the power on and off according to the following procedures.

- Switching power on - Switching power off

<1> QB-78K0RLX3 power on <1> Debugger termination

<2> Target system power on Note <2> Target system power off Note

Note In the procedures, <2> is unnecessary if the target system is not connected.

Caution If the wrong sequence was used for the operation, the target system or QB-78K0RLX3 may fail

CHAPTER 3 SETTINGS AT PRODUCT SHIPMENT

Table 3-1. Settings at Shipment

| Item | Setting | Remarks |
|--------------|-------------|---|
| OSC1 | Not mounted | Oscillator can be mounted ^{Note} . |
| Power switch | ON OFF | Set to OFF at shipment. |

Note The oscillation circuit using an oscillation cannot be used.

CHAPTER 4 CAUTIONS

4.1 Cautions Regarding Differences Between Target Device and Emulator

When debugging is performed by connecting the QB-78K0RLX3 to the target system, the QB-78K0RLX3 emulates the target device as if it operates in the target system. However, the target device and the QB-78K0RLX3 operate differently in the following ways. Consequently, the target device should be used for final evaluation before launching mass production, and the customer is to be responsible for judging the appropriateness of applying the QB-78K0RLX3.

4.1.1 Operation after target system power application

After power application, a program will be executed for the target device mounted in the target system when reset is released. However, with the QB-78K0RLX3, the program does not start until an operation to start execution is performed after the program is downloaded using the debugger.

4.1.2 Oscillator

The QB-78K0RLX3 does not support clock input by an oscillator in the target system. Consequently, the operation clock frequency when the target device is mounted in the target system may differ from the operation clock frequency when the QB-78K0RLX3 is connected.

4.1.3 Pin characteristics

The pin characteristics of the QB-78K0RLX3 slightly differ when the target device is mounted to the target system, because a connector, adapter, emulation probe, and circuit board exist between the QB-78K0RLX3 and the target system. In particular, note that the A/D converter conversion results are commonly-affected.

4.1.4 On-chip debug function

The on-chip debug function cannot be emulated.

4.1.5 Current consumption

The current consumption of the QB-78K0RLX3 differs from that of the target device.

The current consumption may reach up to 10 mA. The same amount of current is consumed in standby mode.

4.1.6 FLMD0 pin

The processing for the FLMD0 pin differs from that of the target device.

Table 4-1. FLMD0 Pin Processing

| Item | FLMD0 Pin Processing | | |
|---------------|---|--|--|
| Target device | Protection resistance: 4.5 kΩ (TYP.) | | |
| | Pull-up/pull-down resistors: 10 k Ω (MIN.), 20 k Ω (TYP.), 100 k Ω (MAX.) | | |
| IECUBE | Protection resistance: 4.7 kΩ (TYP.) | | |
| | Pull-up/pull-down resistors: 26.73 k Ω (MIN.), 27 k Ω (TYP.), 27.27 k Ω (MAX.) | | |

4.1.7 Power-on-clear (POC) voltage value

The power-on-clear (POC) voltage value differs from that of the target device.

Table 4-2. Power-on-clear (POC) voltage value

| Item | | MIN. | TYP. | MAX. |
|---------------|------|--------|--------|--------|
| Target device | VPOR | 1.52 V | 1.61 V | 1.70 V |
| | VPDR | 1.50 V | 1.59 V | 1.68 V |
| IECUBE | VPOR | _ | 1.65 V | _ |
| | VPDR | _ | 1.55 V | _ |

4.1.8 TTL input buffer characteristics

If the port input mode register (PIM) is used to set the input of a pin that can be set for the TTL buffer to the TTL level, the high-level input voltage characteristics differ between the target device and emulator. See Table 4-3 for details.

The following pins can be set for the TTL buffer.

Target pins: P10, P11, P14, P15, P75, P76

Table 4-3. High-Level Input Voltage Characteristics

| Item | Conditions | MIN. |
|------------------------|---------------------------------|-------|
| Target device | 4.0 V ≤ V _{DD} ≤ 5.5 V | 2.2 V |
| | 2.7 V ≤ V _{DD} < 4.0 V | 2.0 V |
| | 1.8 V ≤ V _{DD} < 2.7 V | 1.6 V |
| IECUBE ^{Note} | 1.8 V ≤ V _{DD} ≤ 5.5 V | 2.0 V |

Note Use CMOS input if VDD is 2.0 V or less.

4.1.9 AVREF voltage

If AVREFP is 2 V or less, the input voltages of AVREFP and AVREFP differ between the target device and the emulator. See Table 4-4 for details.

Table 4-4. AVREF Voltage

| Ite | em | MAX. | MIN. |
|-------------------------|--------|----------------------|------------|
| Target | AVREFP | AVDDO | Note1 |
| device | AVREFM | Note1 (1.0 to 1.4 V) | 0 V |
| IECUBE ^{Note2} | AVREFP | AVDDO | AVREFP - 1 |
| | AVREFM | AVREFM - 1 | 0 V |

Notes 1. AVREFP – AVREFM > AVDD0/2 must be satisfied.

2. If $AV_{DD0} \le 2 \text{ V}$, $AV_{REFM} < AV_{REFP} - 1 \text{ must be satisfied.}$

<R> 4.1.10 P121 and P122 Pins

The input characteristics of the P121 and P122 pins differ between the target device and emulator.

Table 4-5. Input Characteristics of P121 and P122 Pins

| Item | Input Characteristics of P121 and P122 Pins | |
|---------------|---|--|
| Target device | Schmitt input | |
| IECUBE | CMOS input | |

APPENDIX REVISION HISTORY

Revisions up to the previous edition are shown below. The "Applied to" column indicates the chapter in each edition to which the revision was applied.

| Edition | Description | Applied to |
|-------------|--|------------|
| 2nd edition | Addition of Caution in Table 1-1. QB-78K0RLX3 Hardware Specifications | CHAPTER 1 |
| | Change of Description in Figure 1-1. System Configuration | GENERAL |
| | Addition of the Remarks 4, 5, 6 in 1.3 System Configuration | |
| | Change of Description in Table 1-3. Adapters and Connectors for Each Target Device | |
| | Change of Description in 1.5 Package Contents | |
| | Addition of 1.6 AC Adapter for IECUBE | |
| | Addition of 4.1.10 P121 and P122 Pins | CHAPTER 4 |
| | | CAUTIONS |

[MEMO]

[MEMO]

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