



dsPIC33CK256MP506
Digital Power
Plug-In Module (PIM)
User's Guide

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Preface

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All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our website (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a “DS” number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is “DSXXXXXXXXXA”, where “XXXXXXXX” is the document number and “A” is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the dsPIC33CK256GS806 Digital Power Plug-In Module (PIM). Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Recommended Reading
- The Microchip Website
- Product Change Notification Service
- Customer Support
- Document Revision History

DOCUMENT LAYOUT

This document describes how to use the dsPIC33CK256MP506 Digital Power PIM as a development tool to emulate and debug firmware on a target board. The document is organized as follows:

- **Chapter 1. “Overview”** — This chapter introduces the dsPIC33CK256MP506 Digital Power PIM and provides a brief overview of its various features.
- **Appendix A. “Board Layout and Schematics”** — This appendix presents the schematics and the board layouts for the dsPIC33CK256MP506 Digital Power PIM.
- **Appendix B. “Bill of Materials (BOM)”** — This appendix presents the Bill of Materials for the dsPIC33CK256MP506 Digital Power PIM.

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	MPLAB® IDE User's Guide
	Emphasized text	...is the <i>only</i> compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	<u>File>Save</u>
Bold characters	A dialog button	Click OK
	A tab	Click the Power tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	#define START
	Filenames	autoexec.bat
	File paths	c:\mcc18\h
	Keywords	_asm, _endasm, static
	Command-line options	-Opa+, -Opa-
	Bit values	0, 1
	Constants	0xFF, 'A'
Italic Courier New	A variable argument	<i>file.o</i> , where <i>file</i> can be any valid filename
Square brackets []	Optional arguments	mcc18 [options] <i>file</i> [options]
Curly brackets and pipe character: { }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}
Ellipses...	Replaces repeated text	var_name [, var_name...]
	Represents code supplied by user	void main (void) { ... }

RECOMMENDED READING

This user's guide describes how to use the dsPIC33CK256MP506 Digital Power PIM. Other useful document(s) are listed below. The following Microchip document(s) are recommended as supplemental reference resources.

- “**dsPIC33EPXXG70X/80X Family Data Sheet**” (DS70005258) is available for download from the Microchip website (www.microchip.com)

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- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip consultant program member listing
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- Distributor or Representative
- Local Sales Office
- Corporate Application Engineer (CAE)
- Embedded Solutions Engineer (ESE)

Customers should contact their distributor, representative or Embedded Solutions Engineer (ESE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the website at:

<http://www.microchip.com/support>.

DOCUMENT REVISION HISTORY

Revision A (October 2018)

This is the initial release of this document.

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NOTES:

Chapter 1. Overview

1.1 INTRODUCTION

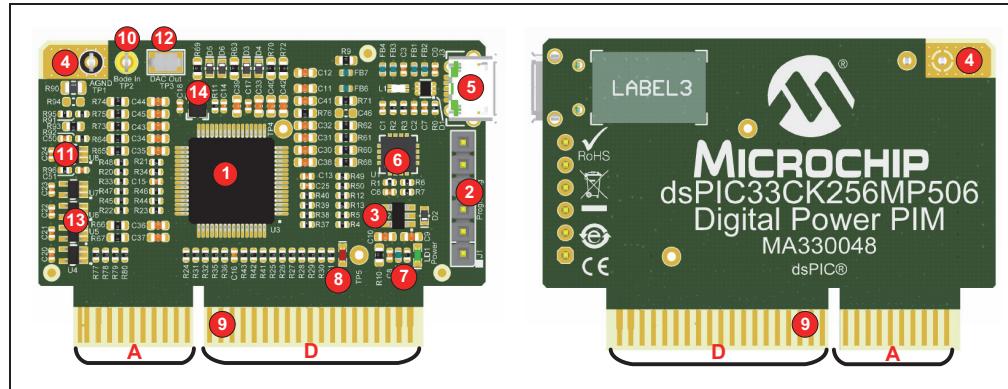
The dsPIC33CK256MP506 Digital Power Plug-In Module (DP PIM) is a demonstration board that, in conjunction with different power boards, showcases the Microchip dsPIC33CK256MP506 16-bit Digital Signal Controller (DSC) features. The DP PIM provides access to the dsPIC33CK256MP506 analog inputs, the Digital-to-Analog Converter (DAC) output, the Pulse-Width Modulator (PWM) outputs and the General Purpose Input and Output (GPIO) ports.

The Microchip series of DP PIMs for digital power share the same pinout at the mating socket. However, these PIM cards show slightly different performing characteristics.

1.2 FEATURES

The dsPIC33CK256MP506 DP PIM has the following features, as shown in Figure 1-1:

FIGURE 1-1: dsPIC33CK256MP506 DP PIM



1. Microchip dsPIC33CK256MP506 16-Bit Digital Signal Controller (64-pin TQFP package).
2. ICSP™ programming header (6-pin, 2.54 mm header).
3. On-board LDO with Power Good (PG) function.
4. Solder pad for ground connection.
5. Micro USB connector.
6. MCP2221A USB to UART/I²C serial converter.
7. Power indicator LED (Green).
8. User LED (Red).
9. Board edge connection interface for analog inputs/outputs, PWM outputs and GPIO ports.
10. Analog input with op amp buffer via test point loop connector; can be used for Bode plot measurements.
11. Op amp buffer for Bode input.
12. Test point loop for DAC output.
13. Op amp buffers for shared ADC inputs.
14. MEMS oscillator.

Board dimensions are: 51 mm (length) x 38.5 mm (width).

1.2.1 Test Points

Table 1-1 lists the test points on the dsPIC33CK256MP506 DP PIM.

TABLE 1-1: TEST POINTS

Test Point Name	Function/Description
TP1, TP2	Bode Measurement Signal Insert Point
TP3	RA3_DAC1_OUT: Digital-to-Analog Converter Output
TP4	Test Point for Debug (right upper corner of the MCU)
TP5	General Purpose Test Point Connected to Port RD15 together with LD2 (Red LED) (near to the right bottom corner beside LD2)

1.2.2 Electrical Characteristics

Table 1-2 shows the electrical characteristics of the dsPIC33CK256MP506 DP PIM.

TABLE 1-2: ELECTRICAL CHARACTERISTICS

Parameter	Value
Input Voltage Range	3.6 VDC to 6.3 VDC
Current Consumption	<100 mA
Power Dissipation	<0.6W max.
Operating Temperature Range	-40°C to +85°C

1.2.3 Analog and Digital Signals

The dsPIC33CK256MP506 DP PIM ensures good signal integrity and provides all signals needed to control a power train. These signals are divided into two main sections: Analog, marked with A; Digital, marked with D (see Figure 1-1).

1. Analog Section

The analog section is located at the left connector side (smaller section in Figure 1-1). It consists of 17 signals, all referenced to analog ground. These lines are split into four subsections:

- High-Speed Comparator Inputs: RC filtered with corner frequency of 10 MHz and maximum signal rise/fall time of 33 ns. These lines are designed to be used with on-chip comparators for signal zero-cross detections, etc.
- High-Speed ADC Inputs: RC filtered with corner frequency of 2 MHz and maximum signal rise/fall time of 180 ns. These lines are connected to the Track-and-Hold (T&H) circuitry of the dedicated ADC inputs and to the Sample-and-Hold (S&H) circuitry of the shared ADC inputs.
- High-Speed ADC Inputs: Buffered through operational amplifiers. RC filtered with corner frequency of 1 MHz and maximum signal rise/fall time of 347 ns. These lines are connected to the Sample-and-Hold (S&H) circuitry of the shared ADC inputs.
- Medium Speed ADC Inputs: RC filtered with corner frequency of 190 kHz and maximum signal rise/fall time of 1.8 μ s. These lines are connected to the Sample-and-Hold (S&H) circuitry of the shared ADC inputs. The different RC filtering is needed because of the significant difference between T&H and S&H circuitry, and the sampling times.

2. Digital Section

The digital section is located at the right connector side (larger section in [Figure 1-1](#)). It consists of 31 signals, all referenced to digital ground. These lines are split into four subsections:

- High-Speed PWM Outputs: Each line has a 75Ω series resistance.
- Medium Speed GPIO: Each line has a 270Ω series resistance.
- Programming/Debugging Lines: Each line has a 100Ω series resistance.
- Communication Lines (I^2C): Each line has a 100Ω series resistance.

Note: RC filtering and series resistance are needed for good signal integrity, and for reducing EMI issues. Hence, the board can be used for development purposes under frequent plug-in/out cycles. This decoupling also increases robustness in case of accidental shorts and EMC issues.

1.2.4 dsPIC33CK256MP506 DP PIM – PCB Edge Connector

The dsPIC33CK256MP506 DP PIM has an edge connector compatible with any application board that provides a mating socket.

The mating socket type is Samtec, Inc.: MECF-30-01-L-DV-WT.

1.3 UART COMMUNICATION

The on-board USB to UART serial bridge enables easy serial connection to PCs. The USB port can provide power to the digital power PIM and allows the user to communicate with the dsPIC® Digital Signal Controller (DSC).

1.4 LOW-FREQUENCY BODE PLOT MEASUREMENTS

The dsPIC33CK256MP506 device, with the additional on-board circuitry, is able to perform Bode plot measurements without the need for an isolation transformer. The transformer might be difficult to use if the injecting signal tends to be at a very low frequency (for instance, in case of Power Factor Correction (PFC) applications).

Perform the following steps:

1. Solder the 150Ω resistor from position R74 to R94.
2. Run the power stage in Open-Loop mode with a fixed duty cycle.
3. Connect the Bode 100 AC output across TP2 and TP1. The on-board operational amplifier is adding a $V_{DD}/2$ (1.65V) offset. In this case, no injection transformer is needed.
4. Connect RA3_DAC1_OUT to CH2 of the Bode 100.
5. Use the AN18 input to sample the signal from Bode 100 in every PWM cycle at Frequency Switching (FSW) (action in firmware is needed).
6. Remove the $V_{DD}/2$ offset to regain a signal with no DC value (action in firmware is needed).
7. Add sampled AC signal to the nominal duty cycle (PDCx) (action in firmware is needed).
8. Use a second dedicated ADC core input (ANx) to sample the output of the plant at FSW. The output can be:
 - Output voltage
 - Average coil current sampled at $T_{ON}/2$, where T_{ON} is the switch-on time.
9. Duty cycle input and plant output are converted into an analog signal using RA3_DAC1_OUT.

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Measured transfer function is the plant (Power Stage + Digital Modulator), after scaling and ADC sampling, versus digital duty cycle input (PDCx).

Note: Due to run-time delays of Sample-and-Hold circuits and conversion time of ADC and DAC, this measurement is only recommended for low-frequency measurements (a maximum of two decades below sampling frequency).

Figure 1-2 and Figure 1-3 show measuring procedure examples for plant and closed-loop measurements.

FIGURE 1-2: MEASURING PROCEDURE FOR PLANT MEASUREMENT

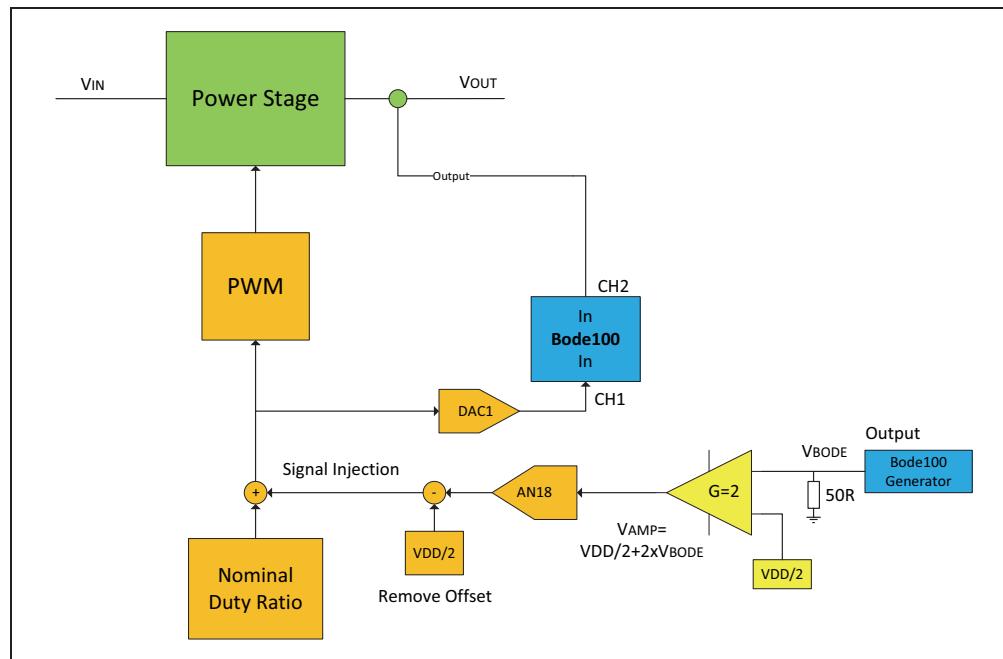
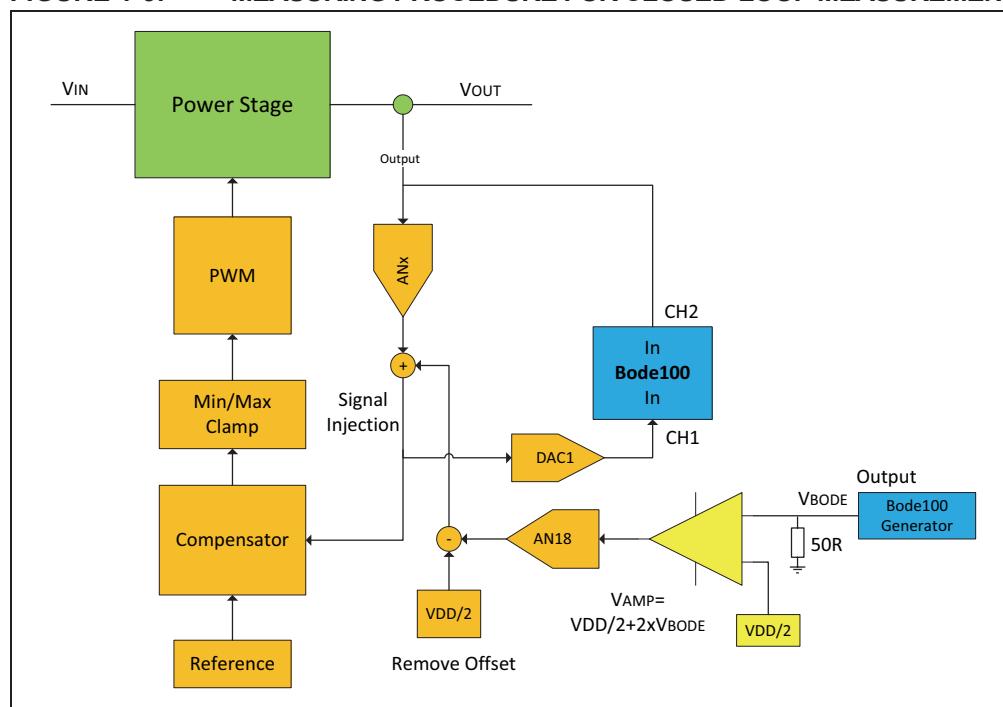


FIGURE 1-3: MEASURING PROCEDURE FOR CLOSED-LOOP MEASUREMENT

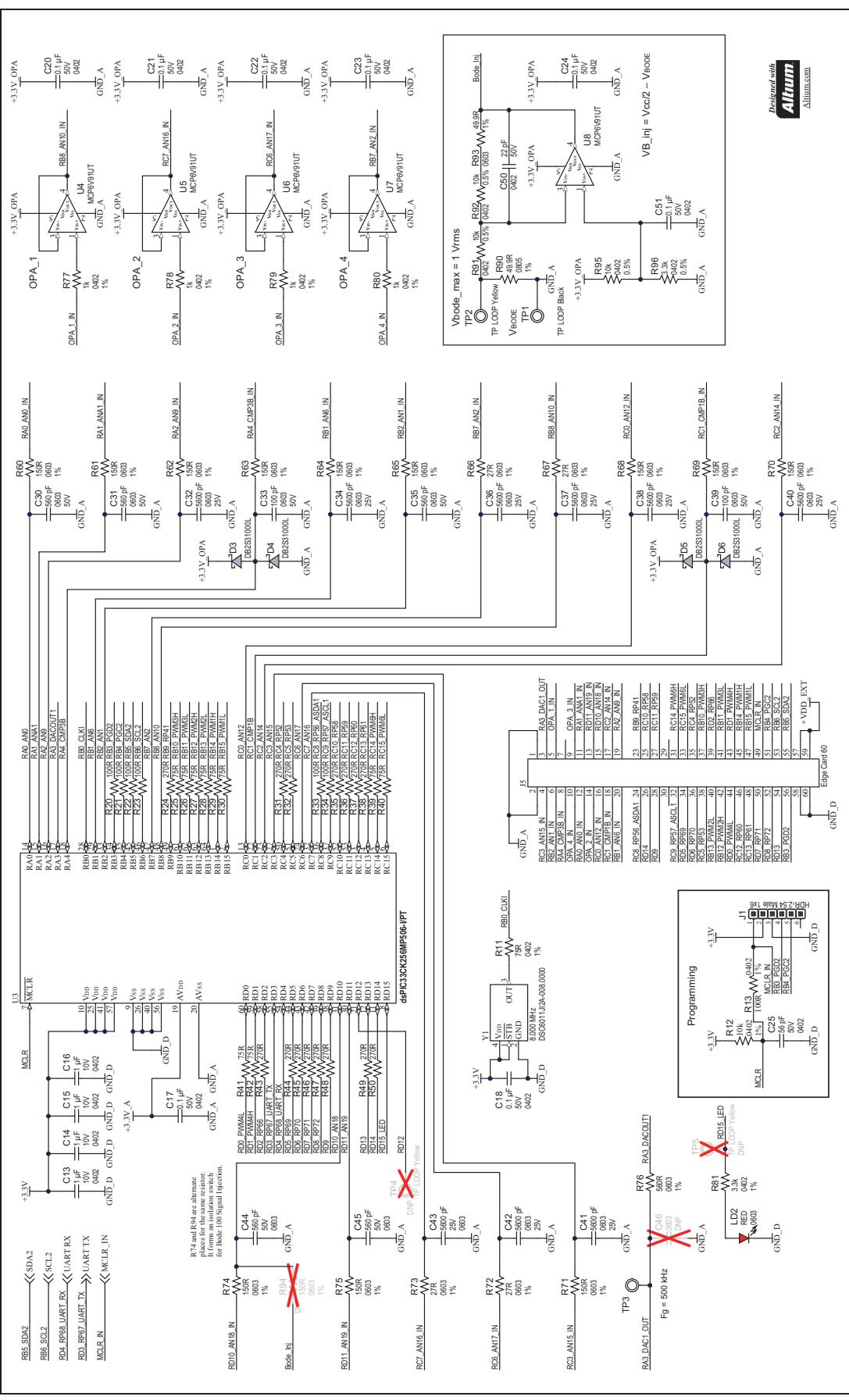


A.2 BOARD SCHEMATICS

Figure A-1 and Figure A-2 show the board schematics.

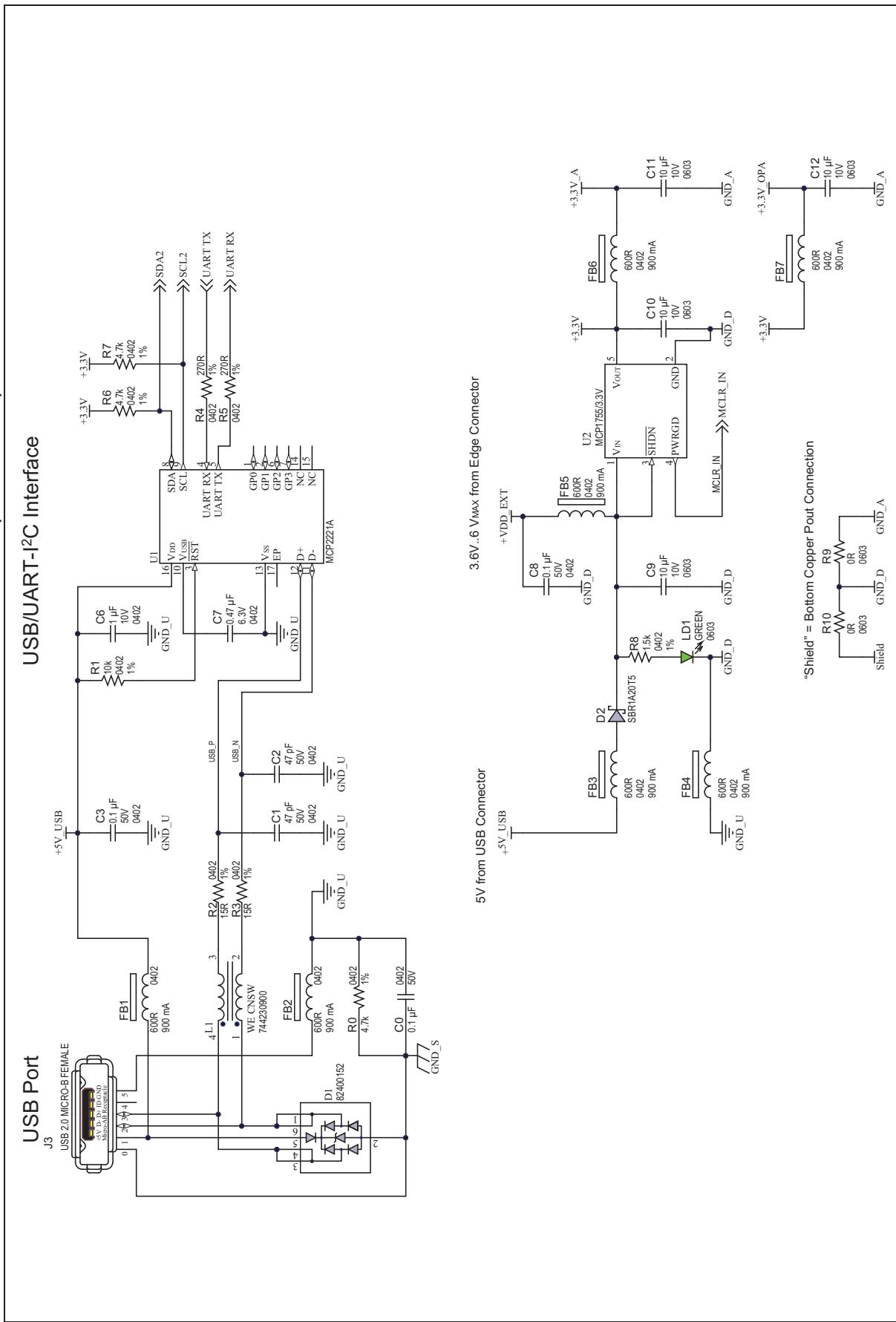
FIGURE A-1:

dsPIC33CK256MP506 DIGITAL POWER PIM SCHEMATIC REV. 1.0 (PAGE 1 OF 2)



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FIGURE A-2: dsPIC33CK256MP506 DIGITAL POWER PIM SCHEMATIC REV. 1.0 (PAGE 2 OF 2)

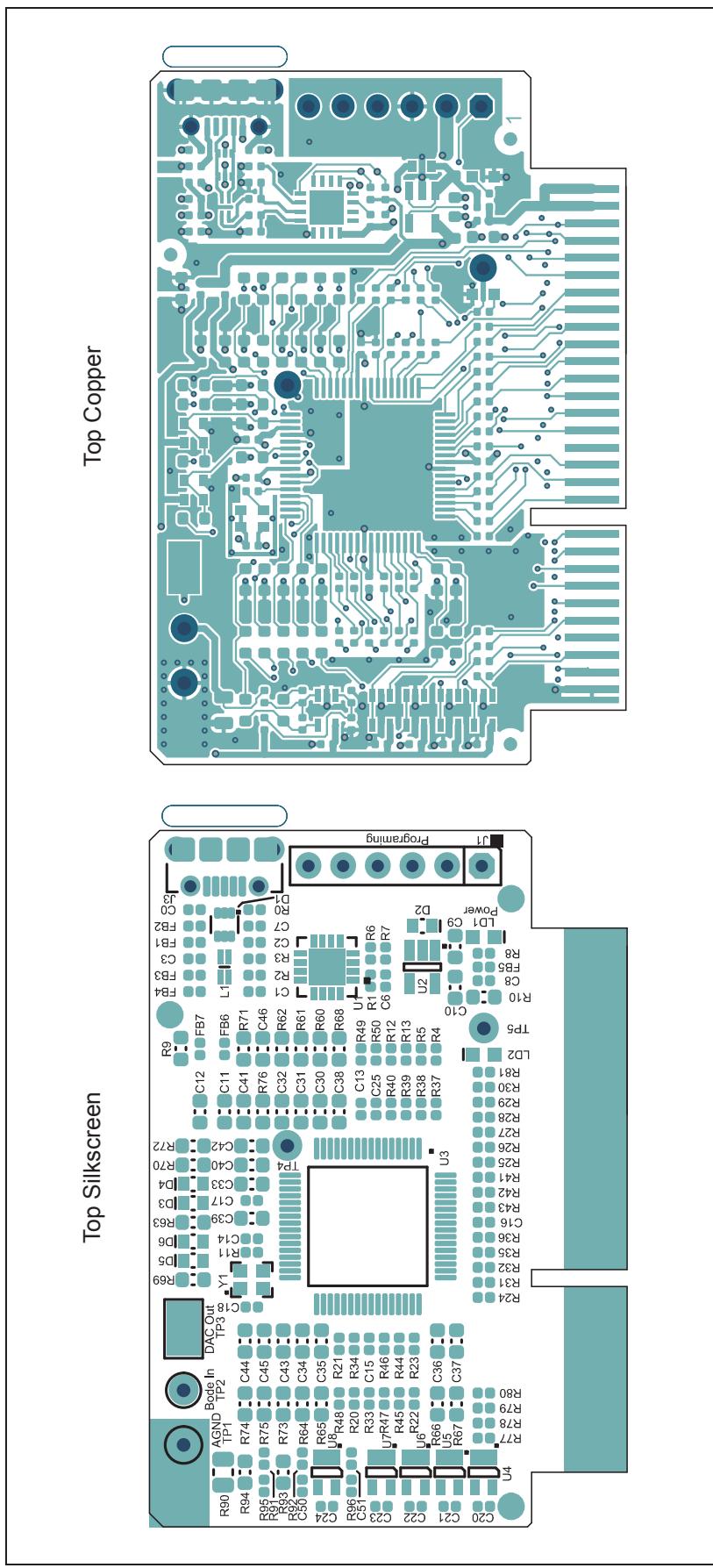


Board Layout and Schematics

A.3 PCB LAYOUT

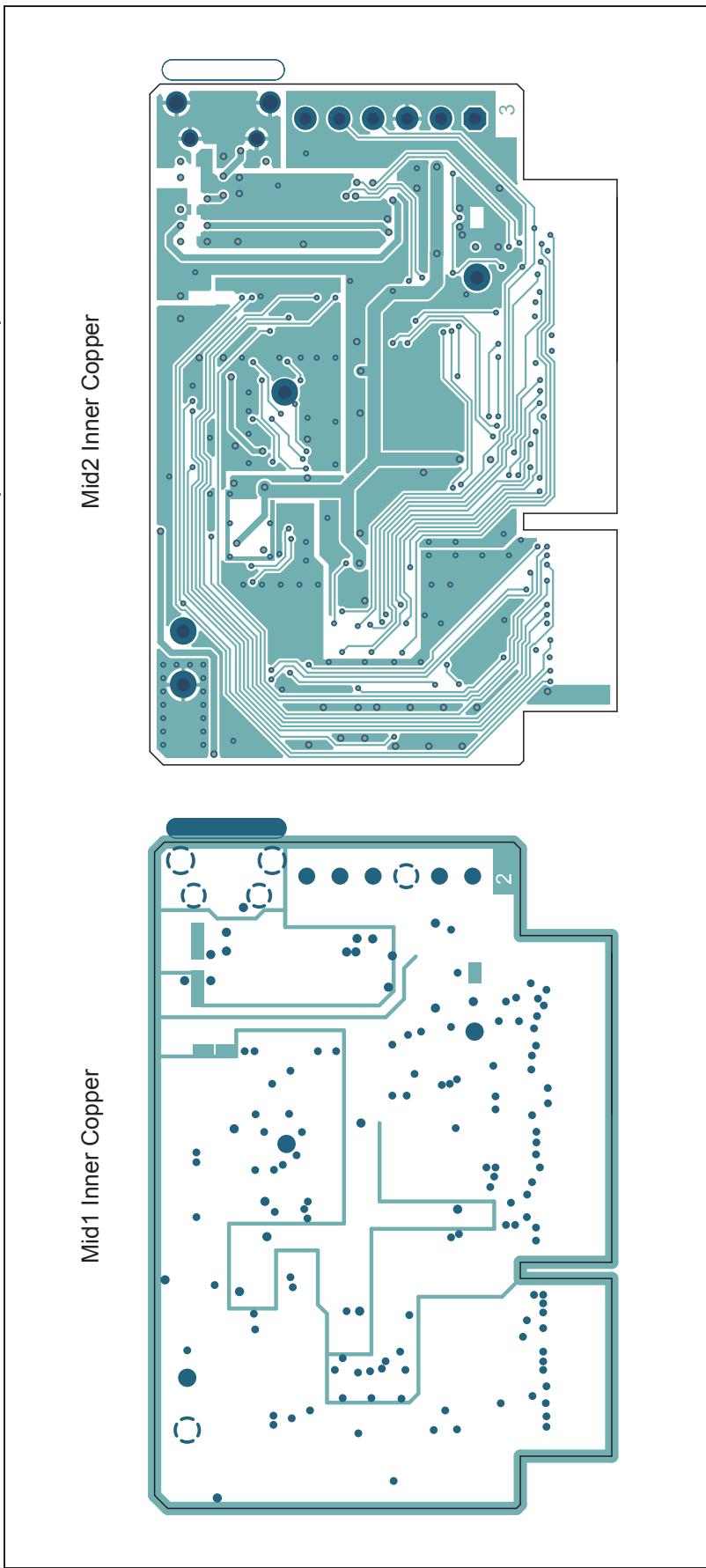
The dsPIC33CK256MP506 DP PIM is a four-layer FR4, 1.55 mm, Plated-Through-Hole (PTH) PCB construction. Figure A-3 through Figure A-5 illustrate the PCB layers and Figure A-6 shows the assembly drawings of the dsPIC33CK256MP506 DP PIM.

FIGURE A-3: dsPIC33CK256MP506 DIGITAL POWER PIM TOP SILKSCREEN AND TOP COPPER

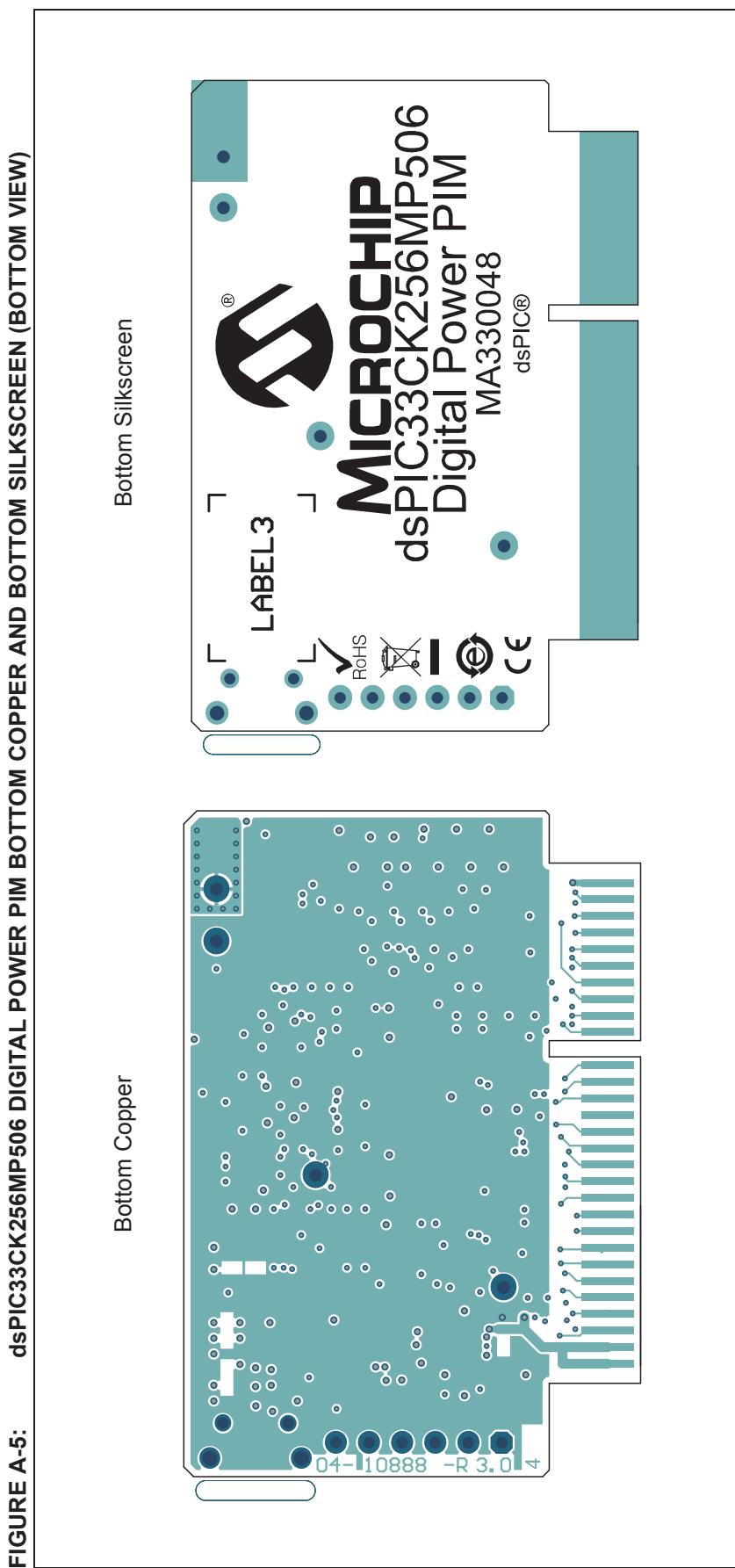


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FIGURE A-4: dsPIC33CK256MP506 DIGITAL POWER PIM MID1 AND MID2 INNER COPPER (BOTTOM VIEW)

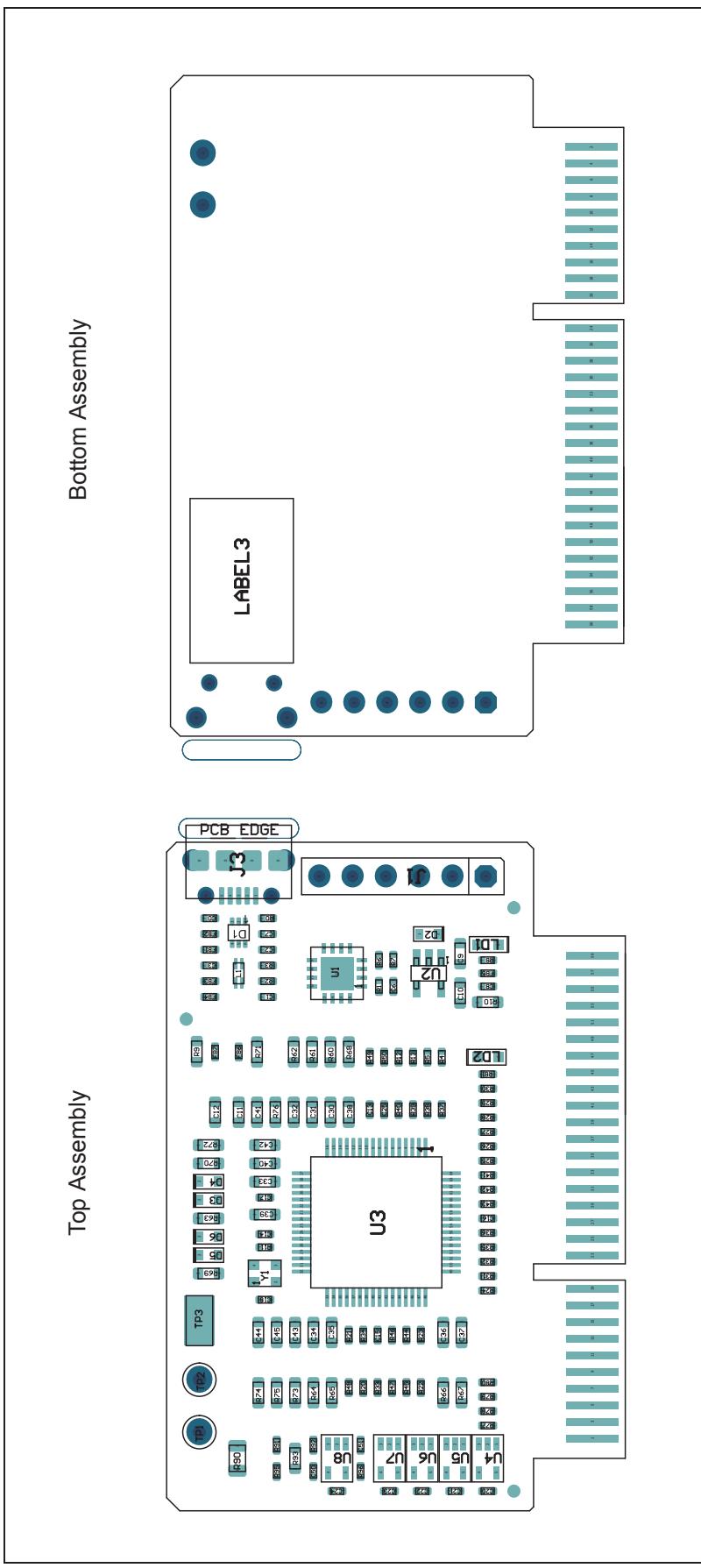


Board Layout and Schematics



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FIGURE A-6: dsPIC33CK256MP506 DIGITAL POWER PIM TOP AND BOTTOM ASSEMBLY



Bill of Materials (BOM)

TABLE B-1: dsPIC33CK256MP506 DIGITAL POWER PIM BILL OF MATERIALS (BOM) (CONTINUED)

Qty.	Designator	Description	Manufacturer	Manufacturer Part Number
1	R96	Resistor TKF, 3.3k, 0.5%, 1/16W, SMD, 0402	Panasonic® - ECG	ERA-2AED332X
1	TP1	Misc, Test Point Multipurpose Mini, Black	Keystone Electronics Corp.	5001
1	TP2	Misc, Test Point PC Mini, 0.040", D, Yellow	Keystone Electronics Corp.	5004
1	TP3	Connector, Test Point, TAB, Silver, Mini, 3.8x2.03, SMD	Keystone Electronics Corp.	5019
1	U1	Microchip Interface, USB, I ² C/UART, MCP2221A-I/ML, QFN-16	Microchip Technology Inc.	MCP2221A-I/ML
1	U2	Microchip Analog LDO, 3.3V, MCP1755T-3302E/OT, SOT-23-5	Microchip Technology Inc.	MCP1755T-3302E/OT
1	U3	Microchip MCU, 16-Bit, 100 MIPS, 256k, 24k, dsPIC33CK256MP506-I/PT, TQFP-64	Microchip Technology Inc.	dsPIC33CK256MP506-I/PT
5	U4, U5, U6, U7, U8	Microchip Analog Op Amp, 1-Ch, 10 MHz, MCP6V91UT-E/LTYCT-ND, SC-70-5	Microchip Technology Inc.	MCP6V91UT-E/LTY
1	Y1	Microchip Clock Oscillator, Single, 8.000 MHz, DSC6011JI2A-008.0000, VDFN-4	Microchip Technology Inc.	DSC6011JI2A-008.0000

