Device Errata
Skywire® Modem

NimbeLink // Products
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1 Applies to NimbeLink Part Numbers

Affected Part Numbers
NL-SW-1xRTT-V
NL-SW-1xRTT-A
NL-SW-EVDO-V
NL-SW-EVDO-A
NL-SW-GPRS
NL-SW-HSPAP
NL-SW-HSPAPE
NL-SW-HSPAPG
NL-SW-LTE-TSVG
NL-SW-LTE-TEUG
NL-SW-LTE-TNAG

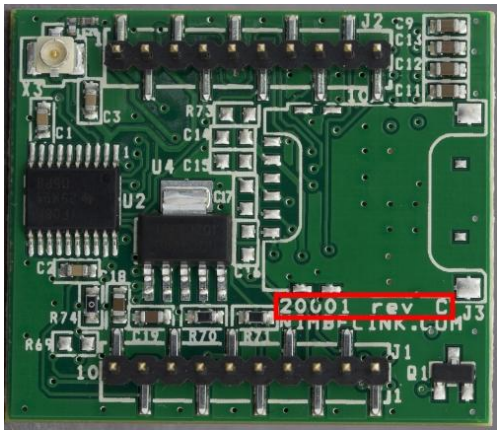
2 Applications Affected

This Errata affects applications requiring use of the UART CTS hardware flow control output from the Skywire modem. The UART CTS signal (Pin 12 on the Skywire interface) is not connected on NimbeLink PCBs prior to PCB revision F. This Errata does not affect your design if any of the following are true:

1) Skywire PCB is revision F or higher
2) USB is used for communication instead of UART
3) Hardware design does not connect to Skywire CTS pin
4) Design does not implement hardware flow control in hardware and/or software
5) Software design implements software flow control (XON/XOFF)

3 How to Identify PCB Revision

The PCB revision is found on the bottom side of the modem between J1 (Skywire interface connector) and J3 (Micro-SIM socket). The revision is written in white silkscreen and is of the form 20001 rev X.



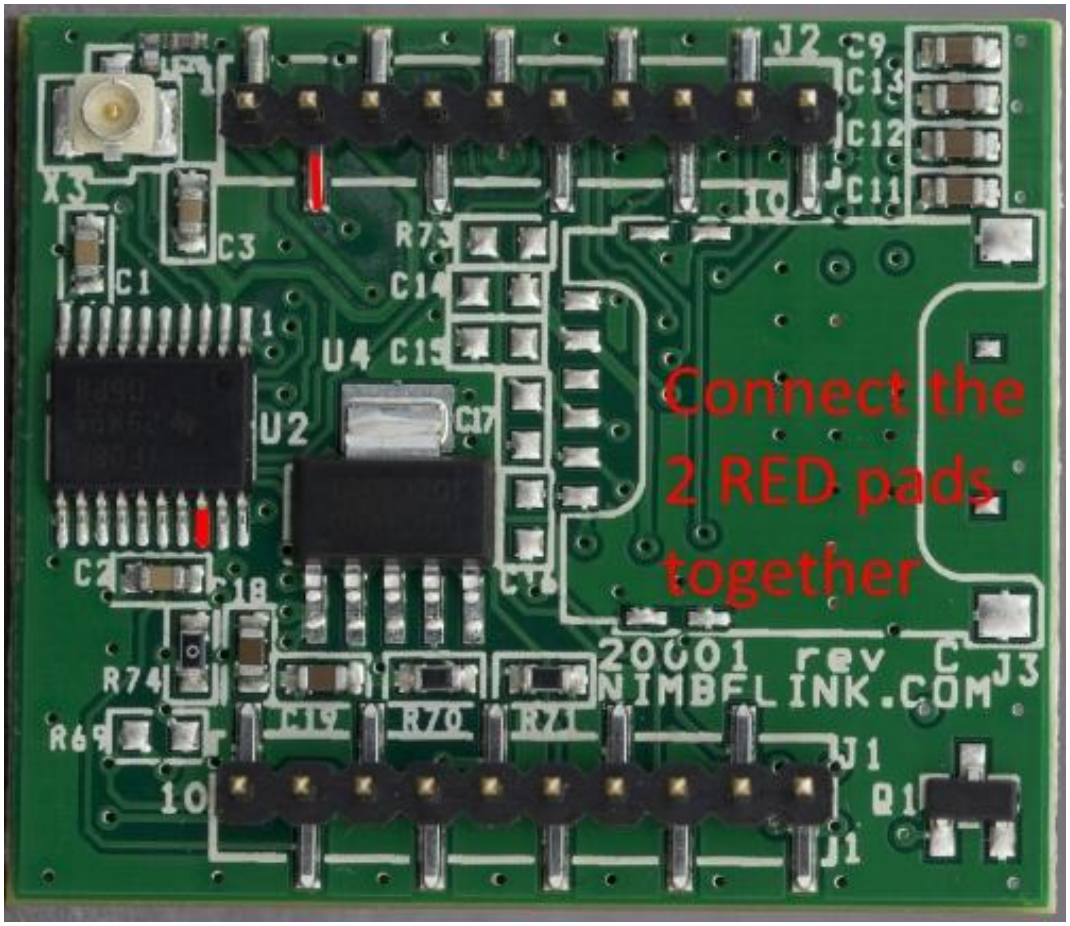
4 Workarounds

For customers that have affected product, these alternatives may permanently alleviate the effects of the Errata in your application.

1) Use USB interface instead of UART
2) Use software flow control (XON/XOFF) instead of hardware flow control
3) Modify existing modem to correct for CTS Errata (see section 5)

5 Board Rework

The Errata can be repaired on affected product by soldering a wire to the underside of the modem. The wire must connect Skywire interface pin 12 to U2 pin 18. Adding this wire will not invalidate the product warranty, however, poor workmanship can damage the modem and invalidate the warranty.



6 Contact

For further information please contact NimbeLink Technical Support: product.support@nimbelink.com.

7 Version Information

A	RGL	Initial draft		05/28/2015
B				00/00/00

