

T25

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## Solid Tantalum SMD Capacitors TANTAMOUNT<sup>™</sup>, Hi-Rel COTS, Low ESR, Metal Case



### **PERFORMANCE CHARACTERISTICS**

**Operating Temperature:** -55 °C to +125 °C (above 85 °C, voltage derating is required) **Capacitance Range:** 22  $\mu$ F to 330  $\mu$ F **Capacitance Tolerance:**  $\pm$  10 %,  $\pm$  20 % **Voltage Rating:** 16 V<sub>DC</sub> to 50 V<sub>DC</sub>

### FEATURES

- High reliability; burn-in at a minimum of rated DC voltage for a minimum of 40 h
- Surge current testing per MIL-PRF-55365 option available
- Low ESR
- Lead (Pb)-free terminations available (tin / lead terminations are under development)
- · Mounting: surface mount
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details





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<b>RATINGS AND</b>	RATINGS AND CASE CODES				
μF	16 V	20 V	25 V	35 V	50 V
22					D
33					
47					
68				D <sup>(1)</sup>	
100			D <sup>(1)</sup>		
150					
220		D (1)			
330	D				

Note

<sup>(1)</sup> Preliminary values, contact factory for availability



STANDARD RA	TINGS					
CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT +25 °C (μΑ)	MAX. DF AT +25 °C (%)	MAX. ESR AT +25 °C 100 kHz (mΩ)	MAX. RIPPLE 100 kHz I <sub>RMS</sub> (A)
		16 V <sub>DC</sub> AT +85 °C; 10	V <sub>DC</sub> AT +125 °C			
330	D	T25D337(1)016(2)(3)(4)	52.8	14	180	1.5
		20 V <sub>DC</sub> AT +85 °C; 13	V <sub>DC</sub> AT +125 °C			
220	D <sup>(1)</sup>	T25D227(1)020(2)(3)(4)		In develop	oment	
		25 V <sub>DC</sub> AT +85 °C; 17	V <sub>DC</sub> AT +125 °C			
100	D <sup>(1)</sup>	T25D107(1)025(2)(3)(4)		In develop	oment	
		35 V <sub>DC</sub> AT +85 °C; 23	V <sub>DC</sub> AT +125 °C			
68	D <sup>(1)</sup>	T25D686(1)035(2)(3)(4)		In develop	oment	
		50 V <sub>DC</sub> AT +85 °C; 33	V <sub>DC</sub> AT +125 °C			
22	D	T25D226(1)050(2)(3)(4)	11	6	500	0.9

### Notes

Part number definitions:

(1) Capacitance tolerance: K, M

(2) Termination and packaging: C, H, E, L

(3) Reliability level: S, Z

(4) Surge current: A, B, S, C, Z

<sup>(1)</sup> Rating in development, contact factory for availability

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VOLTAGE DERATING GUIDELINES (below +85 °C	
VOLTAGE RAIL (V)	CAPACITOR RATED VOLTAGE (V)
3.3	6.3
5	10
10	20
12	25
15	35
≥ 24	50 or series configuration

#### Note

• For more information about recommended voltage derating see technical note <u>www.vishay.com/doc?40246</u>

CARRIER TAPE DIMENSIONS in inches [millimeters]					
ТҮРЕ	CASE CODE	TAPE WIDTH W (mm)	P <sub>1</sub>	K <sub>0</sub> MAX.	B <sub>1</sub> MAX.
T25	D	16	0.476 ± 0.004 [12.0 ± 0.1]	0.3 [7.86]	0.45 [11.3]

POWER DISSIPATION				
CASE CODE	MAXIMUM PERMISSIBLE POWER DISSIPATION AT +25 $^\circ$ C (W) in Free Air			
D	0.408			

STANDARD PACKAGING QUANTITY				
CASE CODE	UNITS PER REEL			
CASE CODE	7" FULL REEL	7" HALF REEL		
D	100	50		

ITEM	PERFORMANCE CHARACTERISTICS		
Category temperature range	-55 °C to +85 °C (to +125 °C with voltage derating	)	
Capacitance tolerance	$\pm$ 20 %, $\pm$ 10 %, tested via bridge method, at +25	°C, 120 Hz	
Dissipation factor	Limit per Standard Ratings table. Tested via bridge	e method, at 25 °C, 120 Hz.	
ESR	Limit per Standard Ratings table. Tested via bridge	e method, at 25 °C, 100 kHz.	
Leakage current	After application of rated voltage applied to capacitors for 5 min using a steady source of power with 1 k $\Omega$ resistor in series with the capacitor under test, leakage current at 25 °C is not more than described in Standard Ratings table. Note that the leakage current varies with temperature and applied voltage. See graph below for the appropriate adjustment factor.		
Capacitance change by temperature	+12 % max. (at +125 °C) +10 % max. (at +85 °C) -10 % max. (at -55 °C)	For capacitance value > 300 μF +20 % max. (at +125 °C) +15 % max. (at +85 °C) -15 % max. (at -55 °C)	
Reverse voltage	Capacitors are capable of withstanding peak voltages in the reverse direction equal to: 10 % of the DC rating at +25 °C 5 % of the DC rating at +85 °C Vishay does not recommend intentional or repetitive application of reverse voltage.		
Ripple current and temperature derating	For maximum permissible ripple current (I <sub>RMS</sub> ) or / and voltage (V <sub>RMS</sub> ) please refer to product datasheet and Guide to Application. If capacitors are to be used at temperatures above +25 °C, the permissible RMS ripple current or voltage shall be calculated using the derating factors: 1.0 at +25 °C 0.9 at +85 °C 0.4 at +125 °C		

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CAPACITOR ELECTRICAL PERFORMANCE CHARACTERISTICS				
ITEM	PERFORMANCE CHARACTERISTICS			
Maximum operating voltage	OPERATING TEMPERATURE			
	+85	5 °C	+125 °C	
	RATED VOLTAGE (V)	SURGE VOLTAGE (V)	RATED VOLTAGE (V)	SURGE VOLTAGE (V)
	16	20	10	12
	20	26	13	16
	25	32	17	20
	35	46	23	28
	50	65	33	40

#### Note

All information presented in this document reflects typical performance characteristics ٠

0.01

0.001

0 10 20 30 40 50 60



#### Notes

- At +25 °C, the leakage current shall not exceed the value listed in the Standard Ratings table
- At +85 °C, the leakage current shall not exceed 10 times the value listed in the Standard Ratings table
- At +125 °C, the leakage current shall not exceed 12 times the value listed in the Standard Ratings table

CAPACITOR PERFORMANCE CHARACTERISTICS				
ITEM	CONDITION	POST TEST PERFOR	MANCE	
Surge voltage	85 °C, 1000 successive test cycles at 1.3 of rated voltage in series with a 1 k $\Omega$ resistor at the rate of 30 s ON, 30 s OFF, MIL-PRF-55365	Capacitance change Dissipation factor Leakage current	Within ± 10 % of initial Not to exceed initial Not to exceed initial	
Life test at +85 °C	2000 h application of rated voltage at 85 °C, MIL-STD-202 method 108	Capacitance change Leakage current	Within $\pm$ 10 % of initial Not to exceed 125 % of initial	
Life test at +125 °C	1000 h application of 2/3 rated voltage at 125 °C, MIL-STD-202 method 108	Capacitance change Leakage current	Within ± 20 % of initial Not to exceed 125 % of initial	

Percent of Rated Voltage

70

80 90 100



CAPACITOR EN	CAPACITOR ENVIRONMENTAL CHARACTERISTICS				
ITEM	CONDITION	POST TEST PERFORI	MANCE		
Moisture resistance	MIL-STD-202, method 106, at rated voltage.	Capacitance change Cap. ≤ 600 µF Cap. > 600 µF Dissipation factor Leakage current	Within $\pm$ 10 % of initial value Within $\pm$ 20 % of initial value Initial specified value or less Initial specified value or less		
Thermal shock	Capacitors are subjected to 6 cycles per MIL-STD-202 method 107 of the following: -55 °C (+0 °C, -6 °C) for 30 min, then +25 °C (+3 °C, -3 °C) for 5 min, then +85 °C (+4 °C, -5 °C) for 40 min, then +125 °C (+4 °C, -0 °C) for 30 min, then +25 °C (+3 °C, -3 °C) for 5 min	Capacitance change Cap. Dissipation factor Leakage current	Within ± 15 % of initial Initial specified value or less Initial specified value multiplied by 12 or less		
Salt atmosphere (corrosion)	Test per MIL-202, method 101, condition B (48 h). 5 % salt solution applying.	exposed metallic surfa	e corrosion, = 90 % protection of ces by finish, markings legible, e terminal hardware or mounting.		

MECHANICAL PERFORMANCE CHARACTERISTICS				
TEST CONDITION	CONDITION	POST TEST PERFORMANCE		
Shear test	Apply a pressure load of 5 N for 10 s $\pm$ 1 s horizontally to the center of capacitor side body. AEC-Q200-006	There shall be no visual damage when viewed at 20 x magnification and the component shall meet the original electrical requirements.		
Vibration	MIL-STD-202, method 204, condition D, 10 Hz to 2000 Hz, 20 <i>g</i> peak	There shall be no mechanical or visual damage to capacitors post-conditioning.		
Shock (specified pulse)	MIL-STD-202, method 213, condition I, 100 <i>g</i> peak	Capacitance changeWithin ± 10 % of initialDissipation factorInitial specified value or lessLeakage currentInitial specified value or less		
		There shall be no mechanical or visual damage to capacitors post-conditioning.		
Resistance to soldering heat	MIL-STD-202, method 210, condition J, except with only one heat cycle.	Capacitance changeWithin ± 10 % of initialDissipation factorInitial specified value or lessLeakage currentInitial specified value or less		
		There shall be no mechanical or visual damage to capacitors post-conditioning.		
Solderability	MIL-STD-202, method 208, ANSI/J-STD-002, test B. Applies only to solder and tin plated terminations. Does not apply to gold terminations.	All terminations shall exhibit a continuous solder coating free from defects for a minimum of 95 % of the critical are of any individual lead.		
Resistance to solvent	MIL-STD-202, method 215	Marking has to remain legible, no degradation of the can material.		
Sleeving	MIL-PRF-39003, paragraph 3.22: apply a DC potential of 2000 V.	Maximum leakage of 20 $\mu$ A is allowed between the capacitor case and the fixture.		
Seal	MIL-STD-202, method 112, condition A or D	There shall be no visual leakage.		



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### **RECOMMENDED REFLOW PROFILES**

Capacitors should withstand reflow profile as per J-STD-020 standard

L C C C C C C C C C C C C C C C C C C C	Max. ramp-up rate = $3 \text{ °C/s}$ Max. ramp-down rate = $6 \text{ °C/s}$ max. Preheat area	
	Time 25 °C to peak TIME (s)	LEAD (Pb)-FREE ASSEMBLY
Preheat / soak		
Temperature min. (T <sub>s min.</sub> )	100 °C	150 °C
Temperature max. (T <sub>s max.</sub> )	150 °C	200 °C
Time ( $t_s$ ) from ( $T_s _{min.}$ to $T_s _{max.}$ )	60 s to 120 s	60 s to 120 s
Ramp-up		
Ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )	3 °C/s max.	3 °C/s max.
Liquidus temperature ( $T_L$ )	183 °C	217 °C
Time (t <sub>L</sub> ) maintained above $T_L$	60 s to 150 s	60 s to 150 s
Peak package body temperature (Tp)	220	250
Time $(t_p)$ within 5 °C of the specified classification temperature $(T_C)$	20 s	30 s
Time 25 °C to peak temperature	6 min max.	8 min max.
Ramp-down		
Ramp-down rate ( $T_P$ to $T_L$ )	6 °C/s max.	6 °C/s max.
Time 25 °C to peak temperature	6 min max.	8 min max.

PAD DIMENSIONS in inches [millimeters]				
			A A	
CASE CODE	A (MIN.)	B (NOM.)	С (NOM.)	D (NOM.)
D	0.276 [7]	0.178 [4.5]	0.079 [2]	0.433 [11]

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