

# AAP Gen 7 (TO-240AA) Power Modules Schottky Rectifier, 220 A



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	220 A			
$V_{R}$	30 V			
Package	AAP Gen 7 (TO-240AA)			
Circuit configuration	Two diodes common cathode			

#### **MECHANICAL DESCRIPTION**

The AAP Gen 7, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- · Low thermal resistance
- UL approved file E78996





#### **BENEFITS**

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- · High surge capability
- · Easy mounting on heatsink

#### **ELECTRICAL DESCRIPTION / APPLICATIONS**

The VS-VSKCS220/030 Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	220	А		
$V_{RRM}$		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	18 000	Α		
V <sub>F</sub>	110 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.57	V		
T <sub>J</sub>	Range	-55 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-VSKCS220/030	UNITS
Maximum DC reverse voltage	$V_{R}$	30	V
Maximum working peak reverse voltage	$V_{RWM}$	30	V



ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at T <sub>C</sub> = 110 °C, rectangular waveform		220	
forward current	per leg	I <sub>F(AV)</sub>			110	
Maximum peak one cycle non-repetitive surge current		I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	18 000	Α
			10 ms sine or 6 ms rect. pulse		2000	
Non-repetitive avalanche energ	У	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 15 A, L = 1 mH		99	mJ
Repetitive avalanche current		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		22	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub>	110 A	T <sub>J</sub> = 25 °C	0.59	V
Maximum forward voltage drop		220 A		0.78	
Maximum forward voltage drop		110 A	T <sub>J</sub> = 125 °C	0.57	
		220 A		0.82	
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	mA
iviaximum reverse leakage current		T <sub>J</sub> = 125 °C		650	IIIA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		7400	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	ge	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance junction to case per leg	,	$R_{thJC}$	DC operation	0.52	°C/W
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub>		0.1	C/VV
Approximate weight				75	g
Approximate weight				2.7	oz.
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the	4	Nm
	busbar		spread of the compound.	3	INIII
Case style			JEDEC®	TO-240AA co	mpatible

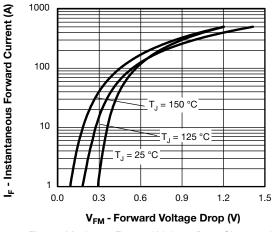


Fig. 1 - Maximum Forward Voltage Drop Characteristics

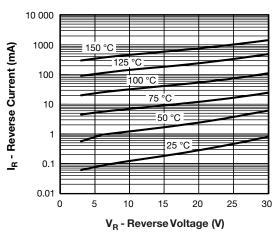


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

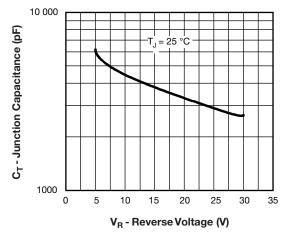


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

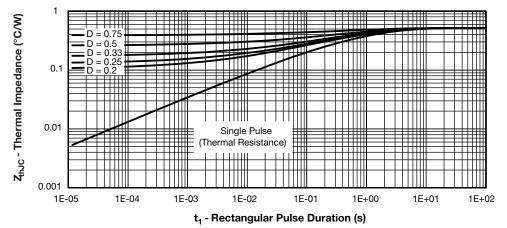


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

#### www.vishay.com

## Vishay Semiconductors

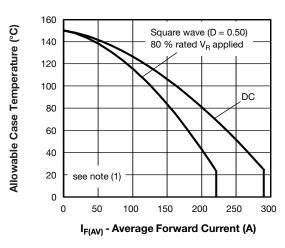


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

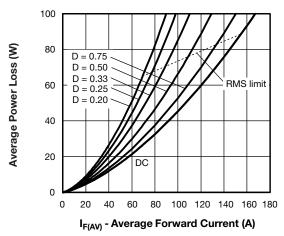
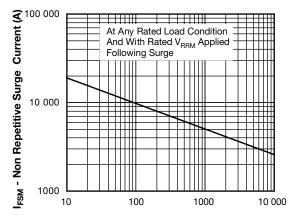


Fig. 6 - Forward Power Loss Characteristics



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

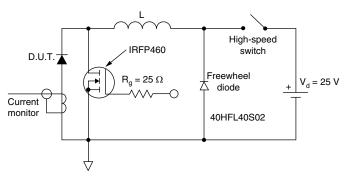


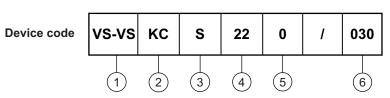
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



#### **ORDERING INFORMATION TABLE**



- VS-VS = Vishay Semiconductors product

2 - Circuit configuration:

KC = ADD-A-PAK - 2 diodes common cathode

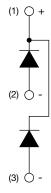
3 - S = Schottky diode

4 - Average rating (x 10)

5 - Product silicon identification

6 - Voltage rating (030 = 30 V)

#### **CIRCUIT CONFIGURATION**

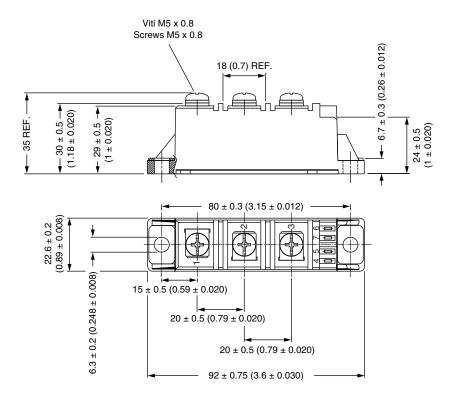


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			



## **ADD-A-PAK Generation VII - Diode**

#### **DIMENSIONS** in millimeters (inches)





## **Legal Disclaimer Notice**

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