



SCALE 2:1



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SC-59 CASE 318D-04 ISSUE H

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NOTES:

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.00	1.15	1.30	0.039	0.045	0.051
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.35	0.43	0.50	0.014	0.017	0.020
С	0.09	0.14	0.18	0.003	0.005	0.007
D	2.70	2.90	3.10	0.106	0.114	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	1.70	1.90	2.10	0.067	0.075	0.083
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.80	3.00	0.099	0.110	0.118

GENERIC **MARKING DIAGRAM**



= Specific Device Code XXX Μ = Date Code

= Pb-Free Package*

(*Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

STYLE 1:	STYLE 2:	STYLE 3:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE
2. EMITTER	2. N.C.	2. ANODE
3. COLLECTOR	3. CATHODE	3. CATHODE
Style 4:	Style 5:	STYLE 6:
Pin 1. Cathode	Pin 1. Cathode	PIN 1. ANODE
2. N.C.	2. Cathode	2. CATHODE
3. Anode	3. Anode	3. ANODE/CATHODE

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