TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (U-MOS IV)

TPCF8304

Notebook PC Applications Portable Equipment Applications

Low drain-source ON resistance: R_{DS} (ON) = 60 mΩ (typ.)

• High forward transfer admittance: |Yfs| = 5.9 S (typ.)

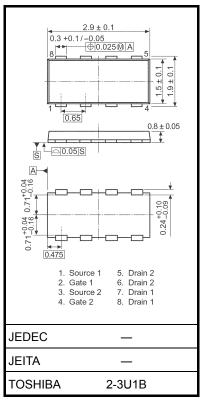
• Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$

• Enhancement model: $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_{D} = -1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

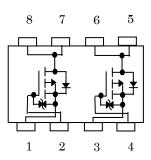
Cha	aracteristic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-30	V	
Drain-gate voltage	e voltage ($R_{GS} = 20 \text{ k}\Omega$) V_{DGR} -30				
Gate-source voltage	e	V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	-3.2	А	
Diam current	Pulse (Note 1)	I_{DP}	-30 -30 ±20	^	
Drain power	Single-device operation (Note 3a)	P _{D (1)}	1.35		
dissipation (t = 5 s) (Note 2a)	Single-device value at dual operation (Note 3b)	P _{D (2)}	1.12	w	
Drain power dissipation (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	P _{D (1)}	0.53		
	Single-device value at dual operation (Note 3b)	P _{D (2)}	0.33		
Single-pulse avala	nche energy (Note 4)	E _{AS}	0.67	mJ	
Avalanche current	lanche current I _{AR} -1.6				
Repetitive avalance Single-device value	E _{AR}	E _{AR} 0.11			
Channel temperatu	T _{ch}	150	°C		
Storage temperatu	T _{stg}	-55~150	°C		

Unit: mm



Weight: 0.011 g (typ.)

Circuit Configuration



Note: For Notes 1 to 5, see the next page.

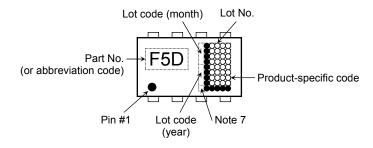
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Caution: This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

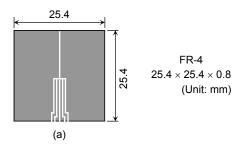
Chara	Symbol	Max	Unit		
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	Single-device operation (Note 3a)	R _{th (ch-a) (1)}	92.6	°C/W	
	Single-device value at dual operation (Note 3b)	R _{th (ch-a) (2)}	111.6	O/VV	
Thermal resistance, channel to ambient	Single-device operation (Note 3a)	R _{th (ch-a) (1)}	235.8	°C/W	
(t = 5 s) (Note 2b)	Single-device value at dual operation (Note 3b)	R _{th (ch-a) (2)}	378.8	C/VV	

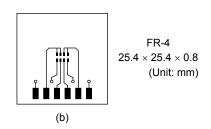
Marking (Note 6)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)





Note 3: a) The power dissipation and thermal resistance values shown are for a single device. (During single-device operation, power is applied to one device only.)

b) The power dissipation and thermal resistance values shown are for a single device. (During dual operation, power is evenly applied to both devices.)

Note 4: $V_{DD} = -24 \text{ V}$, $T_{ch} = 25 ^{\circ}\text{C}$ (initial), L = 0.2 mH, $R_G = 25 \Omega$, $I_{AR} = -1.6 \text{ A}$

Note 5: Repetitive rating; pulse width limited by max channel stemperature

Note 6: • to the lower left of the Part No. indicates Pin 1.

Note 7: A dot marking identifies the indication of product Labels.

Without a dot: [[Pb]]/INCLUDES > MCV

With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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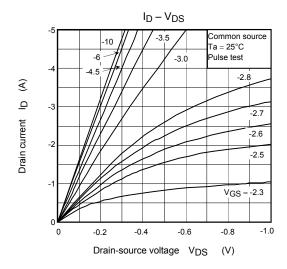
Electrical Characteristics (Ta = 25°C)

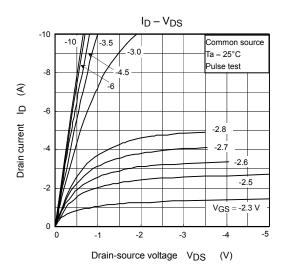
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curre	ent	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V		_	-10	μА
Drain-source breakdown voltage		V _{(BR) DSS}	$I_D = -10$ mA, $V_{GS} = 0$ V	-30	_	_	V
Dialii-source brea	akuowii voitage	V _{(BR)DSX}	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	v
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.5	_	-1.2	٧
Drain-source ON	resistance	R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -1.6 \text{ A}$	1	80	105	mΩ
Dialii-source ON	resistance	R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.6 \text{ A}$	_	60	72	11122
Forward transfer admittance		Y _{fs}	V _{DS} = -10 V, I _D = -1.6 A	2.9	5.9	_	S
Input capacitance	9	C _{iss}		_	600	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	-	60	_	pF
Output capacitance		C _{oss}		-	70	_	
Switching time	Rise time	t _r	V _{GS} 0 V I _D = -1.6 A V _{OUT} V	_	5.3	_	
	Turn-on time	t _{on}		_	12	_	ns
	Fall time	t _f		_	8.4	_	
	Turn-off time	t _{off}	$V_{DD} \simeq -15 \text{ V}$ Duty \leq 1%, $t_W =$ 10 μs	_	34	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -24 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -3.2 \text{ A}$	-	14	_	nC
Gate-source charge 1		Q _{gs1}		_	1.4	_	
Gate-drain ("Mille	er") charge	Q _{gd}		_	2.7	_	

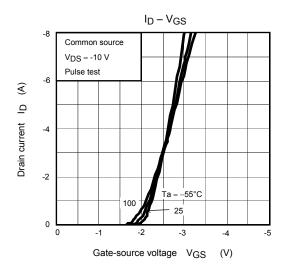
Source-Drain Ratings and Characteristics (Ta = 25°C)

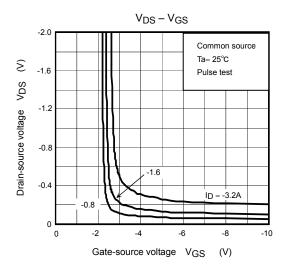
Characteris	tic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_	_	-12.8	Α
Forward voltage (diode)		V _{DSF}	I _{DR} = -3.2 A, V _{GS} = 0 V	_	_	1.2	V

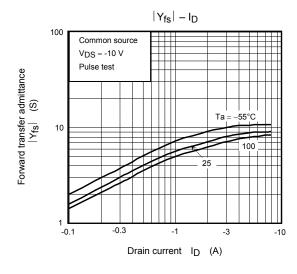
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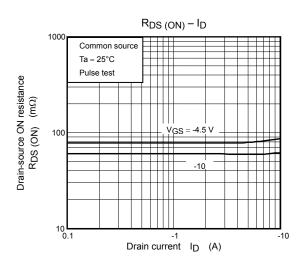


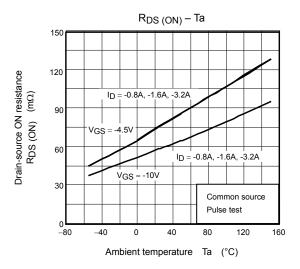


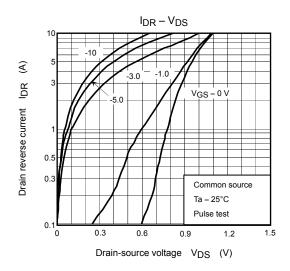


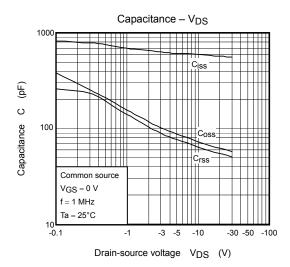


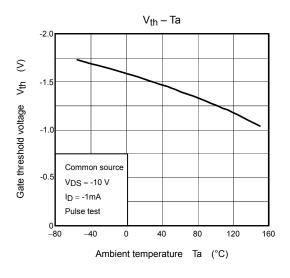


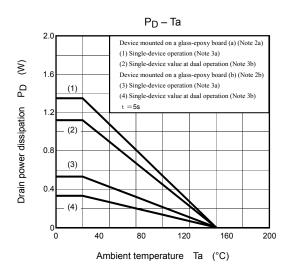


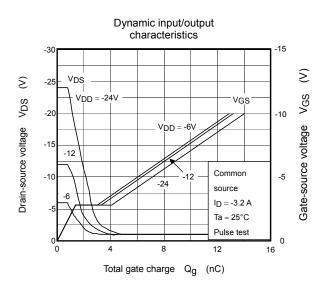


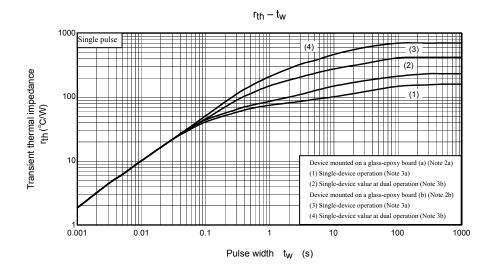


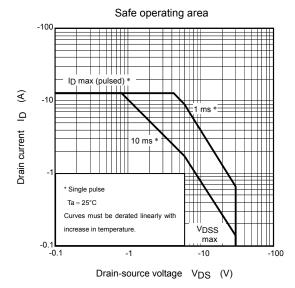












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