

Engineering/Process Change Notice

ECN/PCN No.: 4148

For Manufacturer						
Product Description:	Abracon Part Numb	er / Part Series:	☐ Documentation only	⊠ Series		
PLASTIC SMD MEMS OSCILLATOR	EMF	RC12	□ ECN	☐ Part Number		
			⊠ EOL			
Affected Revision:	New Revision:		Application:	☐ Safety		
A	EC	DL		⋈ Non-Safety		
Prior to Change:						
Active	/51 1D 01 0					
https://abracon.com/datasheets/Ecliptek/EMRC12.pdf						
After Change:						
EOL						
Cause/Reason for Change:						
Discontinuation of manufacturing capability	ty.					
	Chan	ge Plan				
Effective Date:	Additional Remarks:					
2/7/2022	N/A					
Change Declaration:						
N/A						
Issued Date:	Issued By:		Issued Department:			
2/7/2022	Brooke C		Engineering			
	Product	-ngineer				
Approval: Thomas Culhane	Approval: Reuben Q	intanilla	Approval:	.~		
Engineering Director	Quality I		Ying Huang Purchasing Director			
Engineering Director		on EOL only	T dichasing bit	Cettor		
Look Time Day (if applicable).	FUI ADIAC	<u> </u>	oor / Doub Covince			
Last Time Buy (if applicable): 5/7/2022	Alternate Part Number / Part Series: AK7 (frequency=100-220MHz),					
3,7,2322						
Additional Approval:	Additional Approval		reater than 220MHz or less than 100MHz) Additional Approval:			
Additional Approval.	Additional Approval	•	Additional Approval.			
	Customer Appro	oval (If Applicable)				
Qualification Status:	customer Appro	vai (ii Applicable)				
Qualification Status.	□ Annroved	□ Not accepted				
\Box Approved \Box Not accepted Note: It is considered approved if there is no feedback from the customer 1 month after ECN/PCN is released.						
Customer Part Number: Customer Project:						
Company Name:	Company Represent	ative:	Representative Signature	•		
company name.	company nepresent	utite.		•		
Customer Remarks:						

Form #7020 | Rev. G | Effective: 02/22/2021 |











EMRC12 Series



REGULATORY COMPLIANCE











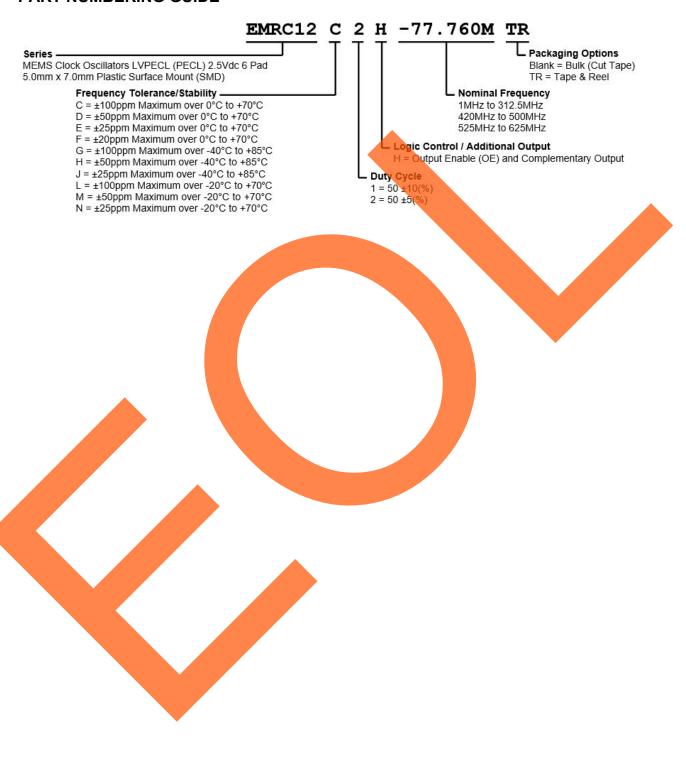
ITEM DESCRIPTION

MEMS Clock Oscillators LVPECL (PECL) 2.5Vdc 6 Pad 5.0mm x 7.0mm Plastic Surface Mount (SMD)

ELECTRICAL SPECIFICAT	TIONS
Nominal Frequency	1MHz to 625MHz
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging at 25°C, Reflow, Shock, and Vibration ±100ppm Maximum over 0°C to +70°C ±50ppm Maximum over 0°C to +70°C ±25ppm Maximum over 0°C to +70°C ±20ppm Maximum over -40°C to +85°C ±50ppm Maximum over -40°C to +85°C ±50ppm Maximum over -40°C to +85°C ±100ppm Maximum over -20°C to +70°C ±50ppm Maximum over -20°C to +70°C ±50ppm Maximum over -20°C to +70°C ±50ppm Maximum over -20°C to +70°C
Aging at 25°C	±1ppm First Y <mark>ear Ma</mark> ximum
Supply Voltage	+2.5Vdc ±10%
Input Current	Excluding Lo <mark>ad Term</mark> ination Current 60mA Typica <mark>l, 70mA</mark> Maximum
Output Voltage Logic High (V _{OH})	Vdd -1.10Vdc <mark>Minimu</mark> m, 1.60Vdc Typical, Vdd -0.70Vdc Maximu <mark>m</mark>
Output Voltage Logic Low (V _{OL})	Vdd -1.90Vdc M <mark>inimum,</mark> 0.80Vdc Typical, Vdd -1.50Vdc Maxim <mark>um</mark>
Rise/Fall Time	Measured over 20% to 80% of waveform 300pSec Typical, 500pSec Maximum
Duty Cycle	Measured at 50% of waveform 50 ±10(%) 50 ±5(%) (Not available with Duty Cycle of 50 ±5(%) over Nominal Frequency range of 312.500001MHz to 524.999999MHz)
Output Swing (VOpp)	600mVdc Minimum, 800mVdc Typical, 1000mVdc Maximum
Load Drive Capability	50 Ohms into Vcc-2.0Vdc
Output Logic Type	LVPECL
Logic Control / Additional Output	Output Enable (OE) and Complementary Output
Output Control Input Voltage	Vih of 70% of Vdd Minimum or No Connect to Enable Output and Complementary Output, Vil of 30% of Vdd Maximum to Disable Output and Complementary Output (High Impedance)
Output Enable Current	35mA Maximum (Without Load)
RMS Phase Jitter	Fj = 12kHz to 20MHz; Random 0.5pSec Typical, 1pSec Maximum
Period Jitter (Deterministic)	0.2pSec Typical
Period Jitter (Random)	1.0pSec Typical
Period Jitter (RMS)	1.4pSec Typical, 1.7pSec Maximum
Period Jitter (pk-pk)	15pSec Typical, 20pSec Maximum
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

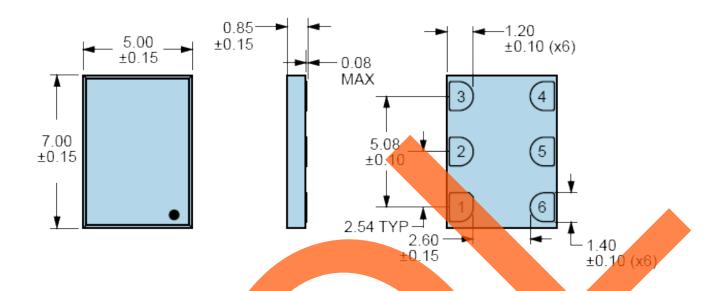


PART NUMBERING GUIDE

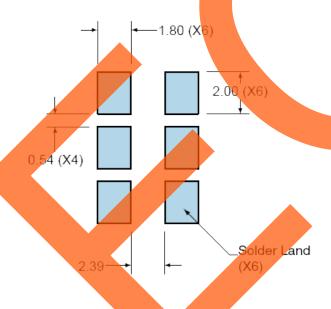




MECHANICAL DIMENSIONS



SUGGESTED SOLDER PAD LAYOUT



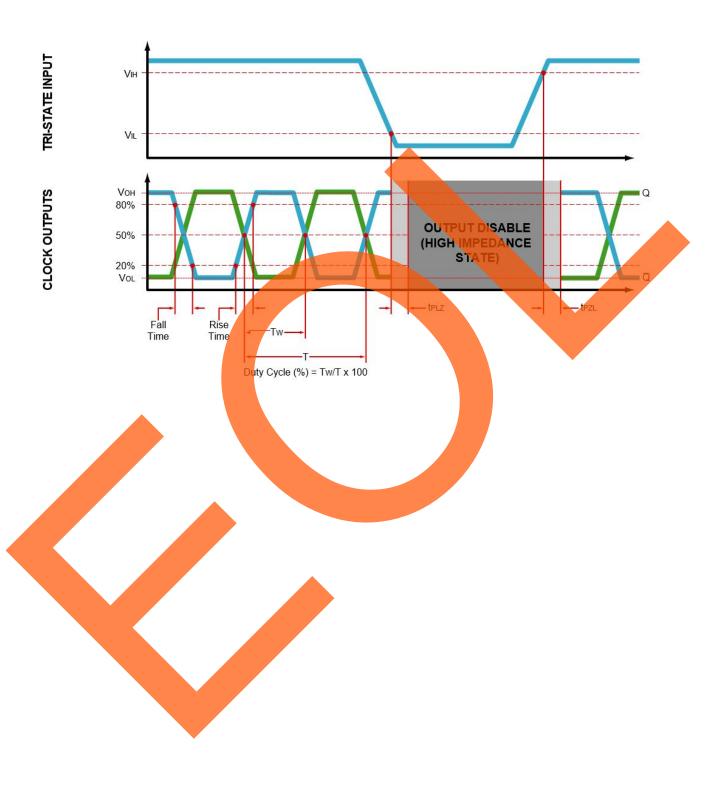
PIN	CONNECTION
1	Output Enable (OE)
2	No Connect
3	Case Ground
4	Output
5	Complementary Output
6	Supply Voltage

All Tolerances are ±0.1

All Dimensions in Millimeters

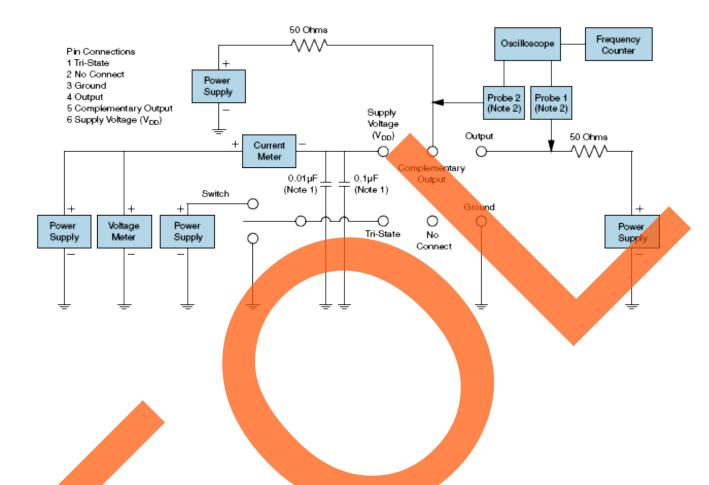


OUTPUT WAVEFORM & TIMING DIAGRAM





TEST CIRCUIT FOR TRI-STATE AND COMPLEMENTARY OUTPUT



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive Probe is recommended.

Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

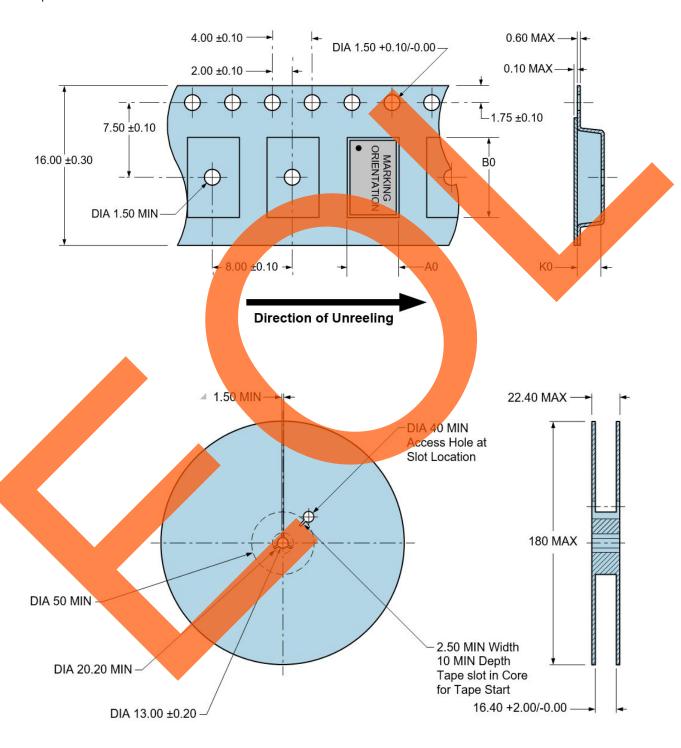
EMRC12 Series



TAPE & REEL DIMENSIONS

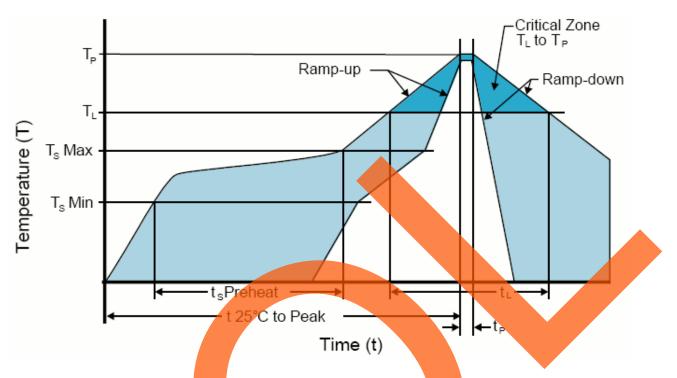
Quantity per Reel: 1,000 Units

All Dimensions in Millimeters
Compliant to EIA-481





RECOMMENDED SOLDER REFLOW METHOD



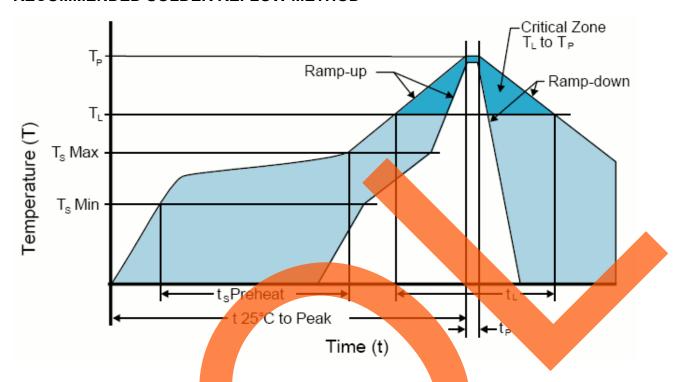
HIGH TEMPERATURE INFRARED/CONVECTION		
T _s MAX to T _L (Ramp-up Rate)	3°C/Second Maximum	
Preheat		
- Temperature Minimum (Ts MIN)	150°C	
- Temperature Typical (T _S TYP)	175°C	
- Temperature Maximum(T _s MAX)	200°C	
- Time (t _s MIN)	60 - 180 Seconds	
Ramp-up Rate (T _L to T _P)	3°C/Second Maximum	
Time Maintained Above:		
- Temperature (T _L)	217°C	
- Time (t _L)	60 - 150 Seconds	
Peak Temperature (T _P)	260°C Maxim <mark>um for 10</mark> Seconds Maximum	
Target Peak Temperature(Tp Target)	250°C +0 <mark>/-5°C</mark>	
Time within 5°C of actual peak (tp)	20 - 40 Seconds	
Ramp-down Rate	6°C/Second Maximum	
Time 25°C to Peak Temperature (t)	8 Minutes Maximum	
Moisture Sensitivity Level	Level 1	
Additional Notes	remperatures shown are applied to body of device.	

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)



RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARED/CONVECTION		
T _s MAX to T _L (Ramp-up Rate)	5°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	N/A	
- Temperature Typical (T _s TYP)	150°C	
- Temperature Maximum(T _s MAX)	N/A	
- Time (t _s MIN)	60 - 120 Seconds	
Ramp-up Rate (T _L to T _P)	5°C/Second Maximum	
Time Maintained Above:		
- Temperature (TL)	150°C	
- Time (t _L)	200 Seconds Maximum	
Peak Temperature (T _P)	240°C Maximum	
Target Peak Temperature(Tp Target)	240°C Maximum 2 Times/230°C Maximum 1Time	
Time within 5°C of actual peak (tp)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time	
Ramp-down Rate	5°C/Second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)