



Product data sheet

1. Product profile

1.1 General description

NPN switching transistor and high-speed switching diode to protect the base-emitter junction in reverse direction in a SOT346 (SC-59A/TO-236) small Surface-Mounted Device (SMD) plastic package.

1.2 Features

- Switching transistor and high-speed switching diode as driver
- High-speed switching diode to protect the base-emitter junction
- Application-optimized pinout
- Internal connections to minimize layout effort
- Space-saving solution
- Reduces component count

1.3 Applications

Power MOSFET driver

1.4 Quick reference data

Table 1.Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|---------------------------|--|--------------|-----|------|------|
| NPN transi | istor | | | | | |
| V _{CEO} | collector-emitter voltage | open base | - | - | 40 | V |
| I _C | collector current | | - | - | 0.6 | А |
| I _{CM} | peak collector current | single pulse; t _p ≤ 1 ms | - | - | 1 | А |
| Diode | | | | | | |
| l _F | forward current | | - | - | -0.2 | А |
| V _F | forward voltage | $I_{F} = -200 \text{ mA}$ | <u>[1]</u> - | - | -1.1 | V |



2. Pinning information

| Table 2. | Pinning | | |
|----------|-----------------------|--------------------|--------|
| Pin | Description | Simplified outline | Symbol |
| 1 | base TR1, cathode D1 | _ | _ |
| 2 | emitter TR1, anode D1 | | 3 |
| 3 | collector TR1 | 1 2 | |

3. Ordering information

| Table 3. Order | ring informa | tion | |
|----------------|--------------|--|---------|
| Type number | Package | | |
| | Name | Description | Version |
| PMD4002K | SC-59A | plastic surface-mounted package; 3 leads | SOT346 |

4. Marking

| Table 4. | Marking codes | |
|----------|---------------|--------------|
| Type num | ber | Marking code |
| PMD4002 | < | D2 |

5. Limiting values

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|------------------------------------|--|--------------|------|------|
| NPN trans | istor | | | | |
| V _{CBO} | collector-base voltage | open emitter | - | 40 | V |
| V _{CEO} | collector-emitter voltage | open base | - | 40 | V |
| I _C | collector current | | - | 0.6 | А |
| I _{CM} | peak collector current | single pulse; $t_p \leq 1 ms$ | - | 1 | А |
| I _B | base current | | - | 0.2 | А |
| I _{BM} | peak base current | single pulse; t _p ≤ 1 ms | - | 0.3 | А |
| P _{tot} | total power dissipation | $T_{amb} \le 25 \ ^{\circ}C$ | <u>[1]</u> _ | 250 | mW |
| | | | [2] _ | 330 | mW |
| | | | [3] _ | 445 | mW |
| Diode | | | | | |
| l _F | forward current | | - | -0.2 | А |
| I _{FRM} | repetitive peak forward current | $t_p \le 1$ ms; $\delta = 0.25$ | - | -0.6 | А |
| I _{FSM} | non-repetitive peak forward | square wave | | | |
| | current | $t_p \le 1 \ \mu s$ | - | -9 | А |
| | | $t_p \leq 100 \ \mu s$ | - | -3 | А |
| | | $t_p \le 10 \text{ ms}$ | - | -1.7 | А |
| Device | | | | | |
| Тj | junction temperature | | - | 150 | °C |
| T _{amb} | ambient temperature | | -65 | +150 | °C |
| T _{stg} | storage temperature | | -65 | +150 | °C |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



6. Thermal characteristics

| Table 6. | Thermal characteristics | | | | | |
|-----------|-------------------------|-------------|--------------|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| NPN tran | sistor | | | | | |
| - un()-a) | thermal resistance from | in free air | <u>[1]</u> _ | - | 500 | K/W |
| | junction to ambient | | [2] | - | 375 | K/W |
| | | | [3] | - | 280 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

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PMD4002K MOSFET driver



PMD4002K 1

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7. Characteristics

| | _ | | | | _ | | |
|--------------------|---|--|-----|-----|------|------|------|
| - | Parameter | Conditions | | Min | Тур | Мах | Unit |
| NPN trar | nsistor | | | | | | |
| I _{CBO} | collector-base cut-off | $V_{CB} = 40 \text{ V}; I_E = 0 \text{ A}$ | | - | - | 10 | nA |
| | current | $\label{eq:VCB} \begin{array}{l} V_{CB} = 40 \ V; \ I_E = 0 \ A; \\ T_j = 150 \ ^\circC \end{array}$ | | - | - | 10 | μA |
| h _{FE} | DC current gain | $V_{CE} = 5 \text{ V}; I_{C} = 1 \text{ mA}$ | | 100 | 210 | - | |
| | | $V_{CE} = 5 \text{ V}; I_{C} = 200 \text{ mA}$ | | 100 | 170 | 300 | |
| | $V_{CE} = 5 \text{ V}; I_{C} = 500 \text{ mA}$ | [1] | 50 | 100 | - | | |
| V _{CEsat} | | $I_{C} = 200 \text{ mA}; I_{B} = 20 \text{ mA}$ | | - | 150 | 250 | mV |
| saturation voltage | $I_{C} = 500 \text{ mA}; I_{B} = 50 \text{ mA}$ | <u>[1]</u> | - | 300 | 500 | mV | |
| V _{BEsat} | base-emitter saturation | $I_{C} = 200 \text{ mA}; I_{B} = 20 \text{ mA}$ | | - | 0.86 | 1 | V |
| | voltage | $I_{C} = 500 \text{ mA}; I_{B} = 50 \text{ mA}$ | [1] | - | 0.95 | 1.1 | V |
| V_{BE} | base-emitter voltage | $V_{CE} = 5 \text{ V}; I_{C} = 300 \text{ mA}$ | | - | 830 | - | mV |
| Diode | | | | | | | |
| V _F | forward voltage | I _F = -200 mA | [1] | - | - | -1.1 | V |
| Device | | | | | | | |
| t _d | delay time | l _C = 0.15 A; l _B = 5 mA | | - | 6 | - | ns |
| t _r | rise time | | | - | 21 | - | ns |
| t _{on} | turn-on time | | | - | 27 | - | ns |
| t _s | storage time | | | - | 484 | - | ns |
| t _f | fall time | | | - | 120 | - | ns |
| t _{off} | turn-off time | | | - | 604 | - | ns |
| Device w | vith optional capacitor C | 1 | | | | | |
| t _d | delay time | I _C = 0.15 A; I _B = 5 mA; | | - | 3 | - | ns |
| t _r | rise time | C1 = 1 nF | | - | 1 | - | ns |
| t _{on} | turn-on time | | | - | 4 | - | ns |
| t _s | storage time | | | - | 23 | - | ns |
| t _f | fall time | | | - | 41 | - | ns |
| t _{off} | turn-off time | | | - | 64 | - | ns |

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8. Test information



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9. Package outline



10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

| Type number | Package | Description | Packing o | uantity |
|-------------|---------|--------------------------------|-----------|---------|
| | | | 3000 | 10000 |
| PMD4002K | SOT346 | 4 mm pitch, 8 mm tape and reel | -115 | -135 |

[1] For further information and the availability of packing methods, see <u>Section 15</u>.

11. Soldering



12. Mounting



13. Revision history

| Table 9. Revision his | tory | | | |
|-----------------------|--------------|--------------------|---------------|------------|
| Document ID | Release date | Data sheet status | Change notice | Supersedes |
| PMD4002K_1 | 20061103 | Product data sheet | - | - |

14. Legal information

14.1 Data sheet status

| Document status[1][2] | Product status ^[3] | Definition |
|--------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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