



$\begin{center} \textbf{SPECIFICATION} (Reference sheet) \end{center}$

• Supplier : Samsung electro-mechanics • Samsung P/N : CL03C101JA3ANNC

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 100pF, 25V, ±5%, C0G, 0201

A. Samsung Part Number

<u>CL</u> <u>03</u> <u>C</u> <u>101</u> <u>J</u> <u>A</u> <u>3</u> <u>A</u> <u>N</u> <u>N</u> <u>C</u> ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor		
② Size	0201 (inch code)	L: 0.6 ± 0.03 mm	W: 0.3 ± 0.03 mm
3 Dielectric	C0G	8 Inner electrode	Pd
Capacitance	100 pF	Termination	Ag
⑤ Capacitance	±5 %	Plating	Sn 100% (Pb Free)
tolerance		Product	Normal
6 Rated Voltage	25 V	Special	Reserved for future use
7 Thickness	0.3 ± 0.03 mm	① Packaging	Cardboard Type, 7" reel

B. Samsung Reliability Test and Judgement condition

Performance	Test condition	
Within specified tolerance	15√5Vrms 0.5~5Vrms	
1000 min		
More than 500Mohm⋅μF	Rated Voltage 60~120 sec.	
No abnormal exterior appearance	Visual inspection	
No dielectric breakdown or	300% of the rated voltage	
mechanical breakdown		
COG		
(From -55℃ to 125℃, Capacitance change should be within ±30PPM/℃)		
No peeling shall be occur on the	200g·F, for 10±1 sec.	
terminal electrode		
Capacitance change: within ±5%	Bending to the limit (1mm)	
	with 1.0mm/sec.	
More than 75% of terminal surface	SnAg3.0Cu0.5 solder	
is to be soldered newly	245±5℃, 3±0.3sec.	
	(preheating : 80~120 ℃ for 10~30sec.)	
Capacitance change: within ±2.5%	Solder pot : 270±5℃, 10±1sec.	
Tan δ, IR : initial spec.		
	1000 min More than 500Mohm·µF No abnormal exterior appearance No dielectric breakdown or mechanical breakdown C0G (From -55℃ to 125℃, Capacitance change) No peeling shall be occur on the terminal electrode Capacitance change: within ±5% More than 75% of terminal surface is to be soldered newly Capacitance change: within ±2.5%	

	Performance	Test condition
Vibration Test	Capacitance change: within ±2.5%	Amplitude: 1.5mm
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)
		2hours × 3 direction (x, y, z)
Moisture	Capacitance change: within ±7.5%	With rated voltage
Resistance	Q: 200 min	40±2℃, 90~95%RH, 500 +12/-0 hours
	IR : More than 25MΩ·μF	
High Temperature	Capacitance change: within ±3%	With 200% of the rated voltage
Resistance	Q: 350 min	Max. operating temperature
	IR : More than 50MΩ·μF	1000+48/-0 hours
Temperature	Capacitance change: within ±2.5%	1 cycle condition
Cycling	Tan δ, IR : initial spec.	Min. operating temperature → 25 °C
		→ Max. operating temperature → 25°C
		5 cycles test

C. Recommended Soldering method:

Reflow (Reflow Peak Temperature : 260+0/-5°C, 10sec. Max)



Product specifications included in the specifications are effective as of March 1, 2013.

Please be advised that they are standard product specifications for reference only.

We may change, modify or discontinue the product specifications without notice at any time.

So, you need to approve the product specifications before placing an order.

Should you have any question regarding the product specifications,

please contact our sales personnel or application engineers.