PIN Diode SPDT 120 Watt Switch for 0.05 - 6.0 GHz Higher Power Applications

Features

- Exceptional Broadband Performance
- Low Insertion Loss: $T_X = 0.20 \text{ dB} @ 2.7 \text{ GHz}$
- High Isolation: R_X = 50 dB @ 2.7 GHz
- High T_x RF Input Power = 120 W CW
 @ 2.0 GHz, 85°C
- Suitable for High Power LTE, TD-SCDMA, WiMAX, and Military Radio Applications
- Surface Mount 4 mm PQFN Package
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MASW-000936 is a SPDT high power, broadband, high linearity, PIN diode T/R switch for 0.05 - 6.0 GHz applications, including WiMAX & WiFi. The device is provided in an industry standard lead free 4 mm PQFN plastic package.

This device incorporates PIN diode die fabricated with a low loss, high isolation switching diode processes.

1. Reference Application Note M513 for reel size information.

Functional Diagram (Top View)



Pin Configuration²

Pin	Pin Name	Description	
1	GND	Ground	
2	ANT	Antenna	
3	N/C	Connect to Ground	
4	N/C	No Connection	
5	N/C	No Connection	
6	N/C	Connect to Ground	
7	RX	Receive	
8	GND	Ground	
9	ShD R _X Bias	ShD R _X Bias	
10	N/C	No Connection	
11	GND	Ground	
12	N/C ³	Do Not Use	
13	GND	Ground	
14	ТХ	Transmit	
15	N/C	Connect to Ground	
16	N/C	No Connection	

2. The exposed pad centered on the package bottom must be connected to RF, DC and Thermal ground.

3. Do not ground pin 12.

* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

Package

Tape and Reel (1K)

Sample Board

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Ordering Information¹

Part Number

MASW-000936-14000T

MASW-000936-001SMB

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Electrical Specifications⁴: Freq. = 2.0, 2.7, 3.5 GHz, $T_A = 25^{\circ}C$, Bias = 100 mA / 28 V

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss ⁴ P _{IN} = 0 dBm	R _x 0.8 GHz T _x 0.8 GHz R _x 2.0 GHz T _x 2.0 GHz R _x 2.7 GHz T _x 2.7 GHz R _x 3.5 GHz T _x 3.5 GHz	dB		0.20 0.07 0.35 0.15 0.50 0.20 0.70 0.25	 0.55 0.75 0.90
Isolation ⁴ P _{IN} = 0 dBm	$\begin{array}{c} R_{X} \text{ to Antenna, 2.0 GHz} \\ T_{X} \text{ to Antenna, 2.0 GHz} \\ R_{X} \text{ to Antenna, 2.7 GHz} \\ T_{X} \text{ to Antenna, 2.7 GHz} \\ R_{X} \text{ to Antenna, 3.5 GHz} \\ T_{X} \text{ to Antenna, 3.5 GHz} \\ T_{X} \text{ to Antenna, 3.5 GHz} \end{array}$		41 40 33 	45 16 50 13 40 11	_
Input Return Loss ⁴ P _{IN} = 0 dBm	R _x T _x	dB		23 34	_
T _X Input P0.1 dB	T _x to Antenna	dBm	_	>50	
T_X IIP3 P _{IN} = +30 dBm	F1 = 2010 MHz, F2 = 2020 MHz	dBm	—	72	—
T _X CW Input Power	85°C Base plate 2.0 GHz 2.7 GHz 3.5 GHz	dBm / W dBm / W dBm / W	_	50.8 / 120 50 / 100 49 / 80	_
R _X CW Input Power	85°C Base plate 2.0 GHz	dBm W	—	41.5 14	_
T _X RF Switching Speed	(10 - 90% RF Voltage) 1 MHz Rep Rate in Modulating Mode	ns	_	200	_

4. See Bias Table

Absolute Maximum Ratings^{5,6}

@ T_A = +25 °C (unless otherwise specified)

Parameter	Absolute Maximum	
Forward Current	150 mA	
Reverse Voltage (RF & DC)	160 V	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-55°C to +150°C	
Junction Temperature	+175°C	
T _x Incident CW Power	50.8 dBm (120 W) ⁷ @ 2 GHz, 85°C	
R _x Incident CW Power	41.5 dBm (14 W) ⁷ @ 2 GHz, 85°C	

5. Exceeding these limits may cause permanent damage.

 MACOM does not recommend sustained operation near these survivability limits.

7. Base-plate temperature must be controlled to a constant +85°C.

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Handling Procedures

Static Sensitivity

Human Body devices.

damage:

Please observe the following precautions to avoid

Silicon Integrated Circuits are sensitive

electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1C

to



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Bias Diagrams & Tables





Bias -15 / +15 V

Bias Table	T _x	R _x	R _x ShDBias	ANT
Pin	Pin 14	Pin 7	Pin 9	Pin 2
T _x -ANT Isolation	(+15 V), 0 mA	(-15 V), -100 mA	GND	GND
T _x -ANT Insertion Loss	(-15 V), -100 mA	(+15 V), 100 mA	GND	GND
R _x -ANT Isolation	(-15 V), -100 mA	(+15 V), 100 mA	GND	GND
R _X -ANT Insertion Loss	(+15 V), 0 mA	(-15 V), 100 mA	GND	GND

Bias 0 / 28 V

Bias Table	T _x	R _x	R _x ShDBias	ANT
Pin	Pin 14	Pin 7	Pin 9	Pin 2
T _x -ANT Isolation	(+28 V), 0 mA	(GND), -100 mA	(+28 V), 0 mA	+28 V
T _x -ANT Insertion Loss	(GND), -100 mA	(+28 V), 100 mA	(GND), -100 mA	+28 V
R _x -ANT Isolation	(GND), -100 mA	(+28 V), 100 mA	(GND), -100 mA	+28 V
R _x -ANT Insertion Loss	(+28 V), 0 mA	(GND), -100 mA	(+28 V), 0 mA	+28 V

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Typical Performance Curves (RF-probed parts), T_x (100 mA Bias Current) PIN Diode SPDT 120 Watt Switch for 0.05 - 6.0 GHz Higher Power Applications

Typical Performance Curves (RF-probed parts), T_X (100 mA Bias Current)

Insertion Loss, T_X





Input Return Loss, T_x



Output Return Loss, T_x



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Typical Performance Curves (RF-probed parts), R_X (100 mA Bias Current)

Insertion Loss, R_x





Input Return Loss, R_x



Output Return Loss, R_x



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Application Schematic⁸



8. Adding an LC network to pin 12 can improve R_{X} performance between 2.0 and 2.7 GHz but may limit performance above 3 GHz. For broadband applications MACOM recommends not using pin 12 and not connecting it to any metal trace.

PCB Footprint



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Parts List

Component	Value	Package	
C1-C3	22 pF	0603	
C4-C6	27 pF	0603	
L1-L4	68 nH	0603	
R1, R2	137 Ω	0603	



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Lead Free 4 mm 16-Lead PQFN[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is NiPdAuAg.

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