

TN5015H-6I

High temperature 50 A SCRs

Datasheet - production data



Features

- High junction temperature: T_j = 150 °C
- High noise immunity dV/dt = 500V/µs up to 150 °C
- Gate triggering current I_{GT} = 15 mA
- Peak off-state voltage V_{DRM}/V_{RRM} = 600 V
- High turn-on current rise dl/dt = 100 A/µs
- ECOPACK[®]2 compliant component
- Insulated package TO-220AB:
 - Insulated voltage: 2500 V_{RMS}
 - Complies with UL 1557 (File ref : E81734)

Applications

- Motorbike voltage regulator circuits
- Inrush current limiting circuits
- Motor control circuits and starters
- Solid state relays
- Overvoltage crowbar protection

Description

This device offers high thermal performance operation up to 50 A thanks to its junction temperature T_j up to 150 °C. Its fully tab insulated (thanks to ceramic inside) TO-220AB package allows a back to back configuration.

Its trade-off noise immunity ($dV/dt = 500 V/\mu s$) versus its gate triggering current ($I_{GT} = 15 mA$) and its turn-on current rise ($dI/dt = 100 A/\mu s$) allow to design robust and compact control circuit for voltage regulator in motorbikes and industrial drives, overvoltage crowbar protection, motor control circuits in power tools and kitchen appliances, inrush current limiting circuits.

Table 1: Device summary

Order code	Package	V _{DRM} /V _{RRM}	I _{GT}
TN5015H-6I	TO-220AB ins.	600 V	15 mA

This is information on a product in full production.

1 Characteristics

Table 2: Absolute maximum ratings (limiting values), T_j = 25 °C unless otherwise specified

Symbol	Param	Value	Unit		
I _{T(RMS)}	RMS on-state current (180 ° conduction angle) $T_c = 75 °C$			50	А
			T _c = 81 °C	30	
It(av)	Average on-state current (180 ° conduction angle)		T _c = 97 °C	25	А
			T _c = 111 °C	20	
	Non repetitive surge peak on-sta	te current	t _p = 8.3 ms	493	Α
I _{TSM}	(T _j initial = 25 °C)		t _p = 10 ms	450	A
I _{PP} ^a	RG-A = 3 kΩ, 8/20 us current sh	ape, 10 pulses	6	1500	А
l ² t	l ² t value for fusing		t _p = 10 ms	1012	A ² s
dl/dt	Critical rate of rise of on-state current $f = 60 \text{ Hz}$ $I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$ f = 60 Hz			100	A/µs
Vdrm/Vrrm	Repetitive peak off-state voltage $T_j = 150 \text{ °C}$			600	V
Vdsm/Vrsm	Non repetitive surge peak off-state voltage t _p = 10 ms			V _{DRM} /V _{RRM} + 100	V
Ідм	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α
P _{G(AV)}	Average gate power dissipation	T _j = 150 °C	1	W	
V _{RGM}	Maximum peak reverse gate volt	5	V		
T _{stg}	Storage junction temperature range			-40 to +150	°C
Tj	Maximum operating junction temperature			-40 to +150	°C
Τı	Maximum lead temperature soldering during 10 s			260	°C
Vins	Insulation RMS voltage, 1 minute			2500	V

Table 3: Electrical characteristics ($T_i = 25$ °C unless otherwise specified)

Symbol	Test conditions		Value	Unit	
lgт	Max.				mA
Vgt	$V_{D} = 12 \text{ V}, \text{R}_{\text{L}} = 33 \Omega$		Max.	1.3	V
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	Min.	0.15	V	
Ін	I _T = 500 mA, gate open Max.				mA
١L	I _G = 1.2 x I _{GT} Max.				mA
dV/dt	$V_D = 402 \text{ V}$, gate open $T_j = 150 \text{ °C}$		Min.	500	V/µs
t _{gt}	$I_{TM} = 100 \text{ A}, V_D = 402 \text{ V}, I_G = 30 \text{ mA}, (dI_G/dt) \text{ max} = 0.2 \text{ A}/\mu \text{s}$ Typ.				μs
tq	$ \begin{array}{l} I_{TM} = 100 \; A, \; V_D = 402 \; V, \; (dI_G/dt)max = 30 \; A/\mu s, \\ V_R = 25 \; V, \; dV_D/dt = 50 \; V/\mu s \end{array} T_j = 150 \; ^\circ C \label{eq:Tj} $		Тур.	85	μs

^a According to IEC61000-4-5 standard (See Section 2)





Characteristics

Table 4: Static characteristics							
Symbol	Test conditions				Unit		
V _{TM}	$I_{TM} = 100 \text{ A}, t_p = 380 \ \mu \text{s}$	T _j = 25 °C	Max.	1.65	V		
V _{TO}	Threshold voltage	T _j = 150 °C	Max.	0.85	v		
RD	Dynamic resistance $T_j = 150 \text{ °C}$ Max.		9	mΩ			
Idrm, Irrm	VD = VDRM = VRRM	T _j = 25 °C	Mov	10	μA		
	VD = VDRM = VRRM	T _j = 150 °C	Max.	6	mA		

Table 5: Thermal parameters

Symbol	Parameter	Value	Unit	
Rth(j-c)	Junction to case (DC) Max.		1.5	00 AA
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W	



Characteristics







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2 Application information

2.1 Overvoltage crowbar protection

An application featuring the strong current robustness of TN5015H-6I is the overvoltage crowbar protection, implemented in AC/DC switch mode power supply. An application global schematic is shown *Figure 12: "Global schematic of a crowbar overvoltage protection application"*.





In case of overvoltage on line power terminals (example: lightning event), the voltage is rising quickly across the DC/DC module and can reach voltages limitations of devices, leading to possible application failure. The overvoltage crowbar protection answers to this issue by placing the SCR in parallel of the rectifier bridge output, and limiting the induced surge current during electrical over-stress.

The protection principle is to trig the TN5015H-6I when the overvoltage reaches the maximum authorized voltage. At TN5015H-6I turn-on, the electrical overvoltage stress becomes an overcurrent stress, flowing through the TN5015H-6I. No more voltage stress is applied across the down stream converter and the TN5015H-6I absorbs the overcurrent, making the application safe.

Additionally, the AC/DC SMPS efficiency and stand-by losses are not impacted by the protection, thanks to the very low leakage current of the TN5015H-6I.

2.2 Peak pulse current robustness

The TN5015H-6I overcurrent capability is guaranteed through a specific parameter called peak pulse current: IPP. This is the maximum current robustness of TN5015H-6I for an 8/20 µs current waveform, as described in the IEC61000-4-5 standard.

The *Figure 13: "IPP 8/20 \mus test circuit"* illustrates the test circuit for the I_{PP} parameter. Overstress is applied 10 times (according to the same standard) with a 1.2/50 μ s overvoltage and 8/20 μ s overcurrent stress generator.



The *Figure 14: "IPP 8/20 \mus test waveform"* (left part) is the 8/20 μ s waveform of current through the TN5015H-6I under test. The maximum gate current in this case is 350 mA (zoom at SCR triggering in the right part) and the maximum non-repetitive rate of current (dl/dt)_{oc} is 250 A/ μ s.





3 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

- Molding compound resin is halogen-free and meets UL standard level V0
- Lead-free package leads finishing
- ECOPACK[®]2 compliant

3.1 TO-220AB package information





TN5015H-6I

Package information

01	Table 6: TO-220AB Insulated package mechanical data					
	Dimensions					
Ref.		Millimeters				
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	15.20		15.90	0.5984		0.6260
a1		3.75			0.1476	
a2	13.00		14.00	0.5118		0.5512
В	10.00		10.40	0.3937		0.4094
b1	0.61		0.88	0.0240		0.0346
b2	1.23		1.32	0.0484		0.0520
С	4.40		4.60	0.1732		0.1811
c1	0.49		0.70	0.0193		0.0276
c2	2.40		2.72	0.0945		0.1071
е	2.40		2.70	0.0945		0.1063
F	6.20		6.60	0.2441		0.2598
I	3.73		3.88	0.1469		0.1528
L	2.65		2.95	0.1043		0.1161
12	1.14		1.70	0.0449		0.0669
13	1.14		1.70	0.0449		0.0669
14	15.80	16.40	16.80	0.6220	0.6457	0.6614
М		2.6			0.1024	

Notes:

 $\ensuremath{^{(1)}}\xspace$ Inch dimensions are for reference only.



4 Ordering information





Order code	Marking Package		Weight Base qty.		Delivery mode	
TN5015H-6I	TN5015H6I	TO-220AB ins.	2.3 g	50	Tube	



5 Revision history

Table 8: Document revision history

Date	Revision	Changes
02-Jun-2017	1	Initial release.
08-Jan-2018	2	Inserted Section 6: "Application information" and updated Table 2: "Absolute maximum ratings (limiting values), $Tj = 25$ °C unless otherwise specified"



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