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October 9, 2014

To: All customers using the RXD-315-KH2, RXD-418-KH2 and RXD-433-KH2

Re: Product Change Notice

Dear customer,

Linx Technologies is announcing the End of Life for the RXD-\*\*\*-KH2 product line and the introduction of the RXM-\*\*\*-KH3 product line.

The end of life is a result of the discontinuance of critical components from Holtek<sup>®</sup>. The KH3 product line incorporates the Linx DS Series Encoder/Decoder IC, providing backwards compatibility for the majority of KH2 applications.

We are committed to working closely with our customers during the transition to address any questions or concerns.

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# Product Change Notice for RXD-315-KH2, RXD-418-KH2 or RXD-433-KH2

PCN #: LPCN-141009-1 Publish Date: October 9, 2014

#### **Type of Change**

End of life notice for the KH2 receiver product line and introduction of the KH3 receiver product line.

#### **Products Affected**

RXD-315-KH2

RXD-418-KH2

RXD-433-KH2

#### **Description of Change**

The HT658 Decoder used in the previous version of the KH2 receiver product line has gone end-of-life by the manufacturer (Holtek<sup>®</sup>). The KH2 receiver product line has been redesigned to incorporate the Linx DS Series Encoder/Decoder, and is being introduced as the KH3 receiver product line. The design change provides for backwards compatibility with existing fielded devices using the Holtek<sup>®</sup> dip switch addressing scheme.

The new generation of product offers additional functionality and optional configuration pins, resulting in a new naming convention for the product line. The KH3 product will fit in the PCB footprint for those customers who have followed Linx recommended layout guidelines for the KH2 product – the additional pins would be unused and non-connected.

#### **Reason for Change**

Component end-of-life.

#### **Effect of Change**

Form: 3 additional pins added to support address interpretation Fit: No change to overall module size Function: No support for tri-state addressing Quality: No change

#### **Anticipated First Ship Date**

Samples available October 2014

#### **Qualification Data**

Qualification plan specifics are not for general release. Please contact Linx directly for additional information or assistance.

#### **Last Time Buy Date**

No formal last time buy date is established.

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## **Specification Comparison**

ELECTRICAL SPECIFICATIONS POWER SUPPLY		RXM-***-KH3			RXD-***-KH2			Linite	Neter
		Min.	Typical	Max.	Min.	Typical	Max.	Units	Notes
Operating Voltage	V <sub>cc</sub>	2.7	3.0	3.6	2.7	3.0	3.6	VDC	-
With Dropping Resistor		4.5	5.0	5.2	4.3	5.0	5.2	VDC	1,4
Supply Current	I <sub>CC</sub>	4.0	5.2	7.0	4.0	5.2	7.0	mA	-
Power-Down Current	I <sub>PDN</sub>	40		57	20.0	28.0	35.0	μΑ	4
			RECEI	VER SECTI	ON				
Receive Frequency	F <sub>C</sub>								
RXM/D-315-KH3/2			315			315		MHz	-
RXM/D-418-KH3/2			418			418		MHz	-
RXM/D-433-KH3/2			433.92			433.92		MHz	-
Center Frequency		-50	_	+50	-50	_	+50	kHz	_
Accuracy		-30	-	+30	-30	-	+30	KI IZ	-
LO Feedthrough			-80			-80		dBm	2,4
IF Frequency	$F_{IF}$		10.7			10.7		MHz	4
Noise Bandwidth	$N_{3DB}$		280			280		kHz	-
Data Rate		100		10,000	100		10,000	bps	-
Receiver Sensitivity		-106	-112	-118	-106	-112	-118	dBm	4
RSSI/Analog									
Dynamic Range			80			80		dB	4
Analog Bandwidth		50		5,000	50		5,000	Hz	4
Gain			16			16		mV/dB	4
Voltage with no Carrier			1.5			1.5			4
			ANT	ENNA POR	T				
RF Output Impedance	R <sub>IN</sub>		50			50		Ω	4
				Timing					
Receiver Turn-On Time:									
Via V <sub>cc</sub>		3.0	7.0	10.0	3.0	7.0	10.0	msec	4,5
Via PDN		0.04	0.25	0.50	0.04	0.25	0.50	msec	4,5
Decoded Output									
Via V <sub>cc</sub>			258					msec	4,5
Via PDN			138					msec	4,5
Max Time Between Transitions			10.0			10.0		msec	4
			E	NCODER					
Data Length	-								
Holtek <sup>®</sup> Protocol			26 bits 3x			26 bits 3x			
DS Serial Protocol						-		-	
Average Duty Cycle	-	-	50%	-	-	50%	-	-	
Decoder Oscillator	$F_{DEC}$	-	N/A	-	-	70	-	kHz	

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Data Input									
Logic Low	V <sub>IL</sub>	0	-	$0.2 x V_{CC}$	0	-	$0.2 x V_{CC}$	VDC	4
Logic High	V <sub>IH</sub>	V <sub>cc</sub> x0.8	-	V <sub>CC</sub>	V <sub>cc</sub> x0.8	-	V <sub>cc</sub>	VDC	4
Power-Down Input									
Logic Low	V <sub>IL</sub>			0.4			0.4	VDC	
Logic High	V <sub>IH</sub>	V <sub>CC</sub> -0.4			V <sub>CC</sub> -0.4			VDC	
Output Drive Current	-	0.6	1.0	1.2	0.6	1.0	1.2	mA	6
ENVIRONMENTAL									
Operating Temperature		-30	-	+70	-30	-	+70	°C	4
Range	-	-30	-	+70	-30	-	+70	Ľ	4

Notes:

The KH2(3) can utilize a 4.3(5) to 5.2 VDC supply provided a 330-ohm resistor is placed in series with VCC. 1.

2. Into a 50-ohm load

3. When operating from a 5 VDC source, it is important to consider that the output will swing to well less than 5 volts as a result of the required dropping resistor. Please verify that the minimum voltage will meet the high threshold requirement of the device to which data is being sent

4. Characterized, not tested

5. Time to valid data output

6. Maximum drive capability of data outputs

## **Footprint Comparison**

1	] NC	ANT <b>(</b> 28
2	ס ב	GND 🔀 27
3	<u>ס ר</u> ו ב	NC 🔀 26
4	GND GND	RSSI 🔀 25
5	עככ vcc	A9 🕻 24
6	D PDN	A8 🔀 23
7	D2 D2	A7 🕻 22
8	<b>∑</b> D3	A6 🕻 21
9	<u>ס</u> ⊿	A5 🔀 20
10		A4 🔀 19
11	ע בע <b>ב</b> ע	A3 🔀 18
12	2 D5	- A2 🕻 17
13		မို A1 🕻 16
14		
	29 30	31

Figure 1a: RXM-\*\*\*-KH3 Footprint

Σ	NC		ANT	ß	28
Į	D0	(	GND	B	27
b	D1		NC	B	26
Z	GND	F	RSSI	B	25
Z	VCC		A9	B	24
Ł	PDN		<b>A</b> 8	B	23
5	D2		A7	B	22
Z	D3		<b>A</b> 6	B	21
Į	D4		<b>A</b> 5	녑	20
Į	DATA		A4	녑	19
b	VT		A3	B	18
Į	D5		A2	B	17
b	D6		A1	B	16
Ł	D7		A0	Ľ	15
	ואואנאנאנאנאנאנאנאנאנאנאנאנאנאנאנאנאנ	D0 D1 GND VCC PDN D2 D3 D4 DATA VT D5	D0 0 D1 GND F VCC PDN D2 D3 D4 DATA VT D5	D0GNDD1NCGNDRSSIVCCA9PDNA8D2A7D3A6D4A5D4A5D4A3VTA3D5A2D6A1	D0GNDD1NCGNDRSSIVCCA9D2A7D3A6D4A5D4A4D5D4D5A2D5A2D6A1

Figure 1b: RXD-\*\*\*-KH2 Footprint

### **Part Number Comparison**

PRODUCT GOING END OF LIFE	REPLACEMENT PRODUCT
RXD-315-KH2	RXM-315-KH3
RXD-418-KH2	RXM-418-KH3
RXD-433-KH2	RXM-433-KH3