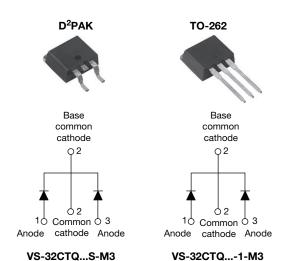
## VS-32CTQ...SHM3, VS-32CTQ...-1HM3 Series

Vishay Semiconductors

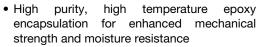
# High Performance Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY					
I <sub>F(AV)</sub>	2 x 15 A				
$V_{R}$	25 V, 30 V				
V <sub>F</sub> at I <sub>F</sub>	0.40 V				
I <sub>RM</sub> typ.	97 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
E <sub>AS</sub>	13 mJ				
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA				
Diode variation	Common cathode				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	30	А				
V <sub>RRM</sub>		25, 30	V				
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	900	А				
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.40	V				
TJ	Range	-55 to +150	°C				

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-32CTQ025SHM3 VS-32CTQ030SHM3 VS-32CTQ030-1HM3 UNITS							
Maximum DC reverse voltage	$V_{R}$	25	30	V			
Maximum working peak reverse voltage	$V_{RWM}$	25	30	V			



# VS-32CTQ...SHM3, VS-32CTQ...-1HM3 Series

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 115 °C, rectangular waveform		30				
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	900	Α			
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	250				
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.20 A, L = 11.10 mH		13	mJ			
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero Frequency limited by $T_J$ maxim	•	3	Α			

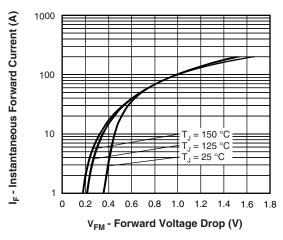
ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
		15 A	T <sub>.1</sub> = 25 °C	0.49			
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	30 A	1J=25 C	0.58	V		
See fig. 1	VFM \	15 A	T <sub>.1</sub> = 125 °C	0.40	V		
		30 A	1J = 125 C	0.53			
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = rated V <sub>R</sub>	1.75	mA		
Maximum reverse leakage current		T <sub>J</sub> = 125 °C	VR = rateu VR	145	IIIA		
Typical reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = rated V <sub>R</sub>	97	mA		
Threshold voltage	V <sub>F(TO)</sub>	T T. maximum		0.233	V		
Forward slope resistance	r <sub>t</sub>	IJ = IJ IIIaxiiIIuIII	$T_J = T_J$ maximum		mΩ		
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range	1300	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs		

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANI	CAL SPE	CIFICAT	ions				
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C		
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub>	DC operation See fig. 4	3.25	°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	-C/VV		
A construction with				2	g		
Approximate weight				0.07	oz.		
Mounting toward	minimum			6 (5)	kgf · cm		
Mounting torque	maximum			12 (10)	(lbf $\cdot$ in)		
			Consisted TO 262AB (D2BAK)	32CTQ025SH			
Marking device			Case style TO-263AB (D <sup>2</sup> PAK)	32CTQ030SH			
			Constitute TO 000AA	32CTQ025-1H			
			Case style TO-262AA	32CTQ	32CTQ030-1H		

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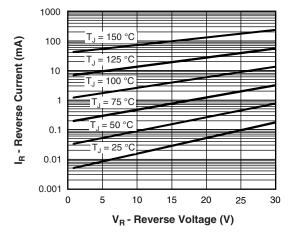


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

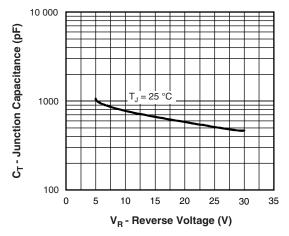


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

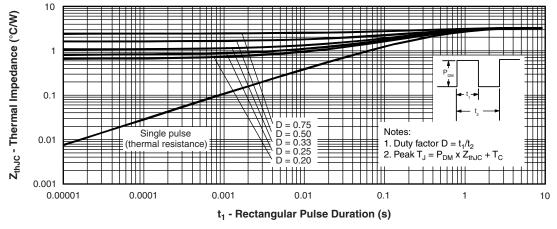


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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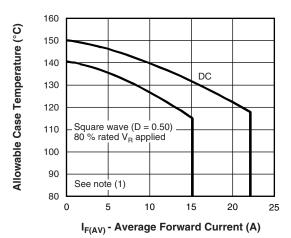


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

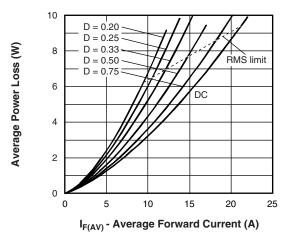


Fig. 6 - Forward Power Loss Characteristics

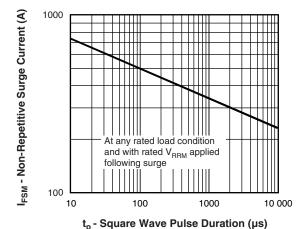


Fig. 7 - Maximum Non-Repetitive Surge Current

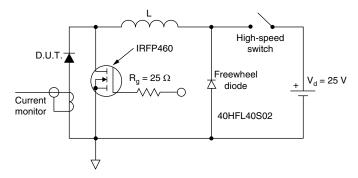


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

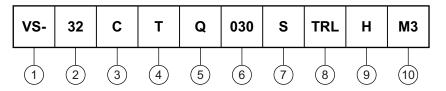
 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

# VS-32CTQ...SHM3, VS-32CTQ...-1HM3 Series

Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 A)

Circuit configuration: C = common cathode

**4** - T = TO-220

5 - Schottky "Q" series

6 - Voltage ratings — 025 = 25 V 030 = 30 V

7 - • S = D<sup>2</sup>PAK

• -1 = TO-262

8 - • None = tube

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK only)

9 - H = AEC-Q101 qualified

- M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-32CTQ025SHM3	50	1000	Antistatic plastic tubes				
VS-32CTQ025STRRHM3	800	800	13" diameter reel				
VS-32CTQ025STRLHM3	800	800	13" diameter reel				
VS-32CTQ025-1HM3	50	1000	Antistatic plastic tubes				
VS-32CTQ030SHM3	50	1000	Antistatic plastic tubes				
VS-32CTQ030STRRHM3	800	800	13" diameter reel				
VS-32CTQ030STRLHM3	800	800	13" diameter reel				
VS-32CTQ030-1HM3	50	1000	Antistatic plastic tubes				

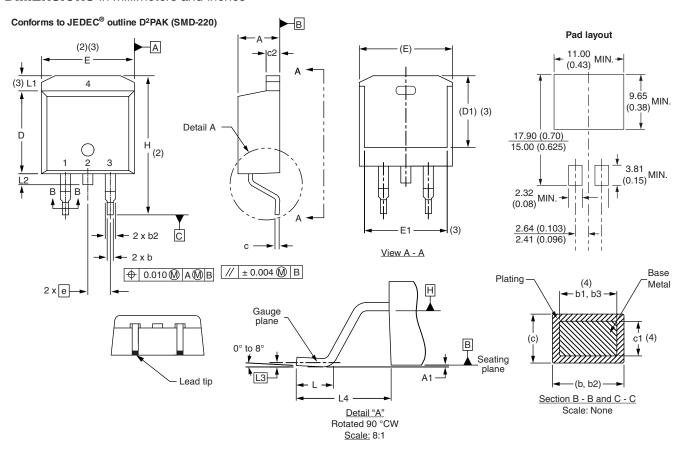
LINKS TO RELATED DOCUMENTS							
Dimensions TO-263AB (D <sup>2</sup> PAK) <u>www.vishay.com/doc?95046</u>							
Dimensions	TO-262AA	www.vishay.com/doc?95419					
Part marking information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information		www.vishay.com/doc?95032					



## Vishay Semiconductors

### D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES SYMBOL MILLIMETERS INCHES		SYMBOL MILLIMETERS		HES	NOTES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

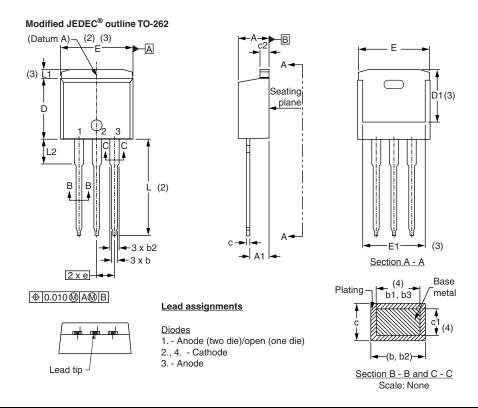
#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

## Vishay Semiconductors

### **TO-262**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	METERS	INC	INCHES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	2.54 BSC		BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.36	3.71	0.132	0.146		

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- 5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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