

150 mA LED driver in DFN2020D-6

Rev. 1 — 13 May 2020

1. Product profile

1.1. General description

LED driver consisting of resistor-equipped NPN transistor with two diodes on one chip in a leadless medium power DFN2020D-6 (SOT1118D) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

Table 1. Product overview

Type number	Package		
	Name	Version	
NCR420PAS	DFN2020D-6	SOT1118D	
NCR421PAS			

1.2. Features and benefits

- Stabilized output current of 10 mA without external resistor
- · Stabilized output current adjustable up to 150 mA when an external resistor is used
- High current accuracy at supply voltage variation
- Low voltage overhead of 1.4 V
- Reduces component count and board space
- High power dissipation of 530 mW
- Supply voltage up to 40 V
- Digital PWM input up to 10 kHz frequency for NCR421PAS
- AEC-Q101 qualified

1.3. Applications

- Constant current LED driver
- Generic constant current source
- Automotive applications (for example: interior lighting, dash board, instrumentation, number plate light)
- Increase stabilized output current by paralleling drivers

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2. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{EN}	enable voltage	I						
	NCR420PAS			-	-	40	V	
	NCR421PAS			-	-	4.5	V	
V _{out}	output voltage			-	-	40	V	
l _{out}	stabilized output current							
	NCR420PAS	V _{out} = 1.4 V; V _{EN} = 24 V	[1]	9	10	11	mA	
	NCR421PAS	V _{out} = 1.4 V; V _{EN} = 3.3 V	[1]	9	10	11	mA	

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$

3. Pinning information

Table 3	. Pinning			
Pin	Symbol	Description	Simplified outline	Symbol
1	VEN	enable voltage	6 5 4	IOUT GND N/C
2	REXT	external resistor		
3	N/C	not connected	7 8	
4	N/C	not connected		
5	GND	ground		
6	IOUT	output current	Transparent top view	VEN REXT N/C aaa-031469
7	IOUT	output current	DFN2020D-6	
8	N/C	not connected	(SOT1118D)	

4. Ordering information

Table 4. Ordering information

	Package		
number	Name	Description	Version
NCR420PAS	DFN2020D-6	1 , J	SOT1118D
NCR421PAS		outline package; no leads; 6 terminals; body 2 x 2 x 0.65 mm	

5. Marking

Table 5. Marking codes				
Type number	Marking code			
NCR420PAS	8C			
NCR421PAS	8D			

6. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
l _{out}	stabilized output current if external resistor is used			-	200	mA
V _{EN}	enable voltage		·			·
	NCR420PAS			-	40	V
	NCR421PAS			-	4.5	V
V _{out}	output voltage			-	40	V
V _R	reverse voltage		[1]	-	0.5	V
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	370	mW
			[3]	-	570	mW
			[4]	-	530	mW
			[5]	-	700	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Between all terminals.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-side copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-side copper, tin-plated; mounting pad for collector 1 cm².

[4] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated; mounting pad for collector 1 cm².



7. Thermal characteristics

[3]

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	R _{th(j-a)} thermal resistance from junction to ambient	in free air	[1]	-	-	338	K/W
		[2]	[2]	-	-	219	K/W
			-	-	236	K/W	
			[4]	-	-	179	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm².

Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated; mounting pad for collector 1 cm².





8. Characteristics

Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 1 mA; I _B = 0 A		40	-	-	V	
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 50 mA	[1]	200	350	-		
R _{int}	internal resistor	I _{Rint} = 10 mA		85	95	105	Ω	
V _{Rint}	voltage drop at internal resistor R _{int}	l _{out} = 10 mA	[1]	0.85	0.95	1.05	V	
I _{EN}	enable current					-1		
	NCR420PAS	V _{EN} = 24 V	[1]	-	1.2	-	mA	
	NCR421PAS	V _{EN} = 3.3 V	[1]	-	1.2	-	mA	
R _B	bias resistor				_			
	NCR420PAS			-	20	-	kΩ	
	NCR421PAS			-	1.5	-	kΩ	
l _{out}	stabilized output current							
	NCR420PAS	V _{EN} = 24 V; V _{out} = 1.4 V	[1]	9	10	11	mA	
	NCR421PAS	V _{EN} = 3.3 V; V _{out} = 1.4 V	[1]	9	10	11	mA	
l _{out}	stabilized output current							
	NCR420PAS at R _{ext} = 5.1 Ω	V _{EN} = 24 V; V _{out} > 2 V	[1]	-	150	-	mA	
	NCR421PAS at $R_{ext} = 5.1 \Omega$	V _{EN} = 3.3 V; V _{out} > 2 V	[1]	-	150	-	mA	
V _{out, min}	lowest sufficient output voltage overhead: V _{out} = V _{CC} - V _{LED}	I _{out} > 10 mA		-	1.4	-	V	
Δl _{out} /(l _{out} x ΔT _{amb})	stabilized output current change over ambient temperature							
	NCR420PAS	V _{EN} = 24 V; V _{out} > 2 V	[1]	-	-0.27	-	%/K	
	NCR421PAS	V _{EN} = 3.3 V; V _{out} > 2 V	[1]	-	-0.27	-	%/K	
$\Delta I_{out} / (I_{out} \times \Delta V_{CC})$	stabilized output current	change over supply voltage			- 1			
	NCR420PAS	V _{EN} = 24 V; V _{out} > 2 V	[1]	-	1	-	%/V	
	NCR421PAS	V _{EN} = 3.3 V; V _{out} > 2 V	[1]	-	1	-	%/V	

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.









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9. Application information

Figure 26 shows a typical application circuit for an LED driver. The constant current ensures a constant brightness in all LEDs. The output current can be adjusted between 10 mA and 150 mA by connecting resistor R_{ext} . Figures 7 and 17 give a first indication for choosing the external resitor R_{ext} . The minimum input voltage is given by voltage drop at the LED's V_{LED} and the maximum is governed by the maximum power dissipation

 V_{LED} + $V_{out, min}$ < V_{CC} < P_{tot} / I_{out} + V_{LED}



NCR421PAS can be used for PWM dimming or on/off function by driving the VEN pin. The enable voltage depends on the drive current, see Figure 23. Figure 27 shows a typical application where VEN is driven via a micro directly. To control more than one NCR421PAS devices by one microcontroller output, a shift register (for example 74AHC(T)594PW) can be used.



To savely drive currents that are above the limits of the NCR42xPAS, two or more devices can be parallel connected as illustrated in Figure 28. When choosing the same values for the external resistors, the drive current splits equally and the capability of handling excess power is doubled. Both, NCR420PAS and NCR421PAS can be used in this configuration.



10. Package outline

Table 9. Package outline



11. Soldering

Table 10. Soldering



12. Revision history

Table 11. Revision history					
Document ID	Release date	Data sheet status	Change notice	Supersedes	
NCR420PAS_NCR421PAS v.1	20200513	Product data sheet	-	-	

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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