# **BF991**

## N-channel dual-gate MOS-FET

Rev. 03 — 20 November 2007

**Product data sheet** 

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**NXP Semiconductors** 



## N-channel dual-gate MOS-FET

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#### **FEATURES**

• Protected against excessive input voltage surges by integrated back-to-back diodes between gates and source.

### **APPLICATIONS**

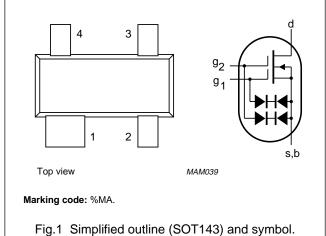
- VHF applications such as:
  - VHF television tuners and FM tuners
  - Professional communication equipment.

#### **PINNING**

PIN	SYMBOL	DESCRIPTION
1	s, b	source
2	d	drain
3	<b>g</b> <sub>2</sub>	gate 2
4	<b>9</b> 1	gate 1

#### **DESCRIPTION**

Depletion type field-effect transistor in a plastic SOT143 microminiature package with interconnected source and substrate.



### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>DS</sub>	drain-source voltage		_	20	V
I <sub>D</sub>	drain current		_	20	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>amb</sub> = 60 °C	_	200	mW
Tj	junction temperature		_	150	°C
Y <sub>fs</sub>	transfer admittance	$f = 1 \text{ kHz}; I_D = 10 \text{ mA}; V_{DS} = 10 \text{ V}; V_{G2-S} = 4 \text{ V}$	14	_	mS
C <sub>ig1-s</sub>	input capacitance at gate 1	$f = 1 \text{ MHz}$ ; $I_D = 10 \text{ mA}$ ; $V_{DS} = 10 \text{ V}$ ; $V_{G2-S} = 4 \text{ V}$	2.1	_	pF
C <sub>rs</sub>	feedback capacitance	$f = 1 \text{ MHz}; I_D = 10 \text{ mA}; V_{DS} = 10 \text{ V}; V_{G2-S} = 4 \text{ V}$	20	_	fF
F	noise figure	$f = 200 \text{ MHz}; G_S = 2 \text{ mS}; B_S = B_{Sopt}; \\ I_D = 10 \text{ mA}; V_{DS} = 10 \text{ V}; V_{G2-S} = 4 \text{ V}$	1	2	dB

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#### **LIMITING VALUES**

In according with the Absolute Maximum Rating System (IEC 134).

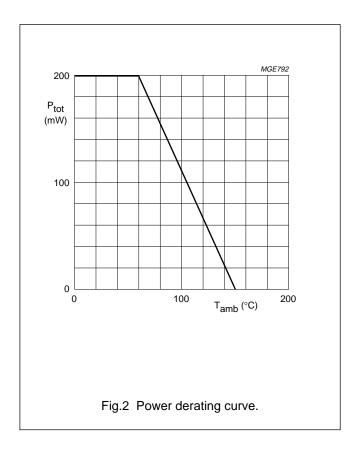
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>DS</sub>	drain-source voltage		_	20	V
I <sub>D</sub>	drain current (DC)		_	20	mA
I <sub>D(AV)</sub>	average drain current		_	20	mA
I <sub>G1-S</sub>	gate 1-source current		_	±10	mA
I <sub>G2-S</sub>	gate 2-source current		_	±10	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>amb</sub> = 60 °C; note 1	_	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT	
R <sub>th j-a</sub>	thermal resistance from junction to ambient	in free air; note 1	460	K/W	

## Note to the Limiting values and the Thermal characteristics

1. Device mounted on a ceramic substrate of  $8 \times 10 \times 0.7$  mm.



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#### STATIC CHARACTERISTICS

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>G1-SS</sub>	gate 1 cut-off current	$V_{G1-S} = 5 \text{ V}; V_{G2-S} = V_{DS} = 0$	_	50	nA
I <sub>G2-SS</sub>	gate 2 cut-off current	$V_{G2-S} = 5 \text{ V}; V_{G1-S} = V_{DS} = 0$	_	50	nA
I <sub>DSS</sub>	drain current	V <sub>DS</sub> = 10 V; V <sub>G1-S</sub> = 0; V <sub>G2-S</sub> = 4 V	4	25	mA
V <sub>(BR)G1-SS</sub>	gate 1-source breakdown voltage	$I_{G1-SS} = 10 \text{ mA}; V_{G2-S} = V_{DS} = 0$	6	20	V
V <sub>(BR)G2-SS</sub>	gate 2-source breakdown voltage	$I_{G2-SS} = 10 \text{ mA}; V_{G1-S} = V_{DS} = 0$	6	20	V
V <sub>(P)G1-S</sub>	gate 1-source cut-off voltage	$I_D = 20 \mu A; V_{DS} = 10 V; V_{G2-S} = 4 V$	-	-2.5	V
V <sub>(P)G2-S</sub>	gate 2-source cut-off voltage	$I_D = 20 \mu A; V_{DS} = 10 V; V_{G1-S} = 0$	_	-2.5	V

## **DYNAMIC CHARACTERISTICS**

Measuring conditions (common source):  $I_D$  = 10 mA;  $V_{DS}$  = 10 V;  $V_{G2-S}$  = 4 V;  $T_{amb}$  = 25 °C.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Y <sub>fs</sub>	transfer admittance	f = 1 kHz	10	14	_	mS
C <sub>ig1-s</sub>	input capacitance at gate 1	f = 1 MHz	_	2.1	_	pF
C <sub>ig2-s</sub>	input capacitance at gate 2	f = 1 MHz	_	1	_	pF
C <sub>rs</sub>	feedback capacitance	f = 1 MHz	_	20	_	fF
C <sub>os</sub>	output capacitance	f = 1 MHz	_	1.1	_	pF
F	noise figure	$f = 100 \text{ MHz}; G_S = 1 \text{ mS}; B_S = B_{Sopt}$	_	0.7	1.7	dB
		$f = 200 \text{ MHz}; G_S = 2 \text{ mS}; B_S = B_{Sopt}$	_	1	2	dB
G <sub>tr</sub>	transducer gain; note 1	$f = 100 \text{ MHz}; G_S = 1 \text{ mS}; B_S = B_{Sopt};$ $G_L = 0.5 \text{ mS}; B_L = B_{Lopt}$	_	29	_	dB
		$f = 200 \text{ MHz}; G_S = 2 \text{ mS}; B_S = B_{Sopt}; G_L = 0.5 \text{ mS}; B_L = B_{Lopt}$	_	26	_	dB

#### Note

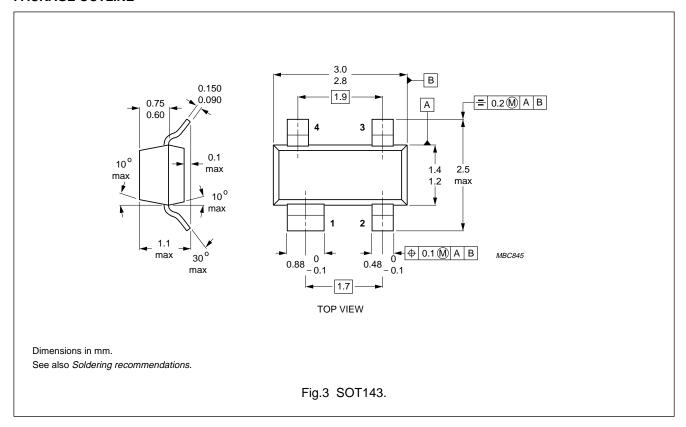
1. Crystal mounted in a SOT103 package.

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## **PACKAGE OUTLINE**



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### N-channel dual-gate MOS-FET

## Legal information

#### **Data sheet status**

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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## **Revision history**

## **Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BF991_N_3	20071120	Product data sheet	-	BF991_2
Modifications:	<ul> <li>Fig. 1 on pag</li> </ul>	ge 2; Figure note changed		
BF991_2	19910401	Product specification	-	BF991_SF_1
BF991_SF_1	-	-	-	-

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