



Rev. V1

The RF Line Controlled "Q" Broadband Power Transistor 125W, 30 to 500MHz, 28V

Designed primarily for wideband large–signal output and driver amplifier stages in the 30 to 500 MHz frequency range.

- Specified 28 V, 400 MHz characteristics Output power = 125 W Typical gain = 10 dB Efficiency = 55% (typ.)
- Built-in input impedance matching networks for broadband operation
- Push-pull configuration reduces even numbered harmonics
- Gold metallization system for high reliability
- 100% tested for load mismatch



Product Image



The MRF392 is two transistors in a single package with separate base and collector leads and emitters common. This arrangement provides the designer with a space saving device capable of operation in a push–pull configuration.

PUSH-PULL TRANSISTORS

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Collector–Emitter Voltage	VCEO	30	Vdc	
Collector-Base Voltage	V _{CBO}	60	Vdc	
Emitter-Base Voltage	VEBO	4.0	Vdc	
Collector Current — Continuous	IC	16	Adc	
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	270 1.54	Watts W/°C	
Storage Temperature Range	T _{stg}	-65 to +150	°C	
Junction Temperature	Tj	200	°C	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _{0JC}	0.65	°C/W

NOTE:

 This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF push-pull amplifier.

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ELECTRICAL CHARACTERISTICS (T_C = 25° C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS (1)					
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, I _B = 0)	V(BR)CEO	30	-	-	Vdc
Collector–Emitter Breakdown Voltage (IC = 50 mAdc, VBE = 0)	V(BR)CES	60	-	-	Vdc
Emitter–Base Breakdown Voltage (I _E = 5.0 mAdc, I _C = 0)	V(BR)EBO	4.0	-	-	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	ICBO	_	-	5.0	mAdc
ON CHARACTERISTICS (1)					
DC Current Gain (I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)	hFE	40	60	100	_
DYNAMIC CHARACTERISTICS (1)				•	
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	Cob	_	75	95	pF
FUNCTIONAL TESTS (2) — See Figure 1	•			•	
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 125 W, f = 400 MHz)	Gpe	8.0	10	-	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 125 W, f = 400 MHz)	η	50	55	-	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 125 W, f = 400 MHz, VSWR = 30:1, all phase angles)	Ψ	No Degradation in Output Power			

NOTES:

1. Each transistor chip measured separately.

2. Both transistor chips operating in push-pull amplifier.



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L5, L6 — 3–1/2 Turns #18 AWG, 0.200 ID



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Figure 4. Output Power versus Supply Voltage





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Figure 6. Series Equivalent Input/Output Impedance

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- U 4 PL Q NOTES: DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. ⊕ Ø 0.76 (0.030) M A M BM κ MILLIMETERS INCHES MIN MAX MIN MAX DIM A 22.60 23.11 0.890 0.910 R -B-9.52 10.03 0.375 0.395 В 6.65 7.16 0.262 0.282 С 1.60 1.95 0.063 0.077 D ¥ Е 2.94 3.40 0.116 0.134 2.87 3.22 0.113 0.127 κ F 7 5 6 16.51 BSC 0.650 BSC G Н 4.01 4.36 0.158 0.172 0.07 0.15 0.003 0.006 4.34 4.90 0.171 0.193 J D 4 PL ĸ F 4 PL 12.45 12.95 0.490 0.510 L 45°NOM 45°NOM 1.051 11.02 0.414 0.434 м V 2 PL N 3.04 3.35 0.120 0.132 Q Q 3.04 3.35 0.120 0.132 R 9.90 10.41 0.390 0.410 U 1.02 1.27 0.040 0.050 V 0.64 0.89 0.025 0.035 G -A-STYLE 1: PIN 1. EMITTER (COMMON) J 2. COLLECTOR 3. COLLECTOR Ν 4. EMITTER (COMMON) 5. EMITTER (COMMON) 6. BASE BASE C 7 Н 8. EMITTER (COMMON) Е -T- SEATING PLANE 1 CASE 744A-01 ISSUE C

PACKAGE DIMENSIONS



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