# **Power MOSFET**

# 60 V, 78 m $\Omega$ , 4.5 A, N-Channel

Automotive Power MOSFET designed to minimize gate charge and low on resistance. AEC-Q101 qualified MOSFET and PPAP capable suitable for automotive applications.

#### **Features**

- 4.5 V Drive
- High ESD Protection
- Low On-Resistance
- CPH6 Package is Pin-Compatible with SOT-26
- Pb-Free, Halogen Free and RoHS Compliance

#### **Typical Applications**

- Load Switch
- Motor Drive

#### **Specifications**

#### **ABSOLUTE MAXIMUM RATINGS** $(T_a = 25^{\circ}C)$

Parameter	Symbol	Value	Unit
Drain to Source Voltage	$V_{DSS}$	60	V
Gate to Source Voltage	$V_{GSS}$	±20	V
Drain Current (DC) (Note 1)	I <sub>D</sub>	4.5	Α
Drain Current (DC) (Note 2)	1	3.5	Α
Drain Current (Pulse) PW ≤ 10 µs, duty cycle ≤ 1%	I <sub>DP</sub>	18	Α
Power Dissipation Ta = 25°C (Note 1)	P <sub>D</sub>	1.9	W
Power Dissipation Ta = 25°C (Note 2)		0.97	W
Junction Temperature and Storage Temperature	Tj, Tstg	–55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

Paramet	er	Symbol	Value	Unit
Junction to Ambient	(Note 1)	$R_{\theta JA}$	78.1	°C/W
	(Note 2)	]	153	°C/W

- 1. Surface mounted on ceramic substrate (900 mm $^2 \times$  0.8 mm).
- 2. Surface mounted on FR4 board using a 92 mm<sup>2</sup>, 1 oz. Cu pad.

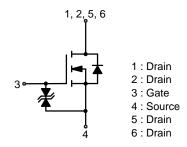


## ON Semiconductor®

#### www.onsemi.com

V <sub>DSS</sub>	R <sub>DS</sub> (on) MAX	I <sub>D</sub> MAX
60 V	78 mΩ @ 10 V	450
60 V	120 mΩ @ 4.5 V	4.5 A

# ELECTRICAL CONNECTION N-Channel



#### MARKING DIAGRAM





#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V	60	_	_	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	1.0	μΑ
Gate to Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate Threshold Voltage	V <sub>GS</sub> (th)	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.2	_	2.6	V
Forward Transconductance	9 <sub>FS</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2 A	_	3.0	_	S
Static Drain to Source On–State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> = 2 A, V <sub>GS</sub> = 10 V	-	60	78	mΩ
		I <sub>D</sub> = 1 A, V <sub>GS</sub> = 4.5 V	-	84	120	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 20 V, f = 1 MHz	_	505	_	pF
Output Capacitance	Coss		_	57	_	pF
Reverse Transfer Capacitance	Crss		_	37	_	pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See Figure 1	-	7.3	-	ns
Rise Time	t <sub>r</sub>		_	9.8	-	ns
Turn-OFF Delay Time	t <sub>d</sub> (off)		_	40	-	ns
Fall Time	t <sub>f</sub>		_	24	-	ns
Total Gate Charge	Qg	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 4.5 \text{ A}$	_	10	-	nC
Gate to Source Charge	Qgs		-	1.6	-	nC
Gate to Drain "Miller" Charge	Qgd		_	2.1	-	nC
Forward Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 4.5 A, V <sub>GS</sub> = 0 V	_	0.86	1.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

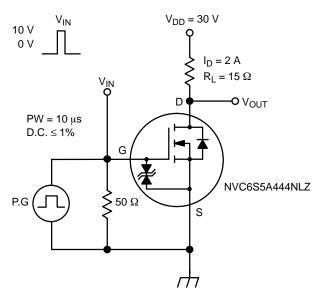
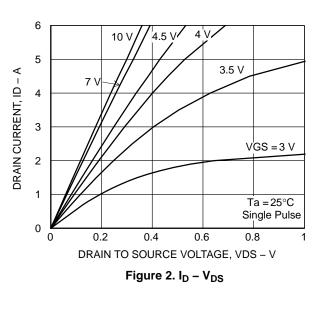


Figure 1. Switching Time Test Circuit

#### TYPICAL CHARACTERISTICS



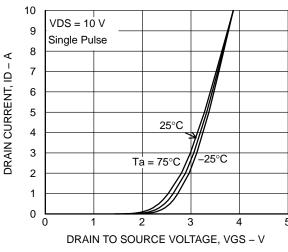
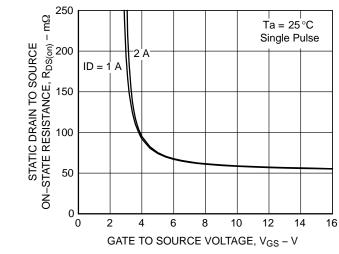


Figure 3. I<sub>D</sub> - V<sub>GS</sub>



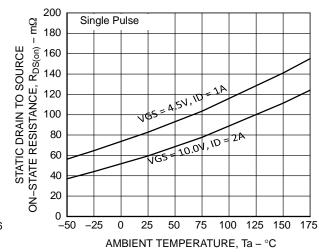
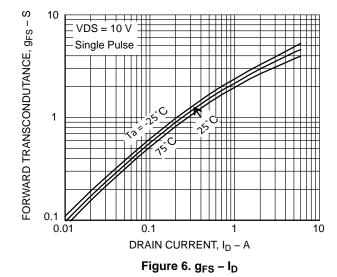


Figure 4. R<sub>DS</sub>(on) - V<sub>GS</sub>

Figure 5. R<sub>DS</sub>(on) – Ta



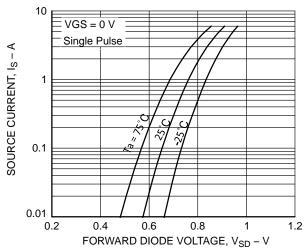


Figure 7. I<sub>S</sub> - V<sub>SD</sub>

#### **TYPICAL CHARACTERISTICS**

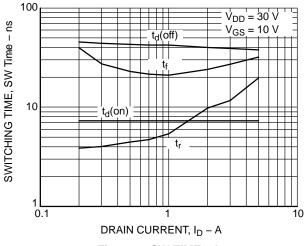


Figure 8. SW TIME –  $I_D$ 

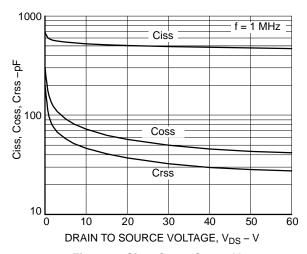


Figure 9. Ciss, Coss, Crss - V<sub>DS</sub>

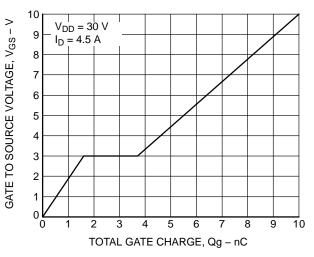


Figure 10. V<sub>GS</sub> - Qg

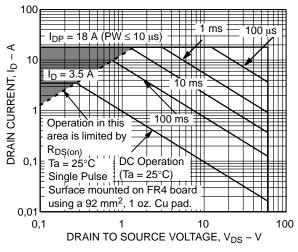


Figure 11. SOA

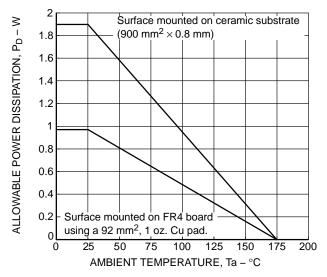


Figure 12. P<sub>D</sub> - Ta

#### TYPICAL CHARACTERISTICS

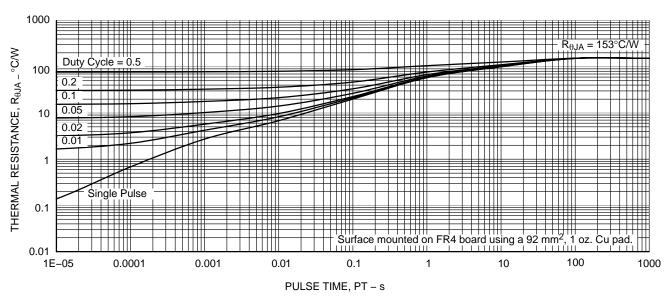


Figure 13.  $R_{\theta JA}$  – PULSE TIME

#### **DEVICE ORDERING INFORMATION**

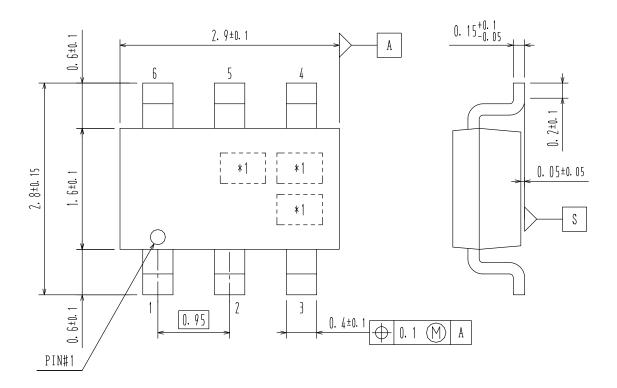
Device	Marking	Package	Shipping <sup>†</sup>
NVC6S5A444NLZT1G	ZW	CPH6	3,000 / Tape & Reel
NVC6S5A444NLZT2G		(Pb-Free / Halogen Free)	

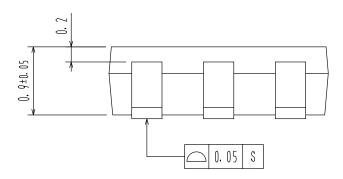
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Since the NVC6S5A444NLZ is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

CPH6 CASE 318BD ISSUE O

**DATE 30 NOV 2011** 





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