

compiled binary file to the storage device.
Use the free <u>LPCXpresso IDE/Debugger</u> together with, for example, an LPC-Link, a <u>10-pos IDC Ribbon cable</u> and connect to the SWD interface (10-pin JTAG connector) on the board, see figure below. This allows you to both



 Use a debugger and JTAG adapter of your choice which supports the Cortex-M3 ARM core and more specifically the NXP LPC1343 microcontroller. You might need an adapter such as the <u>10-pin to 20-pin JTAG Adpater Kit</u> between your JTAG solution and the target board.

Overview Specification	MCU	Related Products	Resources	FAQ
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## LPC1343 QuickStart Board Processor NXP's Cortex-M3 LPC1343 microcontroller in 33-pin HVQFN package. Program Flash 32 KB Data Memory 8 KB 12.000 MHz crystal for maximum execution speed and standard serial bit rates, including Clock Crystals USB requirements. The LPC1343 runs at frequencies up to 72 MHz. Interfaces / • All LPC1343 I/O pins are available on edge expansion connectors, in DIL-30 structure Connectors suitable for bread board prototyping (dual 15 pos, 100 mil/2.54 mm pitch rows, 700 mil/17.78 mm apart). On-board USB Device interface, with mini-B USB connector and proper ESD protection. Dimensions 21 x 40 mm Flexible powering, with on-board 150mA 3.3V voltage regulator (can be powered from USB Power connector or an external +5V supply). • All LPC1343 I/O pins are available on connectors. Connectors • mini-B USB connector. SWD/JTAG connector (0 mil/1.27 mm pitch, standard SWD/JTAG connector). Other • 32 Kbit I2C E2PROM for storing non-volatile parameters. · Onboard reset generation and reset push-button. Push-button for enabling Bootloader mode of the LPC1343. • LED on pin PIO0\_7. · 2 pcs 1x15 pinlists included, but not soldered.

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