

M/A-COM Products

Released - Rev. 07.07

# The RF Line NPN Silicon Power Transistor 80W, 3.0-200MHz, 28V

Designed primarily for wideband large–signal output amplifier stages in the 30–200 MHz frequency range.

- Guaranteed performance at 150 MHz, 28 Vdc Output power = 80 W Minimum gain = 10 dB
- Built-in matching network for broadband operation
- 100% tested for load mismatch at all phase angles with 30:1 VSWR
- Gold metallization system for high reliability applications

## Product Image



### MAXIMUM RATINGS

Rating	Symbol 3 1	Value	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	35	Vdc
Collector–Base Voltage	V <sub>CBO</sub>	65	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector Current — Continuous Peak	Ι <sub>C</sub>	9.0 13.5	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1) Derate above 25°C	PD	220 1.26	Watts W/∘C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R <sub>eJC</sub>	0.8	°C/W

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	35	_	—	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 50 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)CES</sub>	65	_	—	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 50 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	65	_	—	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 5.0 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	4.0	_	—	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	—	—	5.0	mAdc

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- North America Tel: 800.366.2266 / Fax: 978.366.2266
- Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300
- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298
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ON CHARACTERISTICS					
DC Current Gain (I <sub>C</sub> = 4.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	10	-	80	-
DYNAMIC CHARACTERISTICS	ł	•	•		•
Output Capacitance (V <sub>CB</sub> = 28 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	100	130	pF
NOTE: 1. This device is designed for RF operation. The total device d ELECTRICAL CHARACTERISTICS — continued (T <sub>C</sub> = 2)		,	the device is o	operated as ar	(continued) n RF amplifier.
Characteristic	Symbol	Min	Тур	Max	Unit
NARROW BAND FUNCTIONAL TESTS (Figure 1)		1		1	-
Common–Emitter Amplifier Power Gain (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 80 W, f = 150 MHz)	G <sub>PE</sub>	10	13	-	dB
Collector Efficiency (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 80 W, f = 150 MHz)	η	55	-	-	%
Load Mismatch (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 80 W CW, f = 150 MHz,	ψ	No Degradation in Output Power			

VSWR = 30:1 all phase angles)

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Figure 4. Output Power versus Supply Voltage



Figure 5. Output Power versus Supply Voltage

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 $Z_{OL}$  = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 7. Series Equivalent Input-Output Impedance

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### PACKAGE DIMENSIONS



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