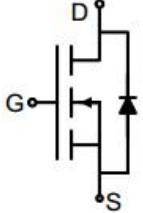
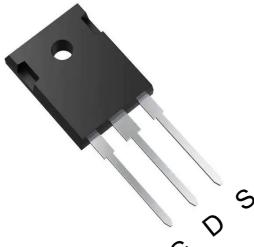


N-Channel Enhancement Mode Power MOSFET

<p>Description</p> <p>The GC120N65QF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.</p> <p>General Features</p> <ul style="list-style-type: none"> ● V_{DS} 650V ● I_D (at $V_{GS} = 10V$) 30A ● $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 120mΩ ● 100% Avalanche Tested ● RoHS Compliant ● Ultra-fast body diode <p>Application</p> <ul style="list-style-type: none"> ● Power switch ● DC/DC converters 	 <p>Schematic diagram</p>  <p>TO-247</p>
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Ordering Information			
Device	Package	Marking	Packaging
GC120N65QF	TO-247	GC120N65	30pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	650	V
Continuous Drain Current	I_D	30	A
Pulsed Drain Current (note1)	I_{DM}	120	A
Gate-Source Voltage	V_{GS}	± 30	V
Power Dissipation	P_D	96.1	W
Single pulse avalanche energy (note2)	E_{AS}	1250	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	°C

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	50	°C/W
Maximum Junction-to-Case	R_{thJC}	1.3	°C/W

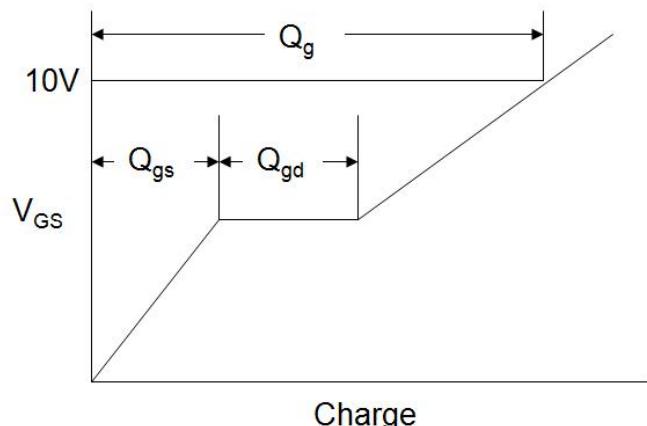
Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	20	μA
Gate-Source Leakage	I_{GSS}	$V_{\text{GS}} = \pm 30\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.0	4.0	5.0	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 38\text{A}$	--	97	120	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{GS}} = 5\text{V}, I_D = 38\text{A}$	--	30	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 275\text{V}, f = 1.0\text{MHz}$	--	3100	--	pF
Output Capacitance	C_{oss}		--	91	--	
Reverse Transfer Capacitance	C_{rss}		--	24	--	
Total Gate Charge	Q_g	$V_{\text{DD}} = 275\text{V}, I_D = 38\text{A}, V_{\text{GS}} = 10\text{V}$	--	68	--	nC
Gate-Source Charge	Q_{gs}		--	14	--	
Gate-Drain Charge	Q_{gd}		--	25	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 275\text{V}, I_D = 38\text{A}, R_G = 4.7\Omega$	--	10	--	ns
Turn-on Rise Time	t_r		--	5	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	80	--	
Turn-off Fall Time	t_f		--	14	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	30	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 38\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = 38\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A}/\mu\text{s}$	--	1.4	--	nC
Reverse Recovery Time	T_{rr}		--	185	--	ns

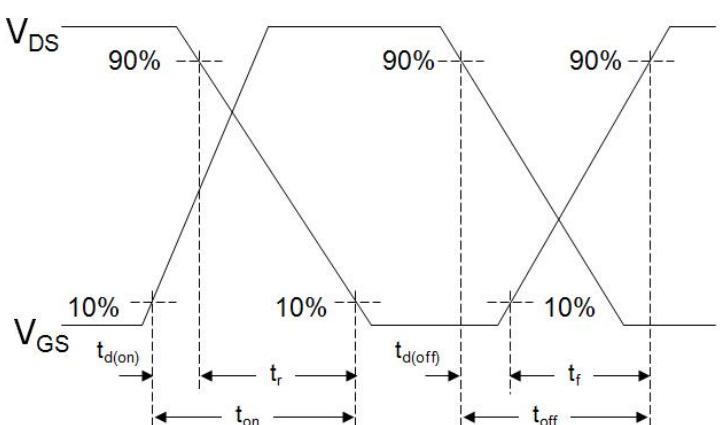
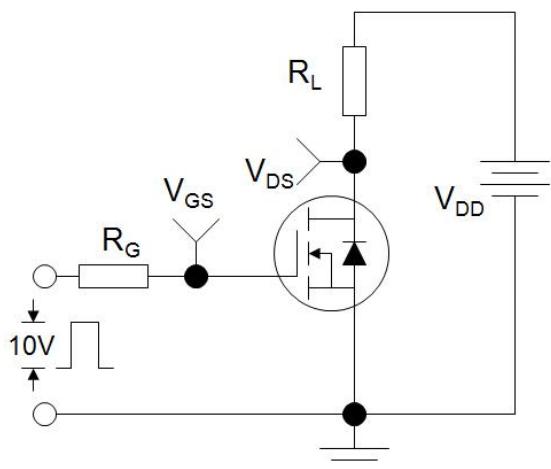
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition : $T_J=25^\circ\text{C}$, $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $R_G=25\Omega$
3. Identical low side and high side switch with identical R_G

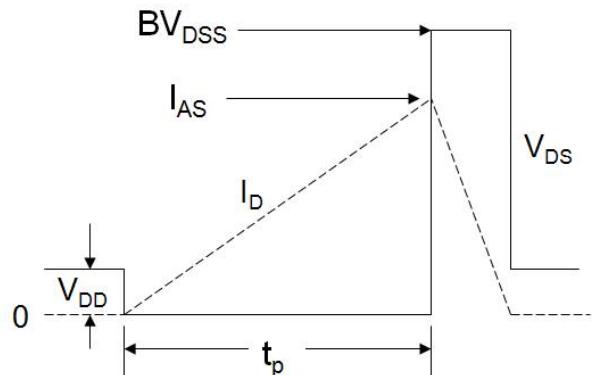
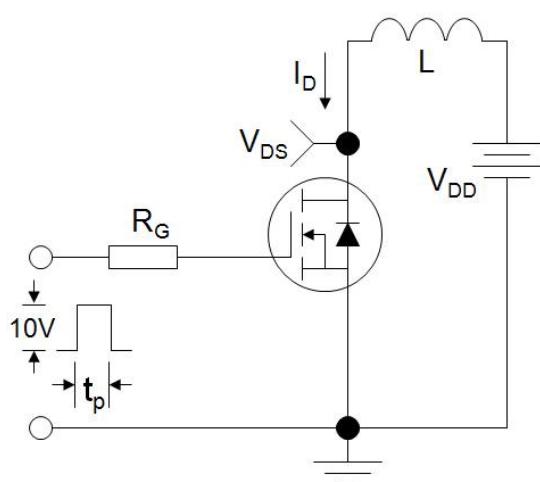
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

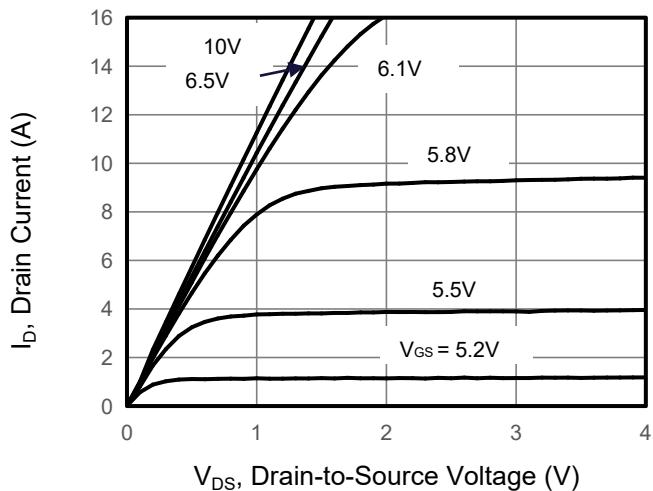


Figure 2. Transfer Characteristics

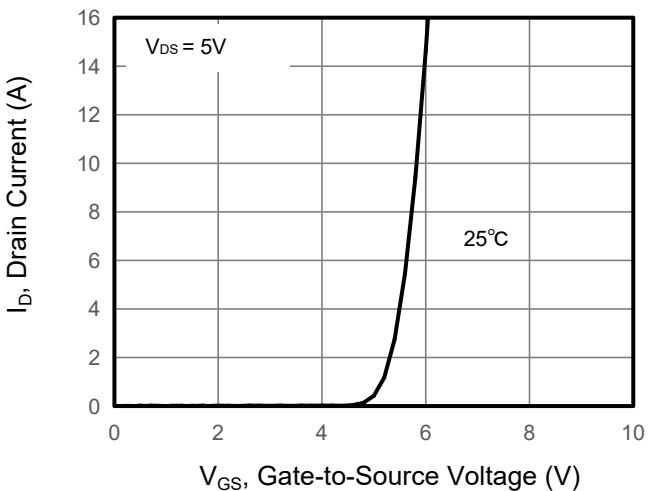


Figure 3. Drain Source On Resistance

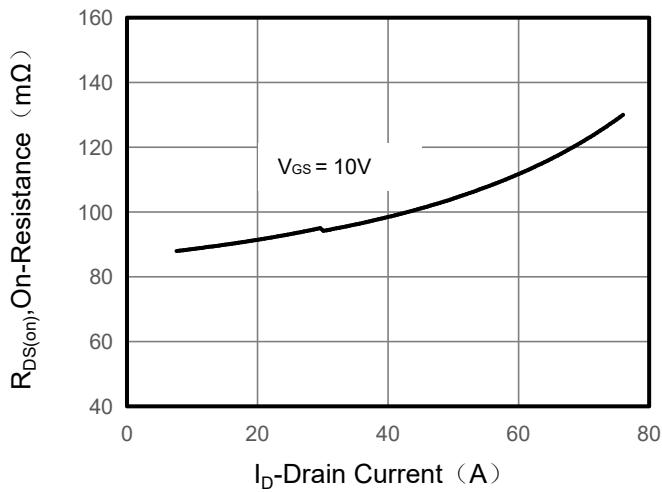


Figure 4. Gate Charge

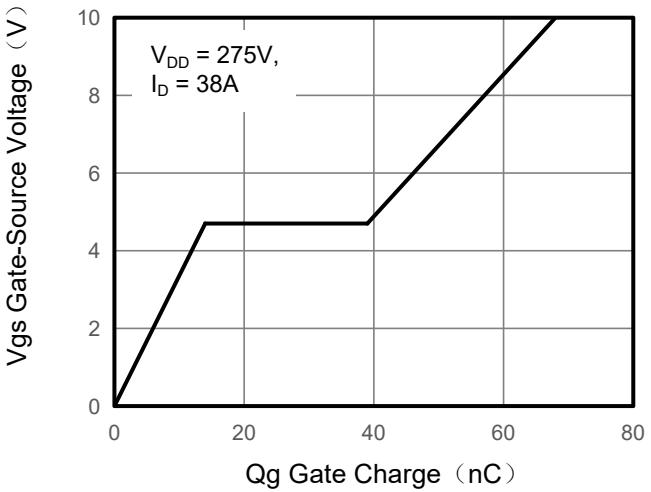


Figure 5. Capacitance

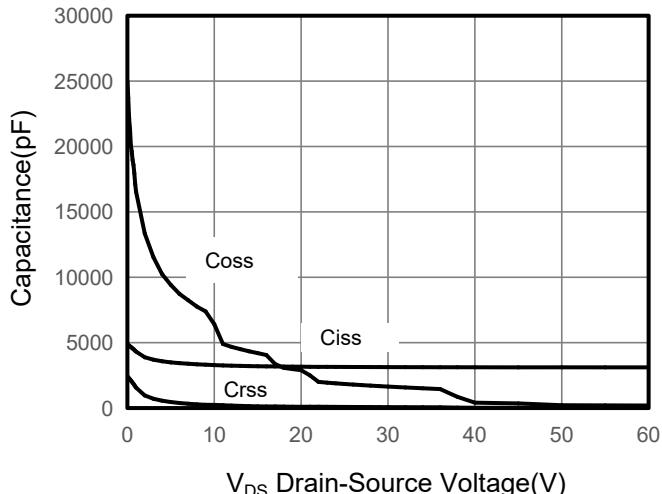
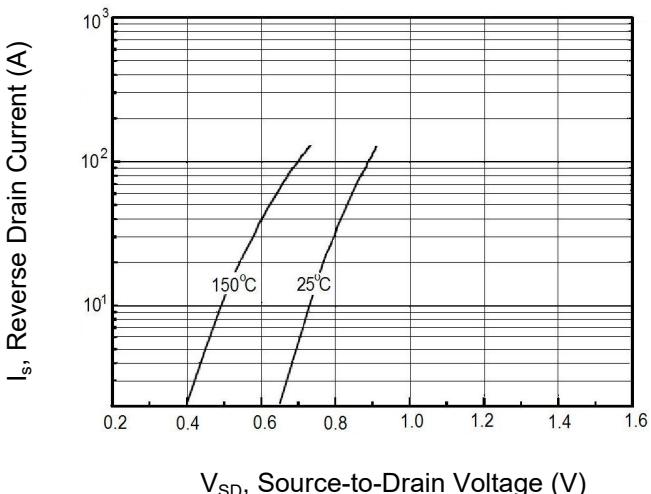


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

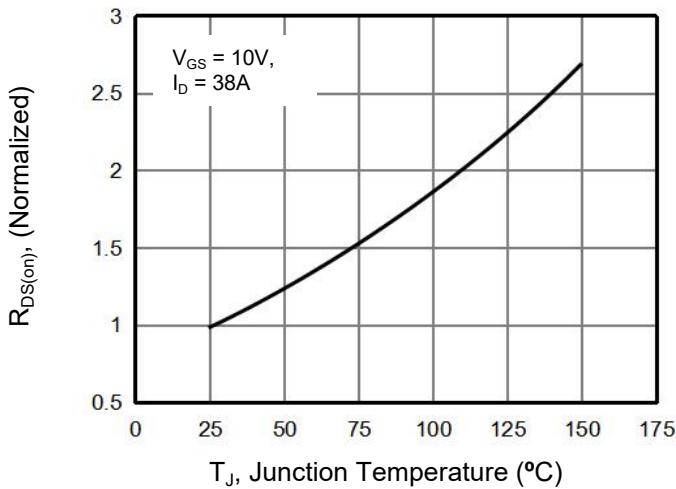


Figure 8. Safe Operation Area

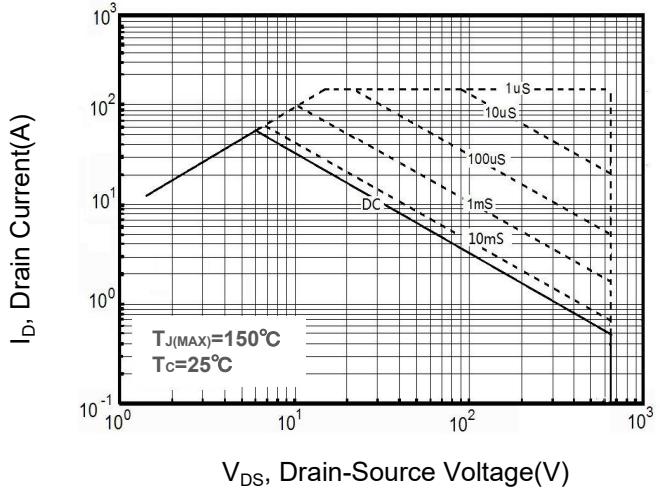
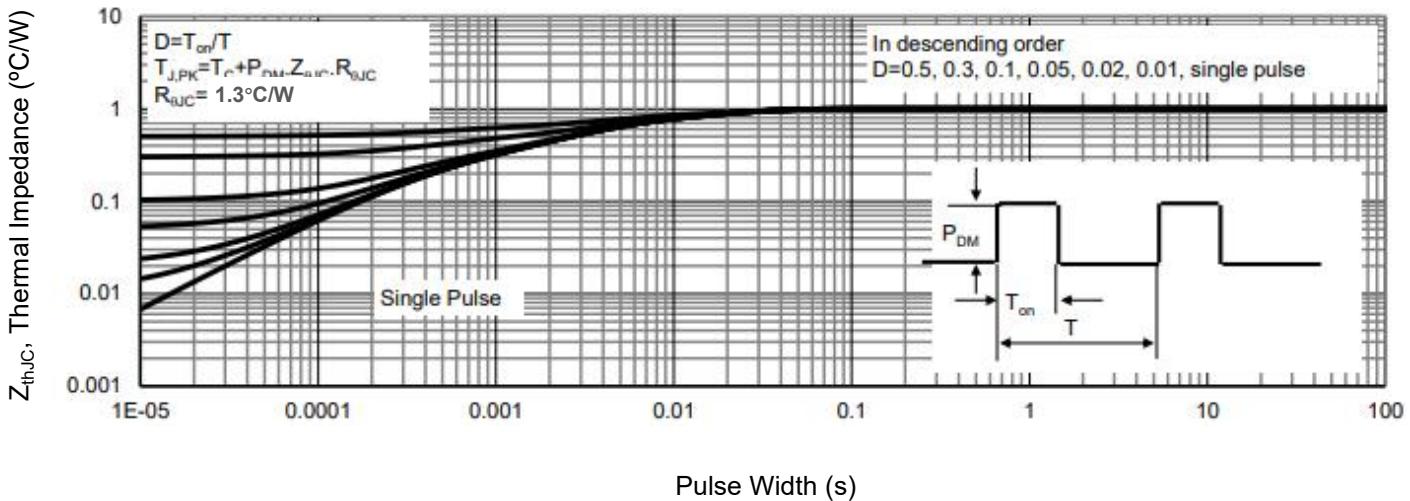
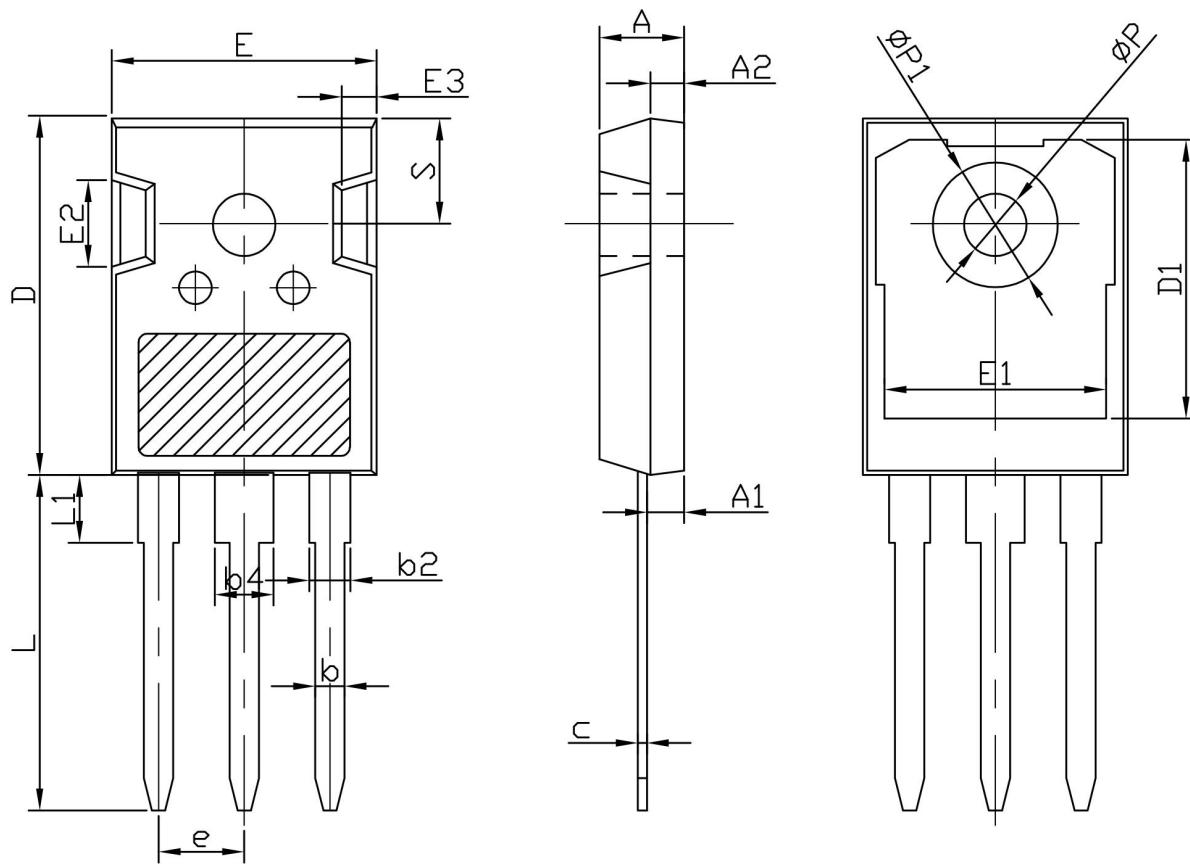


Figure 9. Normalized Maximum Transient Thermal Impedance



TO-247 Package Information



SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
φP	3.40	3.60	3.80
φP1	-	-	7.30
S	6.15BSC		