HF0402B102R-10

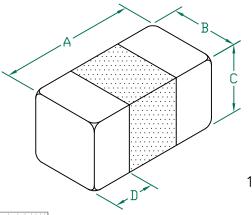
UNCONTROLLED

PHYSICAL DIMENSIONS:

A 1.01 [.040] ± 0.18 [.007]

0.50 [.020] ± 0.20 [.008] ± 0.20 [.008] 0.50 [.020]

D 0.30 [.012] MAX.

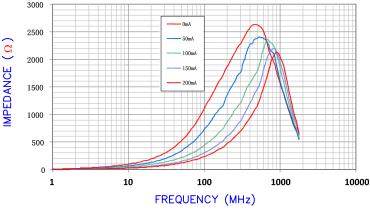


ELECTRICAL CHARACTERISTICS:										
	Z@1000MHz (Ω)	Z@100MHz (Ω)	DCR (Ω)	RATED CURRENT						
Nominal	1800	1000	_							
Minimum	1080	750	_							
Maximum	2520	1250	1.25	200mA						

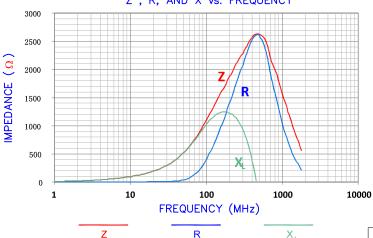
NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED PER CURRENT EIA SPECIFICATIONS 7" REELS, 10K PCS/REEL, PAPER CARRIER TAPE
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMPERATURE TEMP: -40° C~+125° C (INCLUDING SELF-HEATING)

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS

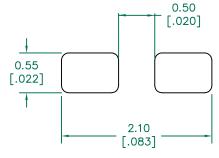






AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 400-10-0057

LAND PATTERNS FOR REFLOW SOLDERING

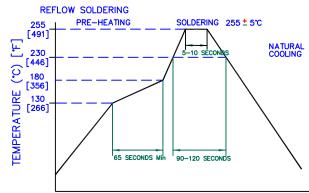


(For wave soldering, add 0.763 (.030 to this dimension.)

ORIGINAL DRAFT

DESCRIPTION

RECOMMENDED SOLDERING CONDITIONS



HF0402B102R-10-B

2 of 2

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird			
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				with the understanding that no	1 2	aird	
				copies shall be made without the		94 I I I	y
				written consent of Laird Tech. All			
				rights to design or invention are reserved.			
				PROJECT/PART NUMBER:	REV	PART TYPE:	DRAV
				HF0402B102R-10	В	CO-FIRE	١.
В	ADD 1000MHz IMPEDANCE	06/20/13	Q	111 0402610211-10	ַ	OO TIKE	`
	ADD OPERATING TEMPERATURE			DATE: 06 /11 /10 SCALE	NITO	SHEET:	

DATE INT