LCD Module Technical S	specification	rst Edition Jan. 15, 2010 nal Revision ******
туре No. Т-55520GD057J-LV	V-ACN	
Customer : OPTREX STA	NDARD	
Customer's Product No :		
OPT	REX CORPORATION	
Che	oved: Noboru Wada Design G. Yasuo Yamaguchi Design G. Satoshi Sano Design G.	
APPROVED		
Ву		
Signature : Date :		
Please return this specification within If not returned within two month, spec as having been accepted.		
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1.Application

This specification applies to 5.7" color TFT-LCD module (T-55520GD057J-LW-ACN).

2. General Specifications

Dot Pixels	:	320 $ imes$ 3 [R.G.B] (W) $ imes$ 240 (H) dots
Dot Size	:	0.12×3 [R.G.B] (W) ×0.36 (H) mm
Pixel Arrangement	:	RGB-Stripe
Color Depth	:	262,144 colors
Viewing Area	:	117.88 (W) $ imes$ 88.24 (H) mm
Outline Dimensions	:	144.0 (W) \times 104.6* (H) \times 13.5max. (D) mm * Without LED Cable
Weight	:	255.0g max.
LCD Type	j	ATS-25837 (TFT / Normally white-mode / Transmissive) 6:00 (Angle of Least Color Inversion)
Interface		18-bit RGB interface(6-bit / color)
Backlight	:	LED Backlight / White
Lead free		Our product corresponds to lead free. Lead free is defined as below:) The solder used in the LCD module.) Electrical components (Terminal section) used in the LCD module. Any lead used within the electrical component does not apply to our module definition of lead free.
RoHS regulation	:	To our best knowledge, this product satisfies material requirement of RoHS regulation. Our company is doing the best efforts to obtain the equivalent certificate from our suppliers.
3. Operating Condition	<u>15</u>	

Item Conditions Temperature Range Remark Operating Temperature Range PNL Surface -20~70°C Note1 Storage Temperature Range PNL Surface -30~80°C Note1:Operating temperature range defines the operation only and the contrast, response time and other display optical characteristics are set at Ta=+25°C.

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6.<u>I/O Terminal</u>

6.1.CN1 Pin Assignment (INTERFACE SIGNAL)

Used connector 08-6260-033-340-829+

Corresponding FPC: P0.5, 33pin, t=0.3mm

1 2 3	GND	Power Supply (0V, GND)
3		
	СК	Clock Signal
	HSYC	Horizontal Sync Input
4	VSYC	Vertical Sync Input
5	GND	Power Supply (0V, GND)
6	R0	Red Data Signal
7	R1	Red Data Signal
8	R2	Red Data Signal
9	R3	Red Data Signal
10	R4	Red Data Signal
11	R5	Red Data Signal
12	GND	Power Supply (0V, GND)
13	GO	Greeen Data Signal
14	G1	Greeen Data Signal
15	G2	Greeen Data Signal
16	G3	Greeen Data Signal
17	G4	Greeen Data Signal
18	G5	Greeen Data Signal
19	GND	Power Supply (0V, GND)
20	В0	Blue Data Signal
21	B1	Blue Data Signal
22	B2	Blue Data Signal
23	B3	Blue Data Signal
24	B4	Blue Data Signal
25	B5	Blue Data Signal
26	GND	Power Supply (0V, GND)
27	ENAB	Input Data Enable Control
28	VCC(3.3V)	Power Supply for Logic
29	VCC(3.3V)	Power Supply for Logic
30	R/L	Control the shift direction of device internal shift resister
31	U/D	Set the Up/Down scan direction
32	NC	Non Connection
33	GND	Power Supply (0V, GND)
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6.2.CN2 Pin Assignment (Backlight)

Corresponding connector : BHR-02(8.0)VS-IN(JST)

No.	Symbol	Functional Discription
1	Cathode	LED Cathode Terminal
2	Anode	LED Anode Terminal

7. Electrical Specifications

7.1.Absolute Maximum Ratings

				Ta=-	20~70°C	, VSS=0V
Parameter	Symbol	Condition	s	Min.	Max.	Units
Supply Voltage for LCD	vcc	-		-0.3	+7.0	Y
					a F	
7.2.DC Characteristic	cs	P A A		Ta	=-20~70°	C, VSS=0V
Parameter	Symbol	Min.	Тур	. 🛛 🛛	Max	Units
Supply Voltage for LCD	VCC	3.0	3.3		3.6	V
High Level	VIH	0.7VCC	-	,	vcc	V
Low Level Input Voltage	VIL	0	-	0.	3VCC	V
Power Supply Current for LCD	ICC	-	100)	150	mA

A) Typical current condition All black pattern with frame 240 line mode. VCC=+3.3V, fH=15.7kHz, fv=60Hz, fcLk=6.4MHz

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7.3.AC Characteristics

7.3.1.Digital Parallel RGB Interface Timing

		Ta=	-20~70°C	, VCC=3	.0~3.6V
Parameter	Symbol	Min.	Тур.	Max.	Units
CK Frequency	1/tosc	-	6.4	-	MHz
CK Period	tosc	-	156	-	ns
CK High Pulse Width Time	toscH	12	-	-	ns
CK Low Pulse Width Time	toscL	12	-	-	ns
CK Pulse Duty ratio	toscH/ tosc	-	50	-	%
Data Setup time	tds	12	-	-	ns
Data Hold time	tdh	12	-	-	ns
HSYC (Horizontal Sync.) Signal Cycle	ТН	-	62.8	-	μ S
		-	408	450	clk
HSYC Pulse Width	THs	5	30	§ -//	clk
Horizontal Display Term	THd	a - A	320		clk
ENAB Setup Time	tens	12			ns
ENAB Hold Time	tenh	12	-	-	ns
VSYC (Vertical Syne.) Signal Cycle	τν	I F	262	350	Line
VSYC Pulse Width	TVs	1	3	5	Line
Vertical Display Term	TVd	-	240	-	Line
Vertical Display Start	TVds	-	18	-	Line
HSYC-ENAB Phase Differrence	THE	-	68	-	clk
HSYC-CK Phase Differrence	THC	12	-	-	ns
HSYC-VSYC Phase Differrence	THV	1	-	-	clk

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7.5.Color Data Assignment

BLACK 0 <th>B2 B1 E 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1 0 0 0 1 1 1 0 0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0 0</th>	B2 B1 E 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1 0 0 0 1 1 1 0 0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0 0
BLACK 0 <td>0 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1</td>	0 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1
RED (63) 1<	0 0 0 0 1 1 1 1 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 0 0
BASIC GREEN (63) 0 0 0 0 0 1 1 1 1 1 1 0 0 0 0 BASIC BLUE (63) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 <td< td=""><td>0 0 1 1 1 1 1 1 0 0 1 1 0 0 1 1 0 0 1 0 0 0 0 0</td></td<>	0 0 1 1 1 1 1 1 0 0 1 1 0 0 1 1 0 0 1 0 0 0 0 0
BASIC BLUE (63) 0 0 0 0 0 0 0 0 0 0 0 0 0 1 <	1 1 1 1 1 1 0 0 1 1 0 0 1 1 0 0 0 0 0 0
COLOR CYAN 0 0 0 0 0 1<	1 1 1 1 0 0 1 1 0 0 /0 0
MAGENTA 1 1 1 1 1 1 1 0 0 0 0 0 0 1 1 1 YELLOW 1	1 1 0 0 1 1 0 0 /0 0
YELLOW 1 <td>0 0 1 1 0 0 0 0</td>	0 0 1 1 0 0 0 0
WHITE 1 <td>1 1 0 0 0 0</td>	1 1 0 0 0 0
RED (0) 0 </td <td>0 0 0 0</td>	0 0 0 0
RED (1) 0 0 0 1 0 </td <td>0 0</td>	0 0
RED (2) 0 0 0 1 0 </td <td></td>	
RED I	0 0
RED (62) 1 1 1 1 0<	1 1
RED (63) 1 1 1 1 1 0<	
RED (63) 1 1 1 1 1 1 0<	
GREEN (0) 0	0 0
GREEN (1) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0
	0 0
	0 0
GREEN (2) 0 0 0 0 0 0 0 0 1 0 0 0 0 0	0 0
GREEN	
GREEN (62) 0 0 0 0 0 0 1 1 1 1 1 0 0 0 0 0	0 0
GREEN (63) 0 0 0 0 0 0 1 1 1 1 1 0 0 0 0	0 0
BLUE (0) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0
BLUE (1) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0
BLUE (2) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 1
BLUE	
BLUE (62) 0 0 0 0 0 0 0 0 0 0 0 0 1 1 1 1	1 1
BLUE (63) 0 0 0 0 0 0 0 0 0 0 0 0 1 1 1 1	1 1
Note]	
 Definition of gray scale Color (n) n indicates gray scale level. 	
Higher n means brighter level.	
2) Data 1:High, 0: Low	
Relation of IC and LCD Module Data Bus	
LCD Module RGB5 RGB4 RGB3 RGB2 RGB1 RGB0 *	*
IC RGB7 RGB6 RGB5 RGB4 RGB3 RGB2 RGB1	RGB0
*Connected to "L" in the LCD Module	
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7.6.Inverted Scan Capability

This module has the capability of inverting scan direction by signaling from controller. Note that scan direction cannot be changed during operation.

The following drawing shows the relationship between the viewing direction and the scan direction.



7.7.Lighting Specifications

7.7.1.Absolute Maximum Ratings

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Foward Current	lF	Note 1	-	-	450	mA
Reverse Voltage	VR	-	-	-	18.6	V
LED Power Dissipation	Po	-	-	-	8.37	W

Note 1 : Refer to the foward current derating curve.



7.7.2. Operating Characteristics

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Foward Voltage	VF	I⊧=240mA	-	(16.6)	-	V

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8. Optical Specifications

8.1.Optical Characteristics

ltem		Symbol Conditions		Sta	ndard Va	lue	Unit	Method of	- .		
		Symbol	θ	φ	С	Min.	Тур.	Max.	Onit	Measure	Remark
Brightness		В	0 °	0 °		TBD	800	-	cd/m ²		
Contrast		CR	Be Viev			210	350	-	-		
	Ded	Rx	0 °	0 °		TBD	TBD	TBD	-		
	Red	Ry	0 °	0 °		TBD	TBD	TBD	-	. (Fig.1)	Note1
	Green	Gx	0 °	0 °		TBD	TBD	TBD	-		
Color		Gy	0 °	0°		TBD	TBD	TBD	-		
Coordinates	Blue Bx	Bx	0 °	0 °		TBD	TBD	TBD	- 6		
		Ву	0 °	0°		TBD	твр	TBD	-] \		
		Wx	0 °	0°		твр	TBD	TBD			
	White Wy	Wy	0°	0°		TBD	твр	твр			
Brightness Unit	ormity	-	0°	0°		70	75		%	(Fig.2)	
Vertical	Up	Đư	-	0°	≥5		70	-	Degree		
Viewing Angle	Down	θ_{D}	-	0°	_≥ 5	-	70	-	Degree		
Horizontal	Left	- OF	0 °	-	≥5	-	70	-	Degree	(Fig.3)	
Viewing Angle	Right	φ _R	0 °	-	≥5	-	70	-	Degree		
Response	Rise	τr	0 °	0 °		-	21	-	ms		
Time	Decay	τd	0 °	0 °		-	10	-	ms	(Fig.4)	

Note1:Under the condition of maximum brightness.

◆ Conditions for Measuring

 \diamond Environment: Dark room with no light or close to no light.

 \bigcirc Temperature: 25±5°C

♦ Humidity: 40~70%RH

 \diamond Driving voltage is set for optimal contrast to measure center of display.

♦ LED Backlight driving condition: IF=80mA/1Line

• Optimal viewing angle

(The angle of Least Color Inversion)









 Method of Viewing Angle Measurement (F Measuring Device ELDIM: EZ CONTRAST 	ig.3)	
(2) Measuring PointCenter of display: Same as Method o	f Brightness Measurement	
 (3) Angle of Measuring θ: An angle vertical to perpendicular li φ: An angle horizontal to perpendicular 	-	
Ten	nperature	
EZ CONTRAST	Rotation Table(θ, φ)	
	<u>Fig. 3</u>	
(4) Method of Measuring Set the module on the rotation table and men fixed $\phi = 0$ degrees horizontal axis direction (Viewing angle is measured automatically t	to θ =90 degrees.	ite that
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- Measuring Response Time (Fig.4)
 - (1) Measuring Device
 TOPCON BM-5, Measuring Field: 1°
 Tektronix Digital Oscilloscope
 - (2) Measuring Point
 - Center of display, same as Method of Brightness Measurement
 - (3) Method of Measuring
 - Set LCD panel to θ =0°, and ϕ =0°.
 - · Input white \rightarrow black \rightarrow white to display by switching signal voltage.
 - If the luminance is 0% and 100% immediately before the change of signal voltage, then τr is optical response time during the change from 90% to 10% immediately after rise of signal voltage, and τd is optical response time during the change from 10% to 90% immediately after decay of signal voltage.



No abnormal function and appearance are found after the following tests.

Conditions: Unless otherwise specified, tests will be conducted under the following condition. Temperature: 20±5°C Humidity : 65±5%RH tests will not be conducted under functioning state.

No.	Parameter	Conditions	Notes
1	High Temperature Operating	70°C±2°C, 96hrs (operation state)	
2	Low Temperature Operating	-20°C±2°C, 96hrs (operation state)	1
3	High Temperature Storage	80°C±2°C, 96hrs	2
4	Low Temperature Storage	-30°C±2°C, 96hrs	1,2
5	Damp Proof Test	40°C±2°C,90~95%RH, 96hrs	1,2
6	Vibration Test	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes	3
7	Shock Test	To be measured after dropping from 60cm high on the concrete surface in packing state.	

Note 1 :No dew condensation to be observed.

Note 2 :The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after removed from the test chamber.

Note 3 :Vibration test will be conducted to the product itself without putting it in a container.

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10. Appearance Standards

10.1.Inspection conditions



NO.	Parameter			Criteria		
	Polarizer Scratches	<u> </u>				
			Zone		eptable Numb	
			Y(mm)	А	В	С
1			01 <w≤0.05< td=""><td></td><td>1</td><td>*</td></w≤0.05<>		1	*
			W > 0.01)	*
			W > 0.05)	*
		X : Length,	7:Width	* : Disregaro	1	
	DENT		7	0		
			Zone		eptable Numb	
2		Dimension (n 0.30 < D ≤		A	В	C
			0.50		1	*
		0.50 < D	Diamater #	(long+short)) /2 * : Disre	<u> </u>
	BLACK SPOT	D . Average		(IUNG+SIIDA)		garu
	WHITE SPOT		Zone	Acc	eptable Numb	er
	BUBBLE	Dimension (n		A	В	C
3		0.30 < D ≤	<u>ah h h b</u>	<u> </u>	5	*
		0,50 ≤ D			<u>,</u>	*
					-	
	LINT					
	تغتيا		Zone	Aco	eptable Numb	ber
		X(mm)	Y(mm)	А	В	С
4		$L \leq 3.0$	$W \leq 0.15$	4	1	*
		L > 3.0	$W \leq 0.15$	()	*
		-	W > 0.15	According to I	BLACK SPOT	*
		X:Length,	r : Width	* : Disregare	ł	
			I			



11. <u>Code System of Production Lot</u>						
The production lot of module is specif	ied as follows.					
Factory Cod Factory Cod Production Month (1~9, Production Year (Lower 2 digit	(1~5) X, Y, Z)	99)				
12. <u>Type Number</u>		Ø				
The type number of module is specific 355520AC 13. Applying Precautions Please contact us when questions and Specifications arise	MINAR					
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- 9) For models which use COG,TAB,or COF:
- (1) The mechanical strength of the product is low since the IC chip faces out unprotected from the rear. Be sure to protect the rear of the IC chip from external forces.
- (2) Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage, avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.
- 10) Models which use flexible cable, heat seal, or TAB:
- (1) In order to maintain reliability, do not touch or hold by the connector area.
- (2) Avoid any bending, pulling, or other excessive force, which can result in broken connections.
- 11) In case of buffer material such as cushion / gasket is assembled into LCD module, it may have an adverse effect on connecting parts (LCD panel-TCP / HEAT SEAL / FPC / etc., PCB-TCP / HEAT SEAL / FPC etc., TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC, etc.,) depending on its materials. Please check and evaluate these materials carefully before use.
- 12) In case of acrylic plate is attached to front side of LCD panel, cloudiness (very small cracks) can occur on acrylic plate, being influenced by some components generated from polarizer film. Please check and evaluate those acrylic materials carefully before use
- 13) Flickering due to optical interference may occur by combination of a) LCD driving frame frequency decided by either internal oscillator in driver IC or external clock input by the customer and b) lighting frequency of either backlight or other light sources. Please evaluate enough at the environment of actual use, and decide the driving condition that does not cause flickering.
- Please be advised that do not apply DC voltage to the LCD.
 If DC voltage is applied to the LCD, then it may cause poor display quality.

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15. Warranty

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery pecifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1) We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 2) We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3) We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4) When the product is in CFL models, CFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
- 5) We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.
- 6) Optrex will not be held responsible for any quality issue(s) after two years and beyond from its production date indicated on the lot number (please refer to "Code System of Production Lot" indicated earlier in this specification).

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