

Vishay General Semiconductor

Photovoltaic Solar Cell Protection Schottky Rectifier

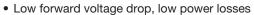
Ultra Low $V_F = 0.30 \text{ V}$ at $I_F = 5 \text{ A}$



PRIMARY CHARACTERISTICS			
I _{F(AV)}	15 A		
V_{RRM}	45 V		
I _{FSM}	200 A		
V _F at I _F = 15 A	0.38 V		
T _{OP} max. (AC mode)	150 °C		
T _J max. (DC forward current)	200 °C		
Package	DO-201AD		
Diode variations	Single die		

FEATURES







High forward surge capability

RoHS Solder dip 275 °C max. 10 s, per JESD 22-B106

• Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: DO-201AD

Molding compound meets UL 94 V-0 flammability rating

Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSB1545S	UNIT	
Device marking code		V1545S		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current (fig. 1, 2)	I _{F(DC)} (1)	15	Δ.	
	I _{F(DC)} (2)	7.0	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	200	А	
Operating junction temperature range (AC mode)	T _{OP}	-40 to +150		
Junction temperature in DC forward current without reverse bias, $t \le 1 \text{ h}$	T _J ⁽³⁾	≤ 200	°C	

Notes

- (1) With heatsink
- (2) Without heatsink, free air
- (3) Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 5.0 A	T _A = 25 °C	25 °C V _F ⁽¹⁾	0.42	-	V	
	I _F = 7.5 A			0.44	-		
	I _F = 15 A			0.48	0.59		
	I _F = 5.0 A	T _A = 125 °C	'	VF (.)	0.30	-	V
	I _F = 7.5 A				0.33	-	
	I _F = 15 A			0.38	0.46		
Reverse current	V _R = 45 V	T _A = 25 °C T _A = 125 °C	I _R ⁽²⁾	-	1000	μΑ	
	v _R = 45 v		I 'R (=)	13.8	30	mA	
Typical junction capacitance	4.0 V, 1 MHz		CJ	1995	-	pF	

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: 40 ms pulse width

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSB1545S	UNIT	
Thermal resistance	R _{0JA} (1)	45	°C/W	
	R _{0JL} (1)	9	C/VV	
Typical thermal resistance	R _{0JL} (2)	4	°C/W	

Notes

(1) Without heatsink, free air; units mounted on PCB with 2 mm x 2 mm copper pad areas at 9.5 mm lead length

(2) Leads clipped at 3 mm lead length from plastic body on 7.0 cm x 2.2 cm x 1.9 cm x 2 heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	EFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE	
VSB1545S-E3/54	1.20	54	1400	13" diameter paper tape and reel	
VSB1545S-E3/73	1.20	73	1000	Ammo pack packaging	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

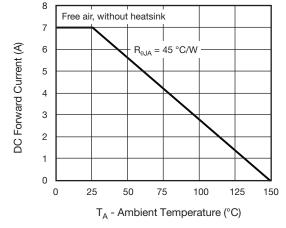


Fig. 1 - Forward Current Derating Curve

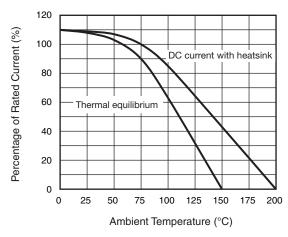


Fig. 2 - Rated Forward Current vs. Ambient Temperature



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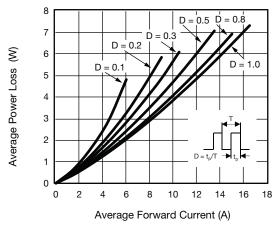


Fig. 3 - Forward Power Loss Characteristics

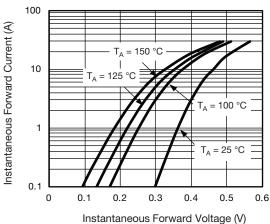
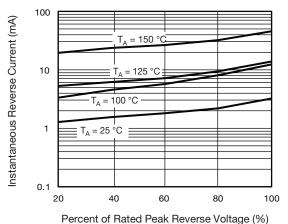


Fig. 4 - Typical Instantaneous Forward Characteristics



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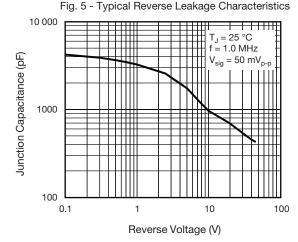
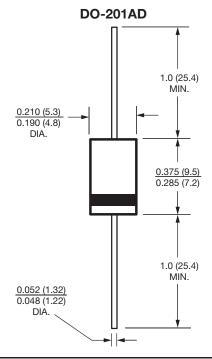


Fig. 6 - Typical Junction Capacitance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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