MJD44E3, NJVMJD44E3T4G

Darlington Power Transistor

DPAK For Surface Mount Applications

Designed for general purpose power and switching output or driver stages in applications such as switching regulators, converters, and power amplifiers.

Features

- Electrically Similar to Popular D44E3 Device
- High DC Gain 1000 Min @ 5.0 Adc
- Low Sat. Voltage 1.5 V @ 5.0 Adc
- Compatible With Existing Automatic Pick and Place Equipment
- Epoxy Meets UL 94 V-0 @ 0.125 in
- ESD Ratings:
 - Human Body Model, 3B > 8000 V
 - Machine Model, C > 400 V
- NJV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free Packages*

MAXIMUM RATINGS

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V_{CEO}	80	Vdc
Emitter-Base Voltage	V _{EB}	7	Vdc
Collector Current - Continuous	Ic	10	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	20 0.16	W W/°C
Total Power Dissipation (Note 1) @ T _A = 25°C Derate above 25°C	P _D	1.75 0.014	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

 These ratings are applicable when surface mounted on the minimum pad sizes recommended.

1



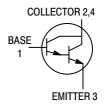
ON Semiconductor®

http://onsemi.com

NPN DARLINGTON SILICON POWER TRANSISTORS 10 AMPERES 80 VOLTS, 20 WATTS



DPAK CASE 369C STYLE 1



MARKING DIAGRAM



A = Assembly Location

Y = Year
WW = Work Week
J44E3 = Device Code
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
MJD44E3T4G	DPAK (Pb-Free)	2,500 / Tape & Reel
NJVMJD44E3T4G	DPAK (Pb-Free)	2,500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ hetaJC}$	6.25	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	71.4	°C/W
Lead Temperature for Soldering	TL	260	°C

^{2.} These ratings are applicable when surface mounted on the minimum pad sizes recommended.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			•	•	
Collector Cutoff Current (V _{CE} = Rated V _{CEO} , V _{BE} = 0)	I _{CES}	-	_	10	μΑ
Emitter Cutoff Current (V _{EB} = 7 Vdc)	ГЕВО	-	-	1	μΑ
ON CHARACTERISTICS					
Collector-Emitter Saturation Voltage (I _C = 5 Adc, I _B = 10 mAdc) (I _C = 10 Adc, I _B = 20 mAdc)	V _{CE(sat)}	- -	- -	1.5 2	Vdc
Base–Emitter Saturation Voltage (I _C = 5 Adc, I _B = 10 mAdc)	V _{BE(sat)}	-	-	2.5	Vdc
DC Current Gain (V _{CE} = 5 Vdc, I _C = 5 Adc)	h _{FE}	1000	-	-	_
DYNAMIC CHARACTERISTICS					
Collector Capacitance (V _{CB} = 10 Vdc, f _{test} = 1 MHz)	C _{cb}	-	-	130	pF
SWITCHING TIMES			•	•	
Delay and Rise Times (I _C = 10 Adc, I _{B1} = 20 mAdc)	t _d + t _r	-	0.6	-	μs
Storage Time (I _C = 10 Adc, I _{B1} = I _{B2} = 20 mAdc)	t _s	-	2	_	μs
Fall Time (I _C = 10 Adc, I _{B1} = I _{B2} = 20 mAdc)	t _f	-	0.5	-	μs

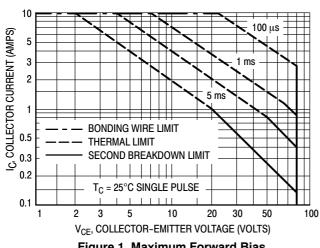


Figure 1. Maximum Forward Bias Safe Operating Area

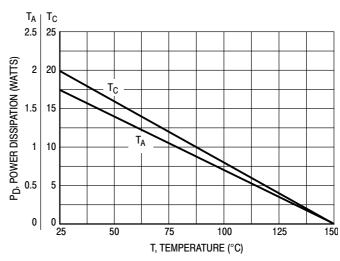
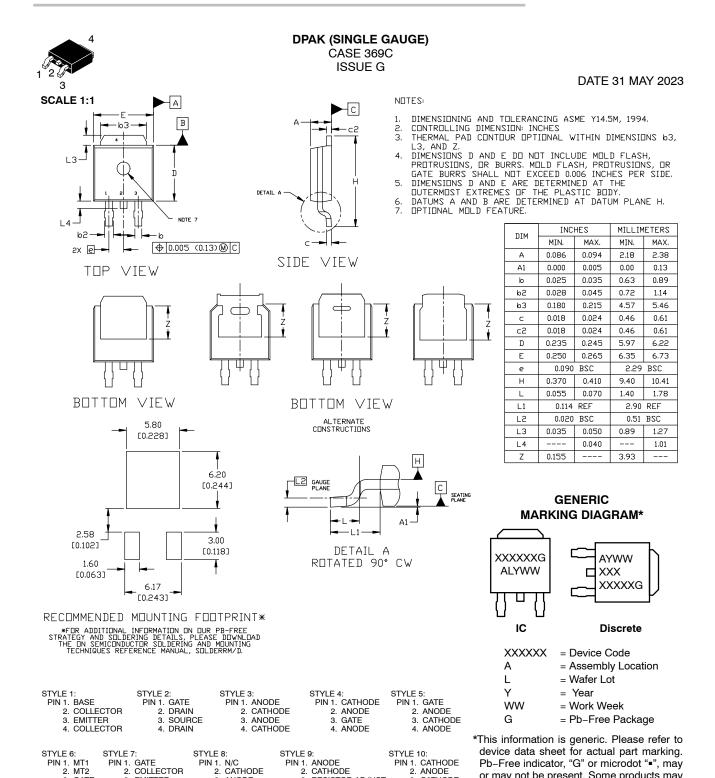


Figure 2. Power Derating





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DESCRIPTION:	DPAK (SINGLE GAUGE)		PAGE 1 OF 1	

3 RESISTOR ADJUST

CATHODE

3 CATHODE

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