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ELECTRICAL CHARACTERISTICS @ 25°C

1.0 TURNS RATIO: (P8-P6-P7): (J6-J3) : 1CT : 1 ± 3% (P2-P3-P1): (J2-J1): 1CT : 1 ± 3%

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias 2.0 INDUCTANCE: (P7-P8)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias (P1-P2)

3.0 LEAKAGE INDUCTANCE: P8-P7 (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz P2-P1 (WITH J2 AND J1 SHORT) : 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P8,P6,P7) TO (J6,J3) : 30pF MAX. @ 1MHz

: 30pF MAX. @ 1MHZ (P3,P2,P1) TO (J2,J1)

: 1.2 ohms Max. : 1.2 ohms Max. 5.0 DC RESISTANCE: (J1-J2) (P8-P7)

RECEIVE

6.0 RETURN LOSS:1MHz TO 30MHz : -18dB MIN.

: -(19-20 LOG (f/30MHz)) : -12dB MIN. 30MHz TO 60MHz

60MHz TO 80MHz

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) (J3, J6) TO (P8,P7) : 1500 Vrms : 1500 Vrms

8.0 INSERTION LOSS: RS=RL=100 ohms : -1.0 dB MAX 1MHz TO 65MHz 65MHz TO 100MHz : -1.1 dB MAX

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

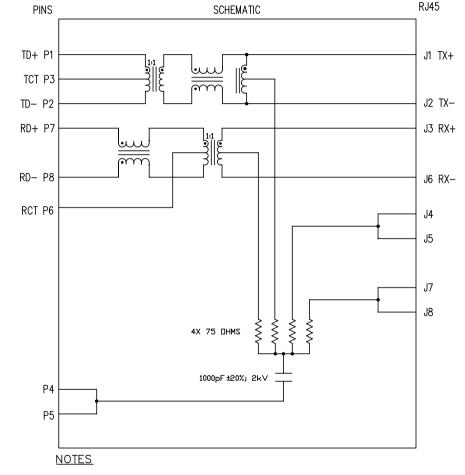
TITLE

OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX : 3.0 nS MAX PULSE WIDTH= 112nS

10.0 CROSS TALK: 1MHz TO 100MHz : -35 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION:

1MHz TO 100MHz : -45dB TYP 100MHz TO 200MHz : -20dB MIN



- 1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.
- 2.0 ALL RESISTORS ARE ±5% TOLERANCE.

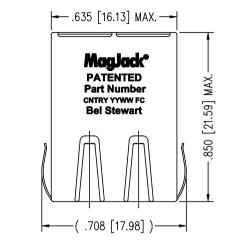
ORIGINATED BY	DATE
Zeng Xiao Chun	07-04-2011
DRAWN BY	DATE
Zhang Ru Sheng	07-04-2011

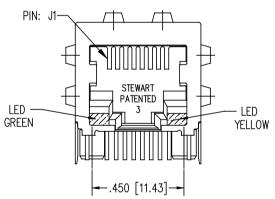
10/100BT **TAB DOWN SHIELDED PATENTED**

PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH		[] METRIC DIM. AS REFERENCE			
SI-60208-F			UNIT : INCH [mm]	REV.: A0		
FILE NAME	.X		SCALE: N/A	SIZE : A4		
01 00000 E DW0	l .xx		SCALE. N/A			
SI-60208-F.DWG	.XXX	±0.005		PAGE: 2		



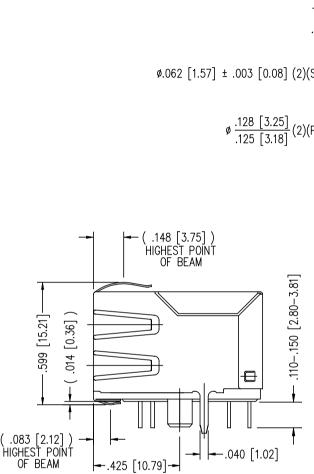
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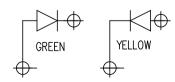


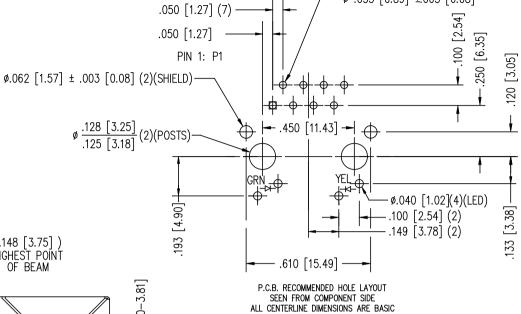
LED SPECIFICATION				
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	* (TYP)	
GREEN	565 nm	2.5 V	2.2 V	
YELLOW	590 nm	2.5 V	2.1 V	

*WITH A FORWARD CURRENT OF 20 mA (TYP)









NOTES:

1. CONNECTOR MATERIALS:

HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD,

50 MICRO-INCHES MIN. IN CONTACT AREA.

RoHS

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.

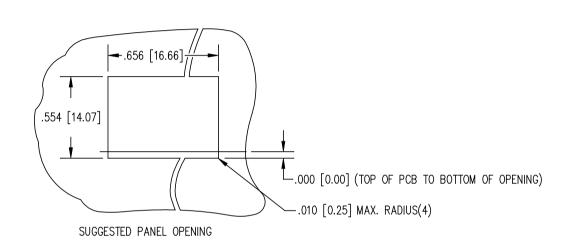
ø .035 [0.89] ±.003 [0.08]

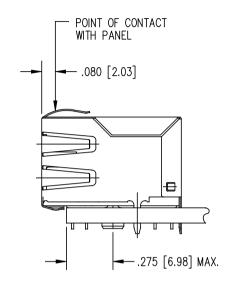
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR 10 SECONDS MAX.

ORIGINATED BY	DATE	MagJack [®]	PART NO. / DRAWING NO.	STANDA	RD DIM.	[] METRIC DIM. AS REF	ERENCE	
Zeng Xiao Chun	07-04-2011		SI-60208-F	TOL. IN	INCH	UNIT : INCH [mm]	REV.: A0	bei
DRAWN BY	DATE	10/100BT	FILE NAME	.X		SCALE: N/A	SIZE : A4	COMPONENTS FOR A
		TAB DOWN SHIELDED	SI-60208-F.DWG	.XX		* -	OIZE . AT	CONNECTED PLANET
Zhang Ru Sheng	07-04-2011	PATENTED	31-00200-1 :5443	.XXX	±0.005		PAGE: 3	

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- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

ORIGINATED BY	DATE	TITLE
Zeng Xiao Chun	07-04-2011	
DRAWN BY	DATE	
Zhang Ru Sheng	07-04-2011	

MagJack®
10/100BT
TAB DOWN SHIELDED
PATENTED

PART NO. / DRAWING NO.	STANDA	ARD DIM.	[] METRIC DIM. AS REFERENCE		
SI-60208-F	TOL. IN	TOL. IN INCH UNIT : INCH [mm]		REV.: A	
FILE NAME	<u>.x</u>		SCALE: N/A	SIZE : A4	
SI-60208-F.DWG	xx		A 1	1	
01-00200-1.5110	L.xxx	±0.005		PAGE:	

