

TYA- Low Profile High Current SMT Power Inductor TYA252010 Series

FEATURES AND APPLICATIONS

Laird TYA series high current power inductors improve performance, reliability and power efficiency. A lower profile benefits consumer electronics, industrial and telecom design. Products feature extremely low DCR with greater efficiency and enable a large current in a small size. Inductors are of magnetic shielding and wire wound construction and perform in operating temperatures ranging from -40 C to 125 C including self-heating rise in temperature.

FEATURES

- Magnetic shielded structure
- Low DCR and high efficiency
- Low profile and small size
- Metal alloy core with high saturation

APPLICATIONS

- DC-DC Converter and Power Suppliers
- LCD TV'S and Gaming Console
- Tablet, Notebooks, Servers and Printers
- Networking and Data storage
- GPS, Set-top-box and Base stations
- Smart meters and Medical instruments



PART NUMBER EXPLANATION

TYA 252010 4R7 M - 10

Product series code	Product size code	Inductance value code (i.e. 4R7: 4.7 μ H)	Tolerance % (i.e. M: \pm 20%)	Standard Catalog P.N
TYA	252010	4R7	M	10

ELECTRICAL SPECIFICATIONS

- Tolerance: M: \pm 20% or N: \pm 30%
- Inductance tested at 1MHz, 1.0Vrms
- Heat Rated Current (Irms) is defined based on temperature rise approximate 40°C (ambient temperature 25 \pm 5°C)
- Saturation Current (Isat) is the DC current at which the inductance drops off approximately 30% from its value without current. (ambient temperature 25 \pm 5°C)
- Operating temperature range: -40°C~+125°C (including self-heating temperature rise)
- Storage temperature range (packaging conditions): -10°C~+40°C and RH 70%(MAX.)

Note: Heat Rated Current (Irms) is tested on a typical PCB and apply a constant current in still air. The temperature rise is dependent on the application system condition including PCB PAD pattern, trace width and thickness and adjacent components etc. It's suggested to verify the temperature rise of the component under the real operation application conditions.

SPECIFICATION

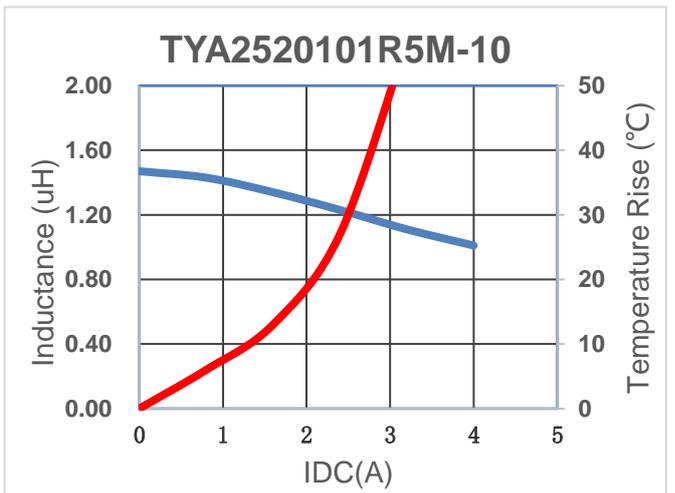
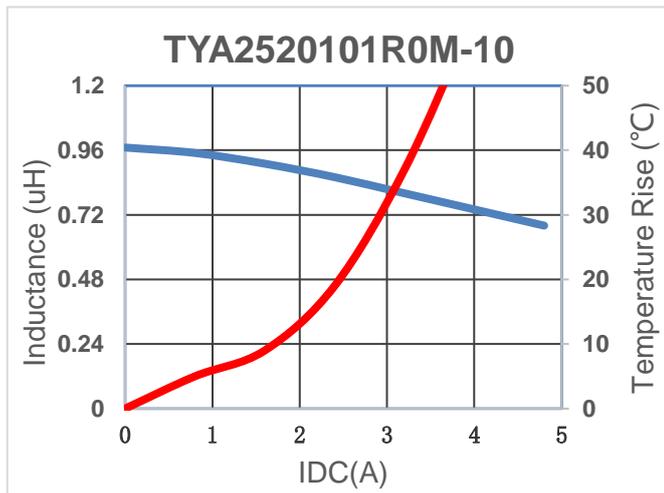
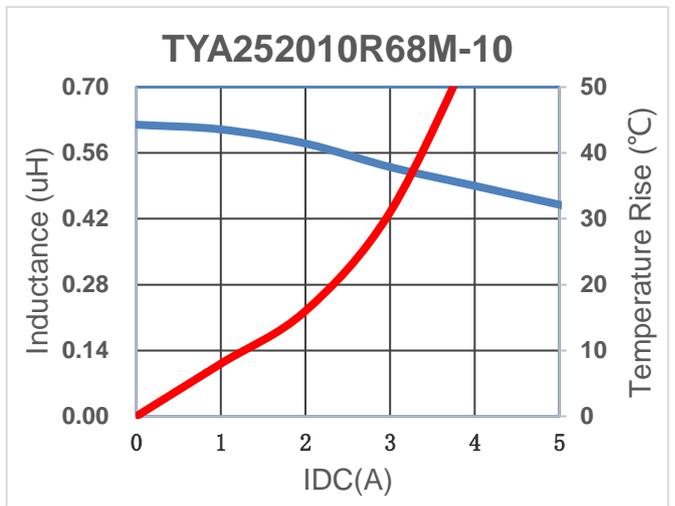
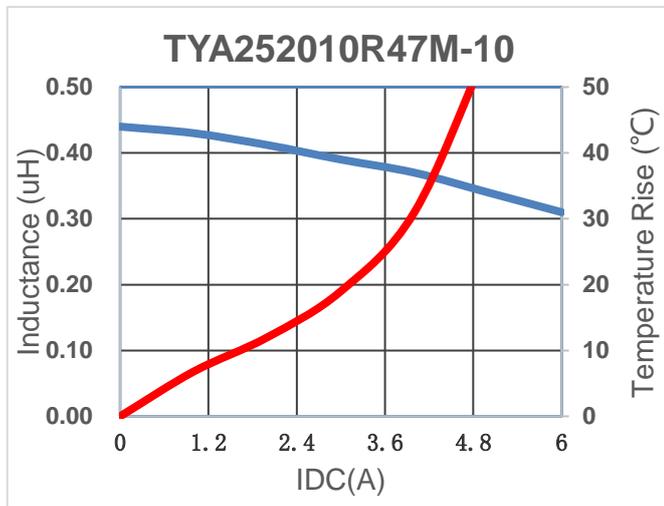
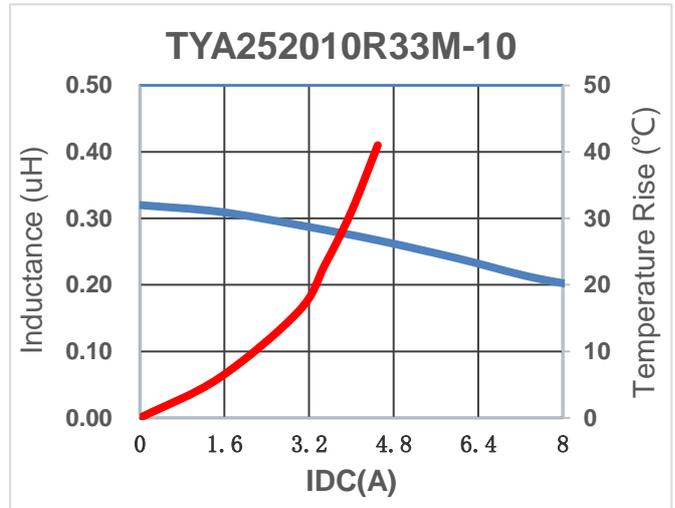
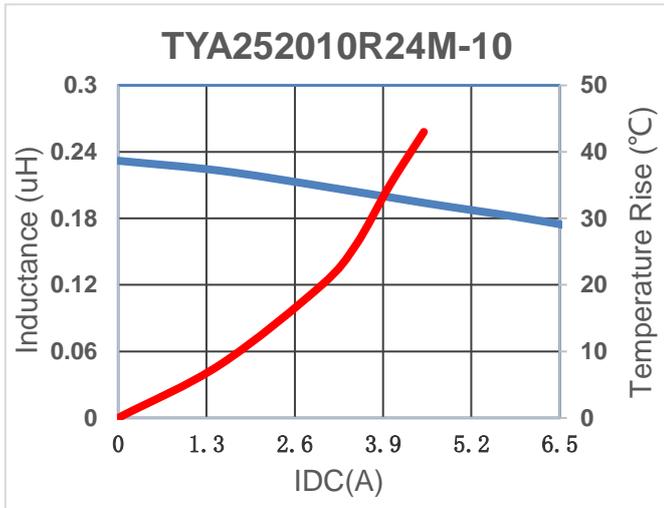
PART NUMBER	INDUCTANCE (uH)	I _{rms} (A) Typ.	I _{sat} (A) Typ.	DCR(mΩ) Max	REMARK
TYA252010R24M-10	0.24	3.70	6.10	33.0	
TYA252010R33M-10	0.33	3.50	4.80	39.0	
TYA252010R47M-10	0.47	3.20	4.40	45.0	
TYA252010R68M-10	0.68	2.75	3.20	59.0	
TYA2520101R0M-10	1.00	2.20	3.10	85.0	
TYA2520101R5M-10	1.50	2.00	2.60	106.0	
TYA2520102R2M-10	2.20	1.50	1.90	155.0	
TYA2520103R3M-10	3.30	1.20	1.60	235.0	
TYA2520104R7M-10	4.70	1.00	1.30	290.0	
TYA2520106R8M-10	6.80	0.95	1.00	480.0	
TYA252010100M-10	10.00	0.65	0.90	740.0	

GENERAL SPECIFICATION:

- 1, Test conditions(L): 1.0MHz, 1Vrms
- 2, Operating temperature: -40°C to +125°C (Including self-heating)
- 3, Storage temperature: -10°C to +40°C
- 4, Humidity range: 70% RH Max.
- 5, Heat Rated Current (I_{rms}) will cause the coil temperature rise approximately Δt of 40°C
- 6, Saturation Current (I_{sat}) will cause L₀ to drop approximately 30%.
- 7, Part Temperature (Ambient+Temp. Rise) : Should not exceed 125°C under worst case conditions.
- 8, Storage condition (component in its packaging)

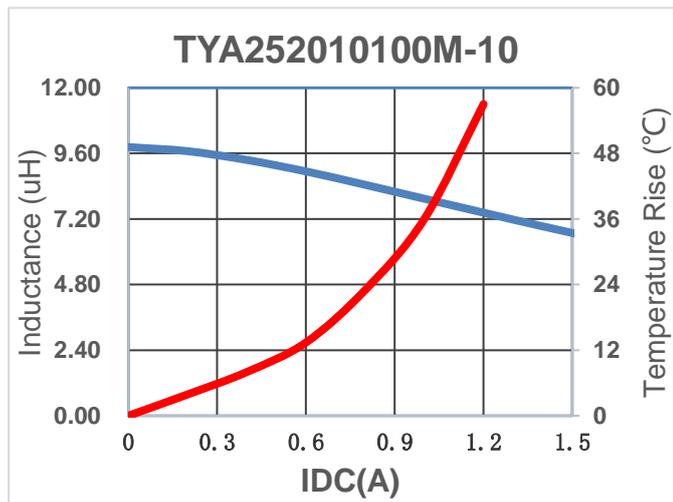
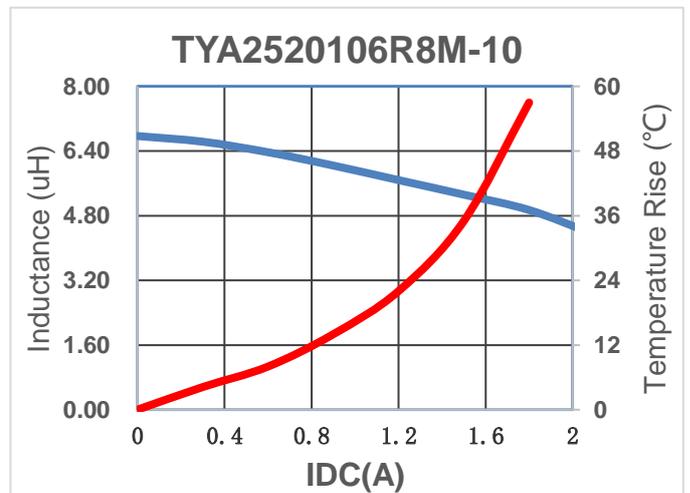
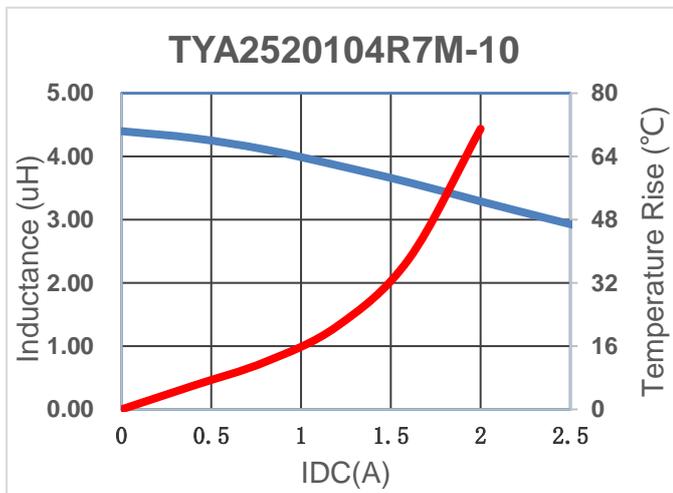
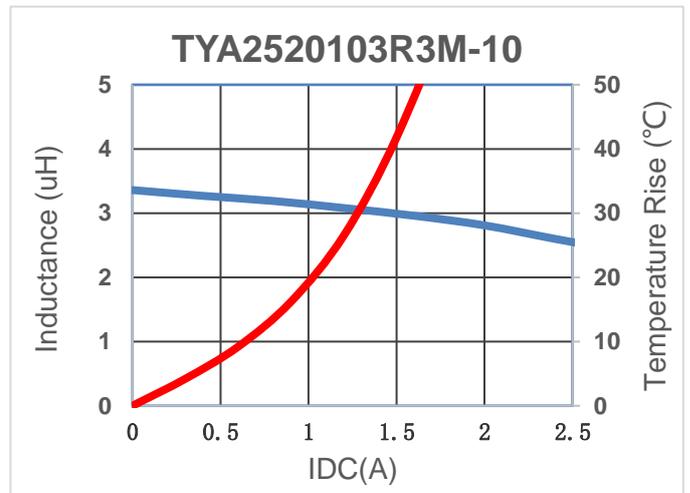
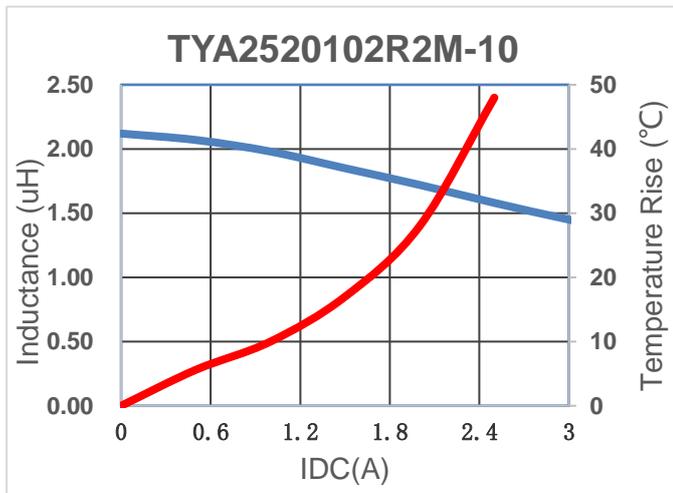
SPECIFICATION

Characteristics Curve



SPECIFICATION

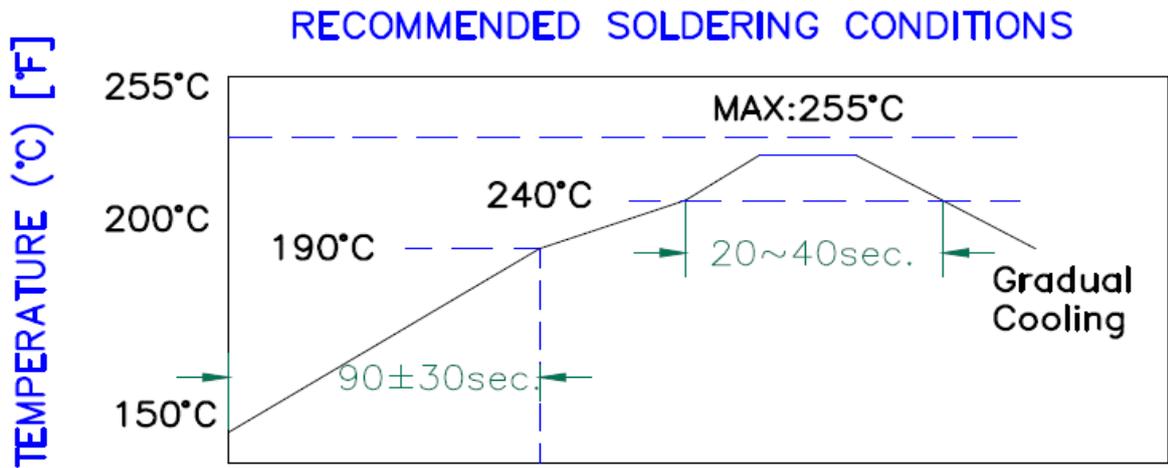
Characteristics Curve



Recommended Soldering Conditions

For Lead-Free Application

Figure 1 . Re-flow Soldering





Steward™

Laird Performance Materials

Shielded Power Inductor

www.laird.com TYA252010 Series Rev: A

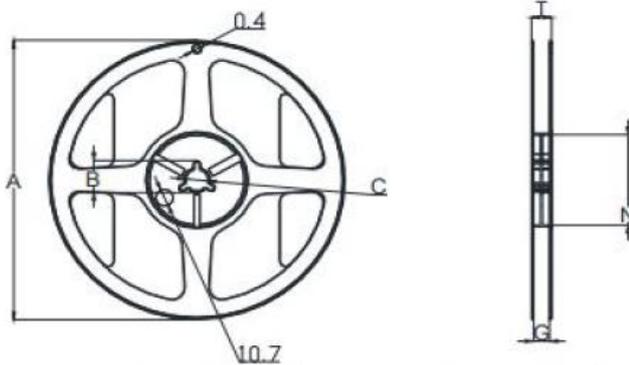
Reliability and Testing Conditions / Pin Type Power Inductors

SMD series(Consumer)

Item	Reference	Additional Requirements
Operating temperature range	-40°C ~ +125°C (Including self-temperature rise)	
Storage temperature and humidity range	-10°C to +40°C , 70% RH Max	
High Temperature Exposure (Storage)	MIL-STD-202 Method 108	85±2°C, 168+24hours
Temperature Cycling	JESD22 Method JA-104	-40°C → +85, transforming interval:20s, 100cycles
Operational Life	MIL-PRF-2	85±°C, 168+24hours Apply maximum rated voltage and current according part drawing
External Visual	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship. Electrical Test not required.
Physical Dimension	JESD22 Method JB-100	Verify physical dimensions to the applicable device detail specification. Note: User(s) and Suppliers spec. Electrical Test not required
Vibration	MIL-STD-202 Method 204	10~55Hz,1.5mm, 2 hours in each 3mutually perpendicular directions (total of 6 hours)
Resistance to Soldering Heat	MIL-STD-202 Method 210	1. Max. 260±5°C,10±1s, 2 times 2.Solder Composition: Sn/3Ag/0.5Cu
Solderability	J-STD-002	245±5°C, 5±1sec, Solder: Sn/3.0Ag/0.5Cu
Electrical Characterization	Print Spec	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max Operating temperatures
Board Flex	AEC-Q200-005	2mm,30±1s
Terminal Strength(SMD)	AEC-Q200-006	10N, 5S, X,Y direct

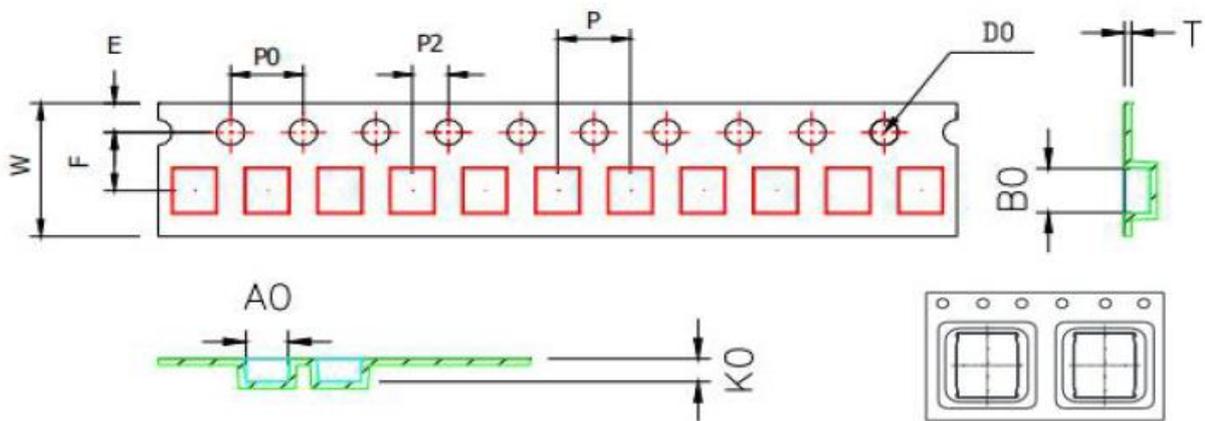
PACKAGING

Reel Dimension



TYPE	A	B	C
8mm	178	20.7±0.8	13±0.4
	G	N	T
	9	60	10.8

Tape Dimension

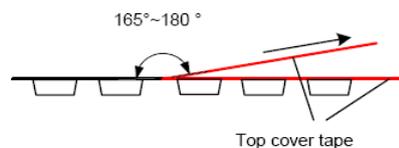


W	E	F	P	A0	B0	P2	P0	K0	t	D0
8.0±0.3	1.75±0.1	3.5±0.1	8±0.1	2.35±0.1	2.65±0.1	2.0±0.1	4.0±0.1	1.4±0.1	0.25±0.05	1.5Ref.

Packaging Quantity

P/N	Chip/Reel	Inner Box	Outer Box
TYA252010 series	2000pcs	10000pcs	50000pcs
Size		-	-

Peeling Off Force



The force peeling off cover tape is 10 to 100 grams			
in the arrow direction under the following conditions			
Room Temp	Room Humidity (%)	Room atn (hPa)	Teaming Speed (mm/min)
5~35	45~85	860~1060	300

※Storage Conditions

1. Temperature and humidity conditions: -10+40°C and 70% RH.
2. Recommended products should be used within 12 months from the time of manufacturing.
3. The packaging material should be kept where no chlorine or sulfur exists in the air.
4. Allowable stacking condition of Packaging box: max height 1.5m or 5 boxes stacking