

# **CP2105 Errata**

This document contains information on the errata of revision F01 of the CP2105.

For errata on older revisions, please refer to the errata history for the device. The device revision is typically the first letter on the line immediately under the part number on the package marking. This is typically the second or third line.

Errata effective date: September 16, 2016.

# 1. Errata Summary

Errata #	Designator	Title/Problem	Workaround Exists	Affected Revision	Resolution
1	CP2105_E101	ROM Programming Voltage	Yes	Data Sheet Revision 1.1 and below	_
2	CP2105_E102	Failure to Enumerate	No	F01 date co- des before 1647	F01 date codes equal to 1647 or later
3	CP2105_E103	RI May Be Grounded	Yes	F01	_
4	CP2105_E104	Closing the COM Port Causes Data Loss	Yes	F01	_

# Table 1.1. Errata Status Summary

# 2. Detailed Errata Descriptions

# 2.1 CP2105\_E101 – ROM Programming Voltage

## Description of Errata

The data sheet incorrectly indicates that VDD must remain at 3.3 V or higher to successfully write to the configuration ROM. Instead, the voltage on the VIO pin must remain at 3.3 V or higher when writing to the configuration ROM.

# Affected Conditions / Impacts

For systems that connect VDD and VIO together, there is no impact. For systems that have a separate voltage source for VIO and are configuring the ROM in-system, VIO must remain at 3.3 V while programming is in progress.

## Workaround

For systems that connect VDD and VIO together, keep both power supplies above 3.3 V when programming. For systems that have a separate voltage source for VIO and are configuring the ROM in-system, VIO must remain at 3.3 V while programming is in progress.

## Resolution

This issue will be resolved in a future version of the datasheet.

# 2.2 CP2105\_E102 - Failure to Enumerate

# **Description of Errata**

Devices can fail to enumerate properly on initial power on, after a device reset, or when connected to a USB port. In the case of a failure, the device will lock up until the next reset or power on reset. The failure rate is intermittent and will vary from device to device.

## Affected Conditions / Impacts

The device can fail to enumerate on initial power on, after a device reset, or when connected to a USB port.

## Workaround

There is currently no workaround for this issue.

#### Resolution

This issue will be resolved in revision F01 date codes 1647 or later devices.

# 2.3 CP2105\_E103 - RI May Be Grounded

#### Description of Errata

Programming an interface to Modem mode causes the /SUSPEND / RI pin to assume the Reset and Suspend Latch values of the / SUSPEND functionality, instead of correctly leaving this pin (RI) as an input.

# Affected Conditions / Impacts

For systems using either CP2105 interface in Modem mode, the RI pin may drive high or low, depending on the /SUSPEND Reset and Suspend Latch settings, rather than staying as an input.

#### Workaround

To workaround this issue and use the interface in Modem mode:

- 1. Set the /SUSPEND Reset and Suspend Latch values for the interface as an input.
- 2. Set the interface to Modem mode.

#### Resolution

There is currently no resolution for this issue.

# 2.4 CP2105\_E104 - Closing the COM Port Causes Data Loss

# Description of Errata

When using the CP2105 standard interface (SCI), closing the COM port before all data transmits can cause data loss. When this occurs, the data will not be retransmitted, even if the COM port is reopened.

# Affected Conditions / Impacts

Closing the COM port before all data transmits can cause data loss in the standard interface (SCI).

# Workaround

To prevent data loss from occurring, wait until all data has transmitted from the standard interface before closing the COM port on hte host.

# Resolution

There is currently no resolution for this issue.

# 3. Revision History

# 3.1 Revision 0.2

September 16, 2016

Updated language for CP2105\_E101, since it is not resolved in the data sheet revision 1.1.

Added CP2105\_E102, CP2105\_E103, and CP2105\_E104.

# 3.2 Revision 0.1

August 22, 2012

Initial release.

Silicon Labs





# **Simplicity Studio**

One-click access to MCU and wireless tools, documentation, software, source code libraries & more. Available for Windows, Mac and Linux!





Quality www.silabs.com/quality Support and Community community.silabs.com

#### Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

#### **Trademark Information**

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress® and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc. 400 West Cesar Chavez Austin, TX 78701 USA

# http://www.silabs.com