600 Watt Peak Power Zener Transient Voltage Suppressors

Bidirectional*

The SMB series is designed to protect voltage sensitive components from high voltage, high energy transients. They have excellent clamping capability, high surge capability, low zener impedance and fast response time. The SMB series is supplied in ON Semiconductor's exclusive, cost-effective, highly reliable Surmetic[™] package and is ideally suited for use in communication systems, automotive, numerical controls, process controls, medical equipment, business machines, power supplies and many other industrial/consumer applications.

Specification Features:

- Working Peak Reverse Voltage Range 10 V to 78 V
- Standard Zener Breakdown Voltage Range 11.7 V to 91.3 V
- Peak Power 600 Watts @ 1 ms
- ESD Rating of Class 3 (> 16 KV) per Human Body Model
- Maximum Clamp Voltage @ Peak Pulse Current
- Low Leakage $< 5 \mu A$ Above 10 V
- UL 497B for Isolated Loop Circuit Protection
- Response Time is Typically < 1 ns

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic

FINISH: All external surfaces are corrosion resistant and leads are readily solderable

MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES:

260°C for 10 Seconds

LEADS: Modified L–Bend providing more contact area to bond pads **POLARITY:** Polarity band will not be indicated **MOUNTING POSITION:** Any

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation (Note 1.) @ T _L = 25°C, Pulse Width = 1 ms	P _{PK}	600	W
DC Power Dissipation @ T _L = 75°C Measured Zero Lead Length (Note 2.)	PD	3.0	W
Derate Above 75°C		40	mW/°C
Thermal Resistance from Junction to Lead	R_{\thetaJL}	25	°C/W
DC Power Dissipation (Note 3.) @ $T_A = 25^{\circ}C$ Derate Above $25^{\circ}C$	PD	0.55	W
Thermal Resistance from Junction		4.4	mW/°C
to Ambient	R_{\thetaJA}	226	°C/W
Operating and Storage Temperature Range	T _J , T _{stg}	-65 to +150	°C

1. 10 X 1000 µs, non-repetitive

2. 1" square copper pad, FR-4 board

 FR-4 board, using ON Semiconductor minimum recommended footprint, as shown in 403A case outline dimensions spec.

*Please see 1SMB5.0AT3 to 1SMB170AT3 for Unidirectional devices.



ON Semiconductor[™]

http://onsemi.com

PLASTIC SURFACE MOUNT ZENER OVERVOLTAGE TRANSIENT SUPPRESSORS 10–78 VOLTS 600 WATT PEAK POWER





SMB CASE 403A PLASTIC

MARKING DIAGRAM



ORDERING INFORMATION

Device †	Package	Shipping
1SMBxxCAT3	SMB	2500/Tape & Reel

Devices listed in *bold, italic* are ON Semiconductor **Preferred** devices. **Preferred** devices are recommended choices for future use and best overall value.

†The "T3" suffix refers to a 13 inch reel.

ELECTRICAL CHARACTERISTICS

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ IPP
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
Ι _Τ	Test Current



ELECTRICAL CHARACTERISTICS (Devices listed in bold,	italic are ON Semiconductor Preferred devices.)
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		V _{RWM}		Breakdown Voltage			V _C @ I _{PP} (Note 6.)		
	Device	(Note 4.)	I _R @ V _{RWM}	V _{BR}	V _{BR} (Note 5.) Volts @ H		Vc	I _{PP}	
Device	Marking	Volts	μΑ	Min	Nom	Max	mA	Volts	Amps
1SMB10CAT3 1SMB11CAT3 1SMB12CAT3 1SMB13CAT3	KXC KZC LEC LGC	10 11 12 13	5.0 5.0 5.0 5.0 5.0	11.1 12.2 13.3 14.4	11.69 12.84 14.00 15.16	12.27 13.5 14.7 15.9	1.0 1.0 1.0 1.0	17.0 18.2 19.9 21.5	35.3 33.0 30.2 27.9
1SMB14CAT3 1SMB15CAT3 1SMB16CAT3 1SMB17CAT3	LKC <i>LMC</i> LPC LRC	14 15 16 17	5.0 5.0 5.0 5.0	15.6 16.7 17.8 18.9	16.42 17.58 18.74 19.90	17.2 18.5 19.7 20.9	1.0 1.0 1.0 1.0	23.2 24.4 26.0 27.6	25.8 24.0 23.1 21.7
1SMB18CAT3 1SMB20CAT3 1SMB22CAT3 1SMB24CAT3	LTC LVC LXC LZC	18 20 22 24	5.0 5.0 5.0 5.0 5.0	20.0 22.2 24.4 26.7	21.06 23.37 25.69 28.11	22.1 24.5 27.0 29.5	1.0 1.0 1.0 1.0	29.2 32.4 35.5 38.9	20.5 18.5 16.9 15.4
1SMB26CAT3 1SMB28CAT3 1SMB30CAT3 1SMB33CAT3	MEC MGC MKC MMC	26 28 30 33	5.0 5.0 5.0 5.0 5.0	28.9 31.1 33.3 36.7	30.42 32.74 35.06 38.63	31.9 34.4 36.8 40.6	1.0 1.0 1.0 1.0	42.1 45.4 48.4 53.3	14.2 13.2 12.4 11.3
1SMB36CAT3 1SMB40CAT3 1SMB43CAT3 1SMB45CAT3	MPC MRC MTC MVC	36 40 43 45	5.0 5.0 5.0 5.0 5.0	40.0 44.4 47.8 50.0	42.11 46.74 50.32 52.63	44.2 49.1 52.8 55.3	1.0 1.0 1.0 1.0	58.1 64.5 69.4 72.2	10.3 9.3 8.6 8.3
1SMB48CAT3 1SMB51CAT3 1SMB54CAT3 1SMB58CAT3	MXC MZC NEC NGC	48 51 54 58	5.0 5.0 5.0 5.0	53.3 56.7 60.0 64.4	56.11 59.69 63.16 67.79	58.9 62.7 66.32 71.18	1.0 1.0 1.0 1.0	77.4 82.4 87.1 93.6	7.7 7.3 6.9 6.4
1SMB60CAT3 1SMB64CAT3 1SMB70CAT3 1SMB75CAT3	NKC NMC NPC NRC	60 64 70 75	5.0 5.0 5.0 5.0	66.7 71.1 77.8 83.3	70.21 74.84 81.90 91.65	73.72 78.58 85.99 92.07	1.0 1.0 1.0 1.0	96.8 103 113 121	6.2 5.8 5.3 4.9
1SMB78CAT3	NTC	78	5.0	86.7	91.26	95.83	1.0	126	4.7

4. A transient suppressor is normally selected according to the working peak reverse voltage (V_{RWM}), which should be equal to or greater than the DC or continuous peak operating voltage level.

5. V_{BR} measured at pulse test current I_T at an ambient temperature of 25°C.

6. Surge current waveform per Figure 2 and derate per Figure 3 of the General Data - 600 Watt at the beginning of this group.









Figure 3. Pulse Derating Curve





APPLICATION NOTES

RESPONSE TIME

In most applications, the transient suppressor device is placed in parallel with the equipment or component to be protected. In this situation, there is a time delay associated with the capacitance of the device and an overshoot condition associated with the inductance of the device and the inductance of the connection method. The capacitive effect is of minor importance in the parallel protection scheme because it only produces a time delay in the transition from the operating voltage to the clamp voltage as shown in Figure 4.

The inductive effects in the device are due to actual turn-on time (time required for the device to go from zero current to full current) and lead inductance. This inductive effect produces an overshoot in the voltage across the equipment or component being protected as shown in Figure 5. Minimizing this overshoot is very important in the application, since the main purpose for adding a transient suppressor is to clamp voltage spikes. The SMB series have a very good response time, typically < 1 ns and negligible inductance. However, external inductive effects could produce unacceptable overshoot. Proper circuit layout,

minimum lead lengths and placing the suppressor device as close as possible to the equipment or components to be protected will minimize this overshoot.

Some input impedance represented by Z_{in} is essential to prevent overstress of the protection device. This impedance should be as high as possible, without restricting the circuit operation.

DUTY CYCLE DERATING

The data of Figure 1 applies for non-repetitive conditions and at a lead temperature of 25°C. If the duty cycle increases, the peak power must be reduced as indicated by the curves of Figure 6. Average power must be derated as the lead or ambient temperature rises above 25°C. The average power derating curve normally given on data sheets may be normalized and used for this purpose.

At first glance the derating curves of Figure 6 appear to be in error as the 10 ms pulse has a higher derating factor than the 10 μ s pulse. However, when the derating factor for a given pulse of Figure 6 is multiplied by the peak power value of Figure 1 for the same pulse, the results follow the expected trend.









Figure 6. Typical Derating Factor for Duty Cycle

UL RECOGNITION

The entire series has *Underwriters Laboratory Recognition* for the classification of protectors (QVGV2) under the UL standard for safety 497B and File #116110. Many competitors only have one or two devices recognized or have recognition in a non-protective category. Some competitors have no recognition at all. With the UL497B recognition, our parts successfully passed several tests including Strike Voltage Breakdown test, Endurance Conditioning, Temperature test, Dielectric Voltage-Withstand test, Discharge test and several more.

Whereas, some competitors have only passed a flammability test for the package material, we have been recognized for much more to be included in their Protector category.

OUTLINE DIMENSIONS

Transient Voltage Suppressors – Surface Mounted

SMB DO-214AA CASE 403A-03 ISSUE D

600 Watt Peak Power



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NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

	INC	HES	MILLIMETERS			
DIM	MIN	MAX	MIN	MAX		
Α	0.160	0.180	4.06	4.57		
В	0.130	0.150	3.30	3.81		
C	0.075	0.095	1.90	2.41		
D	0.077	0.083	1.96	2.11		
н	0.0020	0.0060	0.051	0.152		
J	0.006	0.012	0.15	0.30		
K	0.030	0.050	0.76	1.27		
Р	0.020 REF		0.51 REF			
S	0.205	0.220	5.21	5.59		



SMB Footprint

<u>Notes</u>

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