DIN W72×H72, W48×H96, W144×H72mm Counter/Timer

Features

- •36 input modes and 20 output modes
- •Counting speed : 1cps/30cps/2kcps/5kcps
- •Selectable voltage input (PNP) or No voltage input (NPN)
- •Addition of Up/Down input mode
- •Wide range of power supply : 100-240VAC 50/60Hz

12-24VAC/DC (Option)

- •Selectable Counter/Timer by internal DIP switch
- •Various time range
- •Built-in Microprocessor



Please read "Caution for your safety" in operation 孙 manual before using.



Specifications

	Single p	reset	FX4	FX6	FX4H		
Model [Dual pre	set	FX4-2P	FX6-2P	FX4H-2P	FX4L-2P	FX6L-2P
-	Totalizer	(Indicator)	FX4-I	FX6-I	FX4H-I	FX4L-I	FX6L-I
Digit			4	6	4	4	6
Digit siz	e		W8×H14mm	$W4 \times H8mm$	W6×H10mm	W8×H	I14mm
Power s	upply		100-240VAC 50/60Hz, 12-24VAC/DC (Option)				
Allowab	le voltag	e range	$90 \sim 110\%$ of rated voltage				
Power consumption			 Indicator : Approx. 6VA(240VAC 60Hz), Approx. 2.7W(24VDC), Approx. 5.8VA(24VAC 60Hz) Single preset : Approx. 7VA(240VAC 60Hz), Approx. 3.3W(24VDC), Approx. 6.8VA(24VAC 60Hz) Dual preset: Approx. 8VA(240VAC 60Hz), Approx. 3.8W(24VDC), Approx. 7.6VA(24VAC 60Hz) 				
Max. co for CP1	ounting s , CP2	peed	Selectable 1cps/30cps/2kcps/5kcps by internal DIP switch				
Min. input RESET input signal width INHIBIT input		Approx. 20ms					
Input	()	CP2 input NHIBIT)	Input logic is selectable [Voltage input] Input impedance : 5.4k Ω , "H" level : 5-30VDC, "L" level : 0-2VDC				
	RESET input		[No-voltage input] Impedance at short-circuit : Max. 1k Q, Residual voltage at short-circuit : Max. 2VDC, Impedance at open-circuit : Min. 100k Q				
One-sh	not outpu	it time		gle preset type 🖙 (Il preset type ☞ 1s).05~5sec t. output 0.5sec fixed	, 2st. output : 0.05~;	ōsec
	Contac	Туре	Single preset type : SPDT(1c), Dual preset type : 1st output SPDT(1c), 2nd output SPDT(1c)				output SPDT(1c)
Control	Contac	Capacity		250VAC 3A at resistive load			
Control output	Solid-	Туре		t type:1 NPN oper type:1st output 1 1	n collector NPN open collector, 2	2nd output 1 NPN op	en collector
	Capacity		30VDC Max. 100mA Max.				
Memory protection			10 years(When using non-volatile semiconductor memory)				
External	l sensor	power	12VDC±10% 50mA Max.				
Ambient temperature			−10 ~ +55℃ (at non-freezing status)				
Storage temperature			-25 ~ +65℃ (at non-freezing status)				
Ambient humidity			35 ~ 85%RH				
Insulation resistance			Min. 100MΩ (at 500VDC mega)				
Dielectri	Dielectric strength		2000VAC 50/60Hz for 1 minute				
Noise st	trength -	AC power	$\pm 2kV$ the square wave noise (pulse width:1 μ s) by the noise simulator				
		DC power	±500	V the square wave	noise(pulse width:1µ	s) by the noise simu	lator

nsor ntroller

(A) Counter

(B) Timer

(C)

(D)

(E) Panel meter (F)

Tacho/

Speed/ Pulse

meter (G) Display

unit

Temp. controller

Power controller

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Specifications

	Maakastaat	0.75	1. 1	6 1 0 EEU : 1		6 1 1			
Vibration	Mechanical	0.75mm amplitude at frequency of 10 ~ 55Hz in each of X, Y, Z directions for 1 hour							
VIDIATION	Malfunction	0.5mm amplitude at frequency of 10 ~ 55Hz in each of X, Y, Z directions for 10 minutes							
Shock	Mechanical	300m/s ² (Approx. 30G) in X, Y, Z directions for 3 times							
	Malfunction	100m/s² (Approx. 10G) in X, Y, Z directions for 3 times							
Relay	Mechanical	Min. 10,000,000 operations							
life cycle	Electrical	Min. 100,000 operations at 250VAC 2A(resistive load)							
Approval		c Al us							
Unit weight		FX4 : Approx. 295g FX4-2P : Approx. 305g FX4-I : Approx. 260g	FX6: Approx. 305g FX6-2P: Approx. 315g FX6-I: Approx. 265g	FX4H: Approx. 325g FX4H-2P: Approx. 353g FX4H-I: Approx. 297g	FX4L-2P: Approx.544g FX4L-I: Approx.455g	FX6L-2P: Approx.550g FX6L-I: Approx.461g			

Connections



%CP2(INHIBIT) : Time hold terminal when using for timer. %It is operated by power ON start type when using for timer.

* (Note1) : Connection for PNP input (Note2) : Connection for NPN input

Up/Down Counter/Timer



Input logic selection

•FX series

Input logic is changeable by input logic selection switch located at the one-side of case.

• No-voltage input • Voltage input(PNP) (NPN)



•FXL series

Input logic is changeable by input logic selection switch located at the terminal block.



•FXH series

Input logic is changeable by input logic selection switch(SW3) located at inside of the case.



*Please be sure to turn power OFF before changing input logic.

Input & output connections

◎In case of operating the load by power supply of the sensor



•Please select proper capacity of load, because total value of load capacity and current consumption should not be exceed current capacity.(Max. 50mA)

\bigcirc How to count by external power supply

This unit starts to count when "High" level(5-30VDC) is applied at CP1 or CP2 after selecting PNP.



- The capacity of the load must not be exceed max. 30VDC, max. 100mA of the switching capacity of the transistor.
 Please do not supply the reverse polarity voltage.
- *Please connector the surge absorber (Diode) at both terminals of the load, in case of using the inductive load. (Relay, etc.)



OUsing 2 counters with one sensor

•Please connect as the power of sensor is supplied from only one of counters and design input logic with same way.



Up/Down Counter/Timer



Input operation(Counter)

Inp	Input mode		No-voltage input type(NPN)	Voltage input type(PNP)		
ON 4 OFF	Up/Down-A (Command input)	ON 2 3 OFF	$cp1 \begin{bmatrix} H \\ cp2 \end{bmatrix} \xrightarrow{H} \\ cp2 \begin{bmatrix} H \\ cp3 \end{bmatrix} \xrightarrow{2} \\ count \\ value \\ 0 \end{bmatrix} \xrightarrow{2} \\ count \\ cou$	$\begin{array}{c} cp1 \\ cp2 \\ cp2 \\ count \\ value \\ 0 \\ \end{array}$		
	Up/Down-B (Individual input)	ON OFF	cp1 H cp2 H Count 1 2 3 2 1 2 3 value 0	$\begin{array}{c} cp1 \\ cp2 \\ cp2 \\ count \\ value \\ 0 \end{array}$		
Up mode	Up/Down-C (Phase difference input)	ON 2 3 OFF	$\begin{array}{c} cp1 \\ H \\ cp2 \\ H \\ cont \\ value \\ 0 \\ \end{array}$	$\begin{array}{c} cp1 \\ L \\ cp2 \\ Count \\ value \\ 0 \\ \end{array}$		
	Up	ON 2 3	cp1 H cp2 H Count value 0	cp1 H cp2 H Count value 0		
	(Count up input)	OFF	cp1 H cp2 H cp2 L Count value 0	cp1 H No counting cp2 H 4 5 Count 1 2 3 4 5 value 0 1 2		
ON 4 OFF	Up/Down-D (Command input)	ON 2 3 OFF	cp1 H cp2 H cp2 H count $\frac{n}{n-1}$ $n-2$ $n-3$ $n-2$ $n-1$ $n-2$ $n-3$ value 0	$\begin{array}{c} cp1 & H \\ cp2 & H \\ Count \\ value \\ 0 \end{array} \xrightarrow{n n-1} n-2 n-3 n-2 n-1 n-2 n-3 n-3 n-3 n-3 n-3 n-3 n-3 n-3 n-3 n-3$		
	Up/Down-E (Individual input)	ON 2 3 OFF	cp1 $\overset{H}{\underset{\text{value}}{\overset{H}{\underset{\text{value}}{\overset{n}{\underset{n-1}{\underset{n-2}{\underset{n-3}{\underset{n-2}{\underset{n-2}{\underset{n-1}{\underset{n-1}{\underset{n-2}{\underset{n-3}{\atopn-3}{\underset{n-3}{\atopn-3}{\underset{n-3}{\underset{n-3}{\underset{n-3}{\atopn-3}{\underset{n-3}{\atopn-3}{\atopn-3}{\underset{n-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{\atopn-3}{n-3}{n-3}{n}}{n}}{n}}}}}}}}}}}}}}}}$	cp1 $\stackrel{H}{\underset{\text{cp2}}{}}$ $\stackrel{H}{\underset{\text{cp3}}{}{\underset{\text{cp3}}{}{\underset{\text{cp3}}{}{\underset{\text{cp3}}{}{\underset{\text{cp3}}{\underset{\text{cp3}}{}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}{}{\underset{\text{cp3}}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}}{\underset{\text{cp3}}{\underset{\text{cp3}}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{\text{cp3}}}{\underset{cp3}}}{\underset{cp3}}}{\underset{cp3}}}{\underset{cp3}}}{\underset{cp3}}}}}}}}}}}}}}}}}}}}}}}}}}}}}}}$		
Down mode	Up/Down-F (Phase difference input)	ON 2 3 OFF	$\begin{array}{c} cp1 & H \\ cp2 & H \\ cp2 & H \\ value \\ 0 \end{array} \xrightarrow{n-1} n-2 n-3 n-2 n-3 n-2 n-3 n-3 n-2 n-3 n-3 n-2 n-3 n-3 n-3 n-3 n-3 n-3 n-3 n-3 n-3 n-3$	$\begin{array}{c c} cp1 & H & \hline \\ \hline \\ cp2 & H & \hline \\ cp2 & H & \hline \\ cp1 & n-1 & n-2 & n-2 & n-1 \\ value & 0 & \hline \\ 0 & \hline \end{array}$		
	Down	ON 2 3	cp1 H cp2 H Count $n-1$ $n-2$ $No counting$ value $n-3$ $n-4$ $n-5$	$\begin{array}{c} cp1 \\ cp2 \\ cp2 \\ count \\ value \\ 0 \end{array}$		
	(Count down input)	OFF	cp1 H cp2 H Count $n-1$ value $n -2$ n -4 $n-5$	$\begin{array}{c c} cp1 & H & & & \\ cp2 & H & & & \\ cp2 & L & & & \\ Count & & & & \\ value & & & \\ 0 & & & \\ \end{array}$		

∗ ⑧ : Over Min. signal width, ⑨ : Over 1/2 of Min. signal width.

If the signal width of B or B is less than Min. signal width, ± 1 of count error is occured.

Up/Down Counter/Timer



Time setting mode(timer)

Output operation mode

	4	4	of the second output moc
utput mode (SW1)		ON Down mode	Operation after count up
F	Up, Up / Down-A, B, C	Down, Up / Down-D, E, F	The display value continues until Reset sig
	Reset	- Reset	applied and the output is held.
567 ON	1st Preset	1st Preset	 1st retained output and 2nd output a maintained until Reset signal is applied
OFF	1st Output	- 1st Output	• When using 1st output as one-shot outp
N	2nd Output	- 2nd Output - H H	it will return after operating for 0.5sec.
	2nd Preset	- Reset	-
567 ON	1st Preset	- 1st Preset	 The display value and output will be held u Reset input is applied.
OFF	2nd Output	- 1st Output	-
С			The display value will be Reset Start sta
	2nd Preset	- Reset	- as soon as it reaches to 2nd setting value • 1st retained output will be OFF after 2
567 ON	1st Preset	- 1st Preset	one-shot output. • 1st one-shot output will be reset af
OFF	1st Output	- 1st Output	operating 0.5sec., and it is not related 2nd output.
R			Display value will be maintained until 2
	Reset 2nd Preset	- Reset	 output is Off, then it will be reset. 1st retained output will be OFF after 2
567 ON	1st Preset	- 1st Preset	one-shot output.
OFF	1st Output	- 1st Output - Ind	 1st one-shot output will be reset a operating 0.5sec., and it is not related
К			2nd output. The display value continues until Reset sig
ĸ	Reset	- Reset -	applied.
567 ON	2nd Preset	- 2nd Preset	• 1st retained output will be OFF after 2 one-shot output.
	1st Output	- 1st Output	 1st one-shot output will be reset a operating 0.5sec., and it is not related
Р	2nd Output	- 2nd Output	2nd output.
P	Reset 🗖	Reset -	The display value will be Reset Start stat as soon as it reaches to 2nd setting value
567	2nd Preset	- 2nd Preset	 1st retained output will be OFF after 2 one-shot output.
	1st Output	- 1st Output	• 1st one-shot output will be reset after
OFF L	2nd Output	- 2nd Output	operating 0.5sec., and it is not related 2nd output.
Q		Reset -	- The display continues until 2nd output is O
567	2nd Preset	- 2nd Preset	• 1st retained output will be OFF after 2 one-shot output.
	1st Output	- 0 - 1st Output	 1st one-shot output will be reset after operating 0.5sec. not related to 2nd output
	2nd Output	- 2nd Output	
S		Down	_
	Reset	- 2nd Preset	<pre>- Up, Up/Down-A, B, C input mode -OUT1 is ON when(Display value) ≥</pre>
			_ (1st setting value) -OUT2 is ON when(Display value) ≥
Counter	2nd Output	_ 1st Output Ind Output	(Dual setting value)
567	Up / Down-A, B, C	Up / Down-D, E, F	• Down, Up/Down-D, E, F input mode
ON D F	Reset	- Reset - 2nd Preset -	-OUT1 is ON when(Display value) ≤ (1st setting value)
	1st Preset	- 1st Preset	_ −OUT2 is ON when(Display value) ≤ _ (Zero)
	1st Output 2nd Output	_ 1st Output 2nd Output H	-
S	Reset -	Popot	
Timer	2nd Preset	- Reset - 2nd Preset	- - - When it is used as Timer, 1st output and 2
567 ON	1st Preset	1st Preset	output are flashing repeatedly.
	2nd Output	2nd Output	

Proper usage

©Reset

Reset

In case of changing the input mode after supplying the power, please provide an external reset or manual reset. **If reset is not executed, the counter will be working in previous mode.**

•Reset signal width

To guarantee proper reset, the signal must be supplied for a minimum of **min. 20ms** regardless the signal comes from a contact or a solid-state input.



*In case of a contact reset, contact chattering will not affect the reset as long as it is applied for a minimum of 20ms.

**Input signal at CP1 & CP2 must be applied for a minimum of 50ms after the reset is removed.

OMinimum count signal width



* Please make duty ratio(ON/OFF) as 1:1.

**Minimum signal width 280cps : Min. 16.7ms 2kcps : Min. 0.25ms

©Maximum counting speed

This is a response speed per 1 sec. when the duty ratio (ON:OFF) of input signal is 1:1. If the duty ratio is not 1:1, the width between ON and OFF should be over min. signal width and the response speed will getting slower against input signal. If either ON or OFF signal is shorter than minimum signal width, this product may not respond.



Therefore Ta(ON width) and Tb(OFF width) needed to be over min.signal width.

Max. counting speed is 1/2 value of catalog spec. when duty ratio is 1:3.

lt can not respond because Max. signal width(1a) is small.

\bigcirc Power

The inner circuit voltage starts to rise up for the first 100ms after power on, the input may not work at this time. And also the inner circuit voltage drops down for the last 500ms after power off, the input may not work at this time.



