3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



1 Features and Benefits

- □ Wide operating voltage range: from 3.3V to 18V
- Less than 10μA average supply current in μ-Power Mode
- Flexible magnetic thresholds and temperature coefficient
- Integrated self-diagnostic functions activating dedicated Safe Mode
- □ Reverse supply voltage protection
- □ Under-Voltage Reset protection
- Thermal protection
- Optional IMC integration for lateral sensing
- □ Customer end-of-line programming
- □ Wide programmable magnetic Latch/Switch range
- Developed according to ISO26262-10, 9 as safety HW element out of context with ASIL-B level

3 Ordering Information

2 Application Examples

- □ Automotive, Consumer and Industrial
- Brake light wake-up switch
- Electronic Steering Column Lock
- Door latch system
- Seat positioning
- □ Sunroof/Tailgate opener
- Transmission applications
- Electrical power steering

Product Code	Temperature Code	Package Code	Option Code	Packing Form Code
MLX92292	L	SE	AAA-000	RE
MLX92292	L	UA	AAA-000	BU
MLX92292	L	SE	AAA-200	RE
MLX92292	L	DC	AAA-000	RE
MLX92292	L	SE	AAA-001	RE
MLX92291	L	SE	AAA-005	RE
MLX92291	L	SE	AAA-006	RE
MLX92291	L	SE	AAA-008	RE
MLX92291	L	SE	AAA-009	RE
MLX92291	L	SE	AAA-010	RE
MLX92291	L	SE	AAA-011	RE
MLX92291	L	SE	AAA-012	RE
MLX92291	L	SE	AAA-013	RE
MLX92291	L	SE	AAA-200	RE
MLX92291	L	SE	AAA-201	RE
MLX92291	L	SE	AAA-202	RE
MLX92291	L	SE	AAA-203	RE
MLX92291	L	SE	AAA-204	RE
MLX92291	L	SE	AAA-207	RE
MLX92271	L	SE	AAA-001	RE
MLX92271	L	SE	AAA-014	RE
MLX92271	L	SE	AAA-205	RE
MLX92271	L	SE	AAA-209	RE

Legend:

Temperature Code:	L (-40°C to 150°C)
Package Code:	SE = TSOT-3L
	UA =TO92-3L
	DC = SOIC-8 (dual die)
Option Code:	0xx => normal version (perpendicular sensitive)
	2xx => IMC version (lateral sensitive)
Packing Form:	BU=Bulk RE = Reel CA = Ammopack CR = Carton on Reel
Ordering example:	MLX92292LSE-AAA-000-RE

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4 Functional Diagram



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5 General Description

Melexis has made a major advance in magnetic sensing technology that will have widespread implications for modern automobile design the MLX92292 - effectively represents a whole new way of sensing. This device delivers switch functions, but unlike existing products on the market it can determine the presence of magnetic fields that are lateral, not just orthogonal, to it. The uniqueness of this offering is taken further by the fact that the MLX92292 switch is supporting an ASIL B safety integrity level (in accordance with ISO 26262), with an array of built-in diagnostic mechanisms available.

Flexibility is a key attribute of the MLX92292. OEM customers can choose straightforward pre-programmed units, or alternatively they can benefit from the end-of-line (EoL) programming capacity. Through this each device may be configured (via its output pin) during the OEM production process, so system optimization is fully realized. The programming facility also enables setting of both magnetic operating points to small increments across a range spanning -90mT to +90mT (-40mT to +40mT for lateral sensing versions).

The MLX92292 can be specified with standard orthogonal sensitivity or the lateral sensitivity option. The upshot of lateral sensitivity being that there is potential to replace multiple devices with a single surface mount unit, thereby saving valuable board space and lowering bill-of-materials costs. This stems directly from Melexis' proprietary Integrated Magnetic Concentrator (IMC[™]) technology, which enables substantial heightening of signal-to-noise ratios in magnetic field measurement. In addition, the capacity of this technology to sense laterally allows lower profile system implementations, as the magnet can move alongside the device rather than having to be above it.

Safeguarding the MLX92292 are reverse supply voltage, thermal, electro-static discharge (ESD) and overvoltage protections, plus Under-Voltage Reset features. With the capacity to deal with a 40V load dump, it can be connected directly to the vehicle battery. In order to achieve ASIL B compliance, numerous diagnostic/monitoring functions have been incorporated, including Hall sensor and analog frontend diagnostics. The device comprises a full set of programmable reporting features, giving it compatibility with any existing electronic control unit (ECU) interface. Only the normal application pins are required for this without need of additional diagnostic pins and thus simplifying the design concept considerably.

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6 Absolute Maximum Ratings

Parameter	Symbol	Value	Units
Supply voltage ^(1, 2)	V _{DD}	+28V	V
Supply voltage (Load Dump) ^(1, 4)	Vdd	+ 45V	V
Supply current ^(1, 2, 3)	I _{DD}	+20	mA
Supply current ^(1, 3, 4)	lod	+50	mA
Reverse supply voltage ^(1, 2)	Vddrev	-24	V
Reverse supply voltage ^(1, 4)	Vddrev	-30	V
Reverse supply current ^(1, 2, 5)	Iddrev	-20	mA
Reverse supply current ^(1, 4, 5)	Iddrev	-40	mA
Output voltage ^(1, 2)	Vout	+28	V
Output current ^(1, 2, 5)	Іоит	+20	mA
Reverse output voltage ⁽¹⁾	VOUTREV	-0.5	V
Reverse output current ^(1, 2)	Ioutrev	-50	mA
Maximum junction temperature ⁽⁶⁾	TJ	+165	°C
ESD sensitivity – HBM ⁽⁷⁾	-	8	kV
ESD sensitivity – CDM ⁽⁸⁾	-	1000	V

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

 $^{^{\}scriptscriptstyle 1}$ The maximum junction temperature should not be exceeded

² For maximum 1 hour

³ Including current through protection device

⁴ For maximum 500ms

⁵ Through protection device

⁶ For 1000 hours.

⁷ Human Body Model according AEC-Q100-002 standard

⁸ Charged Device Model according AEC-Q100-011 standard

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7 General Electrical Specifications

DC Operating Parameters V_{DD} = 3.3V to 18V, T_A = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ ⁽¹⁾	Max	Units
Under-Voltage Reset threshold	Vuvr	V _{DD} monitoring during Active phase	1.8	2.5	3	V
UVR reaction time ⁽²⁾	$\label{eq:reaction time} \mbox{R reaction time}^{(2)} \qquad \mbox{tuv}_{R} \qquad \begin{tabular}{c} V_{DD} \\ V_{DD} \end{tabular}$			1	_	μs
Minimum supply voltage for defined output state ⁽²⁾	V _{DD1}	R _{PU} = 2.2kΩ, V _{PU} = 5V		1	1.2	V
Output leakage ⁽⁸⁾	IOFF	Vout =18V, T _A = -4085°C		0.1	1	μΑ
Output leakage	I _{OFF}	V _{OUT} =18V	—	—	5	μA
Output saturation voltage	Vol	Fast Mode, I _{OL} =20mA	0.1	0.25	0.7	V
Output saturation voltage	Vol	µ-Power Mode, Io∟=10mA	—	0.15	0.5	V
Output Rise Time ^(2,5) (R _{PU} dependent)	t _R	R_{PU} =2.2k Ω , V_{DD} =12V, V_{PU} =5V C _{LOAD} =50pF to GND	0.3	0.6	1	μs
Output Fall Time ^(2,5) (On-chip controlled)	t⊧	R_{PU} =2.2k Ω , V_{DD} =12V, V_{PU} =5V C _{LOAD} =50pF to GND	0.3	0.6	1	μs
Power-On time ^(3, 4)	ton	V_{DD} =5V, dV_{DD}/dt > 2V/us		0.5	1	ms
Power-On state	-	Output state during toN		High		
Output update period	TUPD	Fast Mode		40	45	μs
Programmable operating (output update) period	T _{OP}	μ-Power Mode, typical range	0.16(1)	_	260(1)	ms
Operating period 1 (1 st ref. value)	T _{OP1}	μ-Power Mode	196	222	246	ms
Operating period 2 (2 nd ref. value)	T _{OP2}	μ-Power Mode	40	45	50	ms
Programmable diagnostic period in Fail Safe state	T _{DP}	Fast Mode, typical range	0.13(1)	_	260(1)	ms
Programmable "Output Ticking" repetition period	Ттіск	Equal to (multiple of) TOP, typical range	0.6(1)	_	260(1)	ms
Programmable "Output Ticking" duration	tтіск	Typical range	4	—	128	μs
Active phase duration, diagnostic On	t _{ACT_Don}	μ -Power Mode, defined at I _{DD} > 0.7mA	—	40	—	μs
Active phase duration, diagnostic Off	t _{ACT_Doff}	μ -Power Mode, defined at I _{DD} > 0.7mA	—	24	—	μs
Tolerance of operating period ratio t _{ACT} /T _{OP}	RTOL	µ-Power Mode	-5	0	5	%
Active phase supply current, diagnostic On (average value)	IDDACT_Don	µ-Power Mode	1.8	2.4	2.9	mA
Active phase supply current, diagnostic Off (average value)	IDDACT_Doff	µ-Power Mode	2.2	3	3.5	mA
Standby phase supply current ⁽⁸⁾	IDDSTBY	$V_{DD} \le 16V, T_A = -4085^{\circ}C$	—	6	9	μA
Standby phase supply current	IDDSTBY	$V_{DD} \le 16V$	—	6	27	μA
Average supply current ^(8,9)	IDDAVG1	$V_{DD} \le 16V, T_A = -4085^{\circ}C, T_{OP} = T_{OP1}$	—	6.4	9.5	μA
Average supply current ^(8,9)	IDDAVG2	$V_{DD} \le 16V, T_A = -4085^{\circ}C, T_{OP} = T_{OP2}$	_	8.1	11.7	μA
Step response time ⁽²⁾	response time ⁽²⁾ t_{RESP} Fast Mode, $B_{OP} = 1mT$, $B_{RP} = -1mT$, square wave magnetic field with $B > \pm 4mT$, $t_{RISE} = t_{FALL} \le 5\mu s$		15	40	65	μs
Signal bandwidth ^(2,6)	Signal bandwidth ^(2,6) BW		6	8	_	kHz
Peak supply current ⁽²⁾	IDDPEAK	amplitude 5mT For peak duration ≥ 5µs	_	2.9	3.6	mA
Fast Mode supply current	IDDFAST		2.2	2.9	3.5	mA
Fast Mode fail supply current	IDDFAIL		0.1	0.3	0.6	mA
Reverse supply current	IDDREV	V _{DD} = -16V	-1	_	_	mA

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Thermal Protection Activation	TPROT		_	185(7)	_	°C
Thermal Protection Release	T _{REL}		—	175(7)	—	°C
UA package thermal resistance	Rthja	Single layer PCB, JEDEC standard test boards, still air (LFPM=0)	_	200	_	°C/W
SE package thermal resistance	Rthja	Single layer PCB, JEDEC standard test boards, still air (LFPM=0)	_	300	_	°C/W

¹ Unless otherwise specified the typical values are defined at $T_A = +25^{\circ}C$ and $V_{DD} = 12V$

3 The Power-On Time represents the time from reaching V_{DD} = 3.3V to the first refresh of the output state.

4 Power-On Slew Rate is not critical for the proper device start-up.

- 5 R_{PU} and V_{PU} are respectively the external pull-up resistor and pull-up power supply
- 6 OUT switching should track magnetic field frequency without missing pulses

7 T_{PROT} and T_{REL} are the corresponding junction temperature values

8 Guaranteed by correlation with production test at $T_A=150^{\circ}$ C and verified by characterization

9 Average current consumption for μ -Power Mode with diagnostic On

 $I_{DDAVG} = \frac{I_{DDACT} * t_{ACT} * (1 + R_{TOL}/100) + I_{DDSTBY} * (T_{OP} - t_{ACT} * (1 + R_{TOL}/100))}{(1 + R_{TOL}/100)}$

Тор

Where t_{act} and T_{OP} are always typical values. The maximum I_{DDACT} , I_{DDSTBY} and R_{TOL} spec values should be used for the maximum I_{DDAVG} calculation.



² Guaranteed by design and verified by characterization, not production tested

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8 Version specific parameters

8.1 MLX92292LSE-AAA-000-RE/MLX92292LUA-AAA-000-BU/MLX92292LDC-AAA-000-RE

DC Operating Parameters V_{DD} = 3.3V to 18V, T_A = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ ⁽¹⁾	Max	
Operating Point programming range	Вор	V _{DD} =12V, T _A =25°C	-90	_	90	mT
Release Point programming range	Brp	V _{DD} =12V, T _A =25°C	-90	_	90	mT
Operating Point magnitude programming ^(2, 3)	Вор		_	12	_	bit
Release Point magnitude programming ^(2, 3)	Brp		_	12	_	bit
Operating Point polarity selection	Bop		_	1	—	bit
Release Point polarity selection	B _{RP}		_	1	_	bit
Direct or inverted output selection	_		_	1	_	bit
Factory pre-programmed Operating Point, Latch	Вор	V _{DD} =12V, T _A =25°C, programming target 25mT	22	25	28	mT
Factory pre-programmed Release Point, Latch	Brp	V _{DD} =12V, T _A =25°C, programming target -25mT	-28	-25	-22	mT
Temperature Coefficient programming range (4,5,6)	TC	V_{DD} =12V, Latch with B_{OP} =25mT, B_{RP} = -25mT	-2400		0	ppm/°C
Temperature Coefficient selection	TC		_	5	_	bit

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	TickMsg	45	-	128	45

¹ Unless otherwise specified the typical values are defined at T_A = +25°C and V_{DD} = 12V

- ² The programming step is <0.5% of the programmed BOP or BRP value for |BOP| or $|BRP| \ge 6mT$ and <0.02mT for |BOP| or $|BRP| \le 6mT$ at TA = +25°C
- ³ Very low hysteresis magnitude (BOP BRP < 1mT) could lead to output toggling due to noise and mechanical looseness in the magnetic system.

⁴ The Temperature Coefficient is calculated using following formula:

$$TC = \frac{B_{XPTA2} - B_{XPTA1}}{B_{XPTA1} * (T_{A2} - T_{A1})} * 10^{6}, ppm/°C$$
where:
 $T_{A1} = 25°C, T_{A2} = 150°C,$

In case of magnetic Latch application: B_{XPTA1} (B_{XPTA2}) = B_{OP} - B_{RP} at T_{A1} (T_{A2})

In case of magnetic Switch application: B_{XPTA1} (B_{XPTA2}) = B_{OP} or B_{RP} at T_{A1} (T_{A2})

⁵ The factory pre-programmed target TC value is 0ppm/°C.

⁶ TC target values

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8.2 MLX92292LSE-AAA-200-RE

DC Operating Parameters V_{DD} = 3.3V to 18V, T_A = -40°C to 150°C (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ ⁽¹⁾	Max	
Operating Point programming range	Вор	V _{DD} =12V, T _A =25°C	-40	—	40	mT
Release Point programming range	B _{RP}	V _{DD} =12V, T _A =25°C	-40	_	40	mT
Operating Point magnitude programming ^(2,3)	Вор		_	12	_	bit
Release Point magnitude programming ^(2,3)	B _{RP}		_	12	_	bit
Operating Point polarity selection	BOP		_	1	_	bit
Release Point polarity selection	B _{RP}		_	1	_	bit
Direct or inverted output selection	_		_	1	_	bit
Factory pre-programmed Operating Point, Latch	Вор	V _{DD} =12V, T _A =25°C, programming target 20mT	17	20	23	mT
Factory pre-programmed Release Point, Latch	Brp	V _{DD} =12V, T _A =25°C, programming target -20mT	-23	-20	-17	mT
Temperature Coefficient programming range (4.5.6)	TC	V_{DD} =12V, Latch with B_{OP} =20mT, B_{RP} = -20mT	-2400		0	ppm/°C
Temperature Coefficient selection	TC		_	5	_	bit

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms	
Yes	TickMsg	45	-	128	45	

³ Very low hysteresis magnitude (BOP – BRP < 1mT) could lead to output toggling due to noise and mechanical looseness in the magnetic system.

$$TC = \frac{B_{XPTA2} - B_{XPTA1}}{B_{XPTA1} * (T_{A2} - T_{A1})} * 10^{6}, ppm/°C$$
where:

$$T_{A1} = 25°C, T_{A2} = 150°C,$$
In case of magnetic Latch application: $B_{XPTA1} (B_{XPTA2}) = B_{OP} - B_{P}$

In case of magnetic Latch application: B_{XPTA1} (B_{XPTA2}) = B_{OP}-B_{RP} at T_{A1} (T_{A2}) In case of magnetic Switch application: B_{XPTA1} (B_{XPTA2}) = B_{OP} or B_{RP} at T_{A1} (T_{A2})

⁵ The factory pre-programmed target TC value is 0ppm/°C.

⁶ TC target values

¹ Unless otherwise specified the typical values are defined at $T_A = +25$ °C and $V_{DD} = 12V$

² The programming step is <0.5% of the programmed BOP or BRP value for |BOP| or $|BRP| \ge 6mT$ and <0.02mT for |BOP| or $|BRP| \le 6mT$ at TA = +25°C

⁴ The Temperature Coefficient is calculated using following formula:

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8.3 MLX92292LSE-AAA-001-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point Bop (mT)			Release Point B _{RP} (mT)			Output behaviour	Active Pole	
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	8.7	11	13.5	7.2	9.3	11.7			
T _A = 25°C	8.6	10	11.4	7.2	8.5	9.8	-1100 ⁽²⁾	Inverted switch	South pole
T _A = 150°C	6.7	8.6	10.7	5.6	7.4	9.4		Switch	

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	uNoDiag	0.16	-	-	-

8.4 MLX92291LSE-AAA-005-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_{A} = -40°C to 150°C

Test Condition	Operating Point B _{OP} (mT)				Release Point B _{RP} (mT)			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-8.5	-6.4	-4.4	-9.7	-7.5	-5.4			
T _A = 25°C	-7.2	-6	-4.9	-8.2	-7	-5.8	-1100 ⁽²⁾	Direct switch	North pole
T _A = 150°C	-7.2	-5.2	-3.3	-8.2	-6	-4		Switch	

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	OutOnMsg	50	-	-	-

8.5 MLX92291LSE-AAA-006-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition			Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole	
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-6.8	-4.8	-2.9	-7.9	-5.9	-3.9			
T _A = 25°C	-5.6	-4.5	-3.4	-6.6	-5.5	-4.4	-1000 ⁽²⁾	Inverted switch	North pole
T _A = 150°C	-5.8	-3.9	-2.1	-6.8	-4.8	-2.9		Switch	

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	uNoDiag	60	-	-	-

² Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10 # 390109229202

¹ Unless otherwise specified the typical values are defined at $T_A = +25^{\circ}C$ and $V_{DD} = 12V$. Melexis production testing is limited to version specific parameters only.

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8.6 MLX92291LSE-AAA-008-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point Bop (mT) ⁽³⁾				Release Point B _{RP} (mT) ⁽³⁾			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-21.4	-18.1	-15	-22.6	-19.1	-16			
T _A = 25°C	-18.7	-16.85	-15.1	-19.8	-17.85	-16	-1100 ⁽²⁾	DO ⁽²⁾ Direct switch	North pole
T _A = 150°C	-18.1	-14.5	-11.4	-19.1	-15.4	-12.1		SWILCH	

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	uNoDiag	44	-	-	-

8.7 MLX92291LSE-AAA-009-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point B _{OP} (mT)				Release Point B _{RP} (mT)			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-6.4	-4.5	-2.6	-7.5	-5.5	-3.6			
T _A = 25°C	-5.6	-4.5	-3.4	-6.6	-5.5	-4.4	0 ⁽²⁾	Inverted switch	North pole
T _A = 150°C	-6.6	-4.5	-2.5	-7.7	-5.5	-3.4			

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	uNoDiag	10	-	-	-

8.8 MLX92291LSE-AAA-010-RE

Test Condition				Release Point B _{RP} (mT) ⁽³⁾			Output behaviour	Active Pole	
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	5.2	6.9	8.6	3.1	4.6	6.2			
T _A = 25°C	5.2	6.4	7.6	3.2	4.3	5.4	-1100 ⁽²⁾	Direct switch	South pole
T _A = 150°C	3.8	5.5	7.4	2.2	3.7	5.3		50010011	

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	TickMsg	10	-	128	10

 $^{^{1}}$ Unless otherwise specified the typical values are defined at T_{A} = +25°C and V_{DD} = 12V. Melexis production testing is limited to version specific parameters only.

² Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10 ³ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations

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3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



8.9 MLX92291LSE-AAA-011-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point Bop (mT) ⁽³⁾			Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-26.3	-22.3	-18.5	-28.7	-24.4	-20.4			
T _A = 25°C	-23.3	-20.9	-18.6	-25.4	-22.9	-20.5	-999 ⁽²⁾	Direct switch	North pole
T _A = 150°C	-22.8	-18.3	-14.3	-24.8	-20.0	-15.8		50010011	

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	TickOnMsg	0.86	-	128	1.72

8.10 MLX92291LSE-AAA-012-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_{A} = -40°C to 150°C

Test Condition					Release Point B _{RP} (mT) ⁽³⁾			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	3.9	5.9	7.9	2.9	4.8	6.8		Direct switch	
T _A = 25°C	4.4	5.5	6.6	3.4	4.5	5.6	-1000 ⁽²⁾		South pole
T _A = 150°C	2.9	4.8	6.8	2.1	3.9	5.8			

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	uNoDiag	60	-	-	-

8.11 MLX92291LSE-AAA-013-RE

Test Condition	Condition Operating Point Bop (mT) ⁽³⁾		Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole	
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	24.3	28.7	33.6	22.2	26.4	31.0			
T _A = 25°C	24.2	26.8	29.6	22.1	24.6	27.2	-1100 ⁽²⁾	Direct	Courth reals
$T_A = 105^{\circ}C^{(4)}$	20.3	24.2	29.0	18.5	22.4	26.8	-1100(-/	switch	South pole
T _A = 150°C	18.4	23.1	28.5	16.8	21.2	26.2			

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	uNoDiag	1	-	-	-

¹ Unless otherwise specified the typical values are defined at $T_A = +25^{\circ}C$ and $V_{DD} = 12V$. Melexis production testing is limited to version specific parameters only.

² Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10

³ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations

⁴ Table " $T_A = 105$ °C" is based on theoretical calculations only, not production tested.

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



8.12 MLX92291LSE-AAA-200-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point Bop (mT)				Release Point BRP (mT)			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	7.6	10.5	13.6	5.6	8.1	11			
T _A = 25°C	7	9	11	5.1	7	8.9	-2200 ⁽²⁾	Inverted switch	South pole
T _A = 150°C	4.5	6.6	8.9	3.2	5.2	7.2			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	FlddMsg	-	2	-	-

8.13 MLX92291LSE-AAA-201-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_{A} = -40°C to 150°C

Test Condition	Operating Point B _{OP} (mT) ⁽³⁾				Release Point B _{RP} (mT) ⁽³⁾			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	7.0	9.6	12.5	6.7	9.3	12.1			
T _A = 25°C	7.0	8.8	10.7	6.7	8.5	10.4	-1100 ⁽²⁾	Inverted switch	South pole
T _A = 150°C	5.4	7.7	10.2	5.2	7.4	9.9			

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	uNoDiag	45	-	-	-

8.14 MLX92291LSE-AAA-202-RE

Test Condition	Operating Point Bop (mT)				Release Point B _{RP} (mT)			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	3.8	5.9	8.2	2.8	4.8	7.0		Inverted switch	
T _A = 25°C	3.8	5.4	7.0	2.9	4.4	6.0	-1100 ⁽²⁾		South pole
T _A = 150°C	2.8	4.7	6.8	2.0	3.9	5.8			

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	uNoDiag	10	-	-	-

 $^{^{1}}$ Unless otherwise specified the typical values are defined at T_A = +25°C and V_{DD} = 12V. Melexis production testing is limited to version specific parameters only. 2 Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10.

³ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations.

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



8.15 MLX92291LSE-AAA-203-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point Bop (mT) ⁽³⁾				Release Point B _{RP} (mT) ⁽³⁾			Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-5.9	-3.6	-1.3	-7.0	-4.6	-2.3			North pole
T _A = 25°C	-4.8	-3.33	-1.8	-5.9	-4.33	-2.8	-1100 ⁽²⁾	Direct switch	
T _A = 150°C	-5.1	-2.9	-0.7	-6.0	-3.7	-1.6			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	uNoDiag	44	-	-	-

8.16 MLX92291LSE-AAA-204-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Test Condition Operating F B _{OP} (mT) ⁽³⁾			Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-23.9	-19.9	-16.3	-24.4	-20.5	-16.8			North pole
T _A = 25°C	-20.9	-18.6	-16.4	-21.5	-19.1	-16.8	-1100 ⁽²⁾	Direct switch	
T _A = 150°C	-20.2	-16.0	-12.3	-20.7	-16.5	-12.7			

ІМС	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	OutOnMsg	50	-	-	-

8.17 MLX92291LSE-AAA-207-RE

Test Condition	est Condition Operating Point Bop (mT) ⁽³⁾			Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	0.4	2.8	5.3	-0.5	1.9	4.3			
T _A = 25°C	1.0	2.5	4.0	0.2	1.7	3.2	-2000 ⁽²⁾	Direct switch	South pole
T _A = 150°C	-0.1	1.9	3.9	-0.7	1.3	3.3			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	uNoDiag	6.8	-	-	-

¹ Unless otherwise specified the typical values are defined at $T_A = +25$ °C and $V_{DD} = 12V$. Melexis production testing is limited to version specific parameters only.

² Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10 ³ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations

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3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



8.18 MLX92271LSE-AAA-001-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	est Condition Operating Point Bop (mT) ⁽³⁾		Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole	
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-0.9	0.4	1.9	-1.9	-0.4	0.9		Latch	South pole
T _A = 25°C	-0.9	0.4	1.9	-1.9	-0.4	0.9	0 ⁽²⁾		
T _A = 150°C	-0.9	0.4	1.9	-1.9	-0.4	0.9			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	FNoDiag	-	-	-	-

8.19 MLX92271LSE-AAA-014-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition	Operating Point B _{OP} (mT) ⁽³⁾			Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-0.1	2.1	4.5	-4.5	-2.1	0.1			South pole
T _A = 25°C	0.5	2	3.5	-3.5	-2	-0.5	-1100 ⁽²⁾	Latch	
T _A = 150°C	-0.4	1.7	3.9	-3.9	-1.7	0.4			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
No	FNoDiag	-	-	-	-

8.20 MLX92271LSE-AAA-205-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_A = -40°C to 150°C

Test Condition				Release B _{RP} (m1			TC (ppm/°C)	Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	-0.9	1.5	3.9	-3.9	-1.5	0.9			
T _A = 25°C	-0.2	1.5	3.2	-3.2	-1.5	0.2	0 ⁽²⁾	Latch	South pole
T _A = 150°C	-0.9	1.5	4.0	-4.0	-1.5	0.9			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	FNoDiag	-	-	-	-

¹ Unless otherwise specified the typical values are defined at T_A = +25°C and V_{DD} = 12V. Melexis production testing is limited to version specific parameters only. ² Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10.

³ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations.

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3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



8.21 MLX92271LSE-AAA-209-RE

DC Operating Parameters V_{DD} = 3.3 to 18V, T_{A} = -40°C to 150°C

Test Condition Oper Bop (ing Point T) ⁽³⁾			Release Point B _{RP} (mT) ⁽³⁾		TC (ppm/°C)	Output behaviour	Active Pole
	Min	Typ ⁽¹⁾	Max	Min	Typ ⁽¹⁾	Max	Typ ⁽¹⁾		
T _A = -40°C	6.6	9.4	12.3	-12.3	-9.4	-6.6			South pole
T _A = 25°C	6.9	8.75	10.6	-10.6	-8.75	-6.9	-1100 ⁽²⁾	Latch	
T _A = 150°C	4.9	7.5	10.5	-10.5	-7.5	-4.9			

IMC	Safe message	Operating period, ms	Diagnostic period in Fail Safe state, ms	"Output Ticking" duration, μs	"Output Ticking" repetition period, ms
Yes	FNoDiag	-	-	-	-

¹ Unless otherwise specified the typical values are defined at $T_A = +25^{\circ}C$ and $V_{DD} = 12V$. Melexis production testing is limited to version specific parameters only. ² Typical TC programmed. The Temperature Coefficient is calculated using formula from page 9/10.

³ Final magnetic parameters will be covered in the PPAP documentation set, the table below is based on theoretical calculations.

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet





3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



9 Magnetic Behaviour

9.1 Latch Sensor



South Pole Active Latch





Direct South Pole Active Switch



North Pole Active Latch



Inverted South Pole Active Switch

3-wire $\mu Power$ programmable ASIL B capable Hall Effect Latch/Switch Datasheet



Direct North Pole Active Switch

Inverted North Pole Active Switch



3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



10 Functional Safety Capability

10.1 Sensor Development

MLX92292 is developed according to the ISO26262 requirements for ASIL B level.

10.2 Technical Safety Requirements

The main (mission) technical safety requirement for MLX92292 is the following:

10.2.1 TS_RQT_Mission

ASIL: B

MLX92292 shall not provide wrong information about the detection of presence of magnetic field done by comparing the magnetic field with magnetic thresholds Bop and Brp, each of them being within a Safe Tolerance Interval (STI) defined in mT as ±a*Bxp ±b, where Bxp is the actual magnetic threshold (Bop or Brp) and a, b are parameters depending on sensitivity direction and application temperature range. The following table shows the values of a and b for normal and lateral sensitivity device (with IMC) at 25°C and over the full temperature range.

	Ta = 25°C	Ta = -40°C150°C
Normal (orthogonal)	a = 0.18 (18%)	a = 0.22 (22%)
sensitive device	b = 1.4mT	b = 1.7mT
Lateral sensitive device	a = 0.18 (18%)	a = 0.22 (22%)
(with IMC)	b = 2mT	b = 2.4mT

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10.2.2 TS_RQT_Safe_Message

MLX92292 shall report detected failures that could prevent TS_RQT_Mission.

One of the following programmable Safe Message options can be chosen depending on the application:

Message Option	B <brp Diagnostic OK</brp 	B>Bop Diagnostic OK	All B values Diagnostic Failed	Diagnostic Coverage	Safe States
μ-Power Mode				SPFM	
TickMsg ^(1,2)	OUT = Off+Ticking	OUT = On+Ticking	OUT = Off	92%	B _{OP} , B _{RP} within STI ; OUT = On/Off w/o ticking
TickOffMsg ⁽¹⁾	OUT = Off+Ticking	OUT = On	OUT = Off	82%	Bop, B _{RP} within STI ; OUT = Off
TickOnMsg ⁽²⁾	OUT = Off	OUT = On+Ticking	OUT = On	55%	Bop, B _{RP} within STI ; OUT = On
OutOffMsg	OUT = Off	OUT = On	OUT = Off	82%	B _{OP} , B _{RP} within STI ; OUT = Off
OutOnMsg	OUT = Off	OUT = On	OUT = On	55%	Bop, B _{RP} within STI ; OUT = On
StbyX2Msg ⁽³⁾	OUT = Off	OUT = On	OUT = Off Twice increased Top	77%	Bop, B _{RP} within STI ; Twice increased Top
uNoDiag ⁽⁴⁾	OUT = Off	OUT = On	-	No diagnostic	BOP, BRP within STI
Fast Mode				_	
FlddMsg	OUT = Off, Idd = Iddfast	OUT = On, Idd = Iddfast	OUT = Off, Idd = Iddfail	67%	B _{OP} , B _{RP} within STI ; OUT=Off & I _{DD} =I _{DDFAIL}
FNoDiag ⁽⁴⁾	OUT = Off	OUT = On	-	No diagnostic	BOP, BRP within STI

Note (1) Off+Ticking signal means that the Off state duration lasts significantly longer than the On state duration.

Note (2) On+Ticking signal means that the On state duration lasts significantly longer than the Off state duration.

Note (3) If T_{OP} is set <5ms then T_{OP} increases less than twice in case of diagnostic fail.

Note (4) This message option does not offer integrated diagnostic.

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



11 Application Information

11.1 Typical Automotive Application Circuit



Notes:

1. For proper operation, a 10nF to 100nF bypass capacitor should be placed as close as possible to the V_{DD} and ground pin.

2. A capacitor connected to the output will improve the EMC performance

11.2 Automotive and Harsh, Noisy Environments Application Circuit



Notes:

1. For proper operation the bypass capacitor C1 should be placed as close as possible to the VDD and GND pins.

2. If negative transients over supply line V_{PEAK} -30V are expected, usage of the diode D1 is recommended. Otherwise only R1 is sufficient. When selecting the resistor R1, three points are important:

- the resistor has to limit $I_{\text{DD}}/I_{\text{DDREV}}$ to 40mA maximum
- the resistor has to withstand the power dissipated in both over voltage conditions ($V_{R1}^2/R1$)
- the resulting device supply voltage V_{DD} has to be higher than V_{DD} min (V_{DD} = V_{CC} R1.I_{DD})

3. If positive transients over supply line with V_{PEAK}> 40V are expected, usage of Zener diode Z1 is recommended. The R1-Z1 network should be sized to limit the voltage over the device below the maximum allowed.

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



12 Package Information

12.1 TSOT-3L (SE Package)

12.1.1 TSOT-3L – Package dimensions



SY MBOL	MINIMUM	MAXIMUM
A		1.00
A1	0.025	0.10
A2	0.85	0.90
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.70
L	0.30	0.50
b	0.30	0.45
С	0.10	0.20
е	0.95	BSC
e1	1.90	BSC
α	0°	8°

NOTE :

1. ALL DIMENSIONS IN MILLIMETERS (mm) UNLESS OTHERWISE STATED.

2. DIMENSION D DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS OF MAX 0.15 mm PER SIDE.

3. DIMENSION E DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS OF MAX 0.25 mm PER SIDE.

4. DIMENSION & DOES NOT INCLUDE DAMBAR PROTRUSION OF MAX 0.07 mm.

5. DIMENSION L IS THE LENGTH OF THE TERMINAL FOR SOLDERING TO A SUBTRATE.

6. FORMED LEAD SHALL BE PLANAR WITH RESPECT TO ONE ANOTHER WITH 0.076 mm SEATING PLANE.

12.1.2 TSOT-3L - Sensitive spot



3-wire $\mu Power$ programmable ASIL B capable Hall Effect Latch/Switch Datasheet



12.1.3 TSOT-3L – Package marking / Pin definition



Pin #	Name	Туре	Function
1	VDD	Supply	Supply Voltage pin
2	OUT	Out	Open drain output pin
3	GND	Ground	Ground pin

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12.2 TO92-3L (UA Package)

12.2.1 TO92-3L – Package dimensions



S≻∑BOL	MINIMUM	MAXIMUM
Α	2.90	3.10
D	4.00	4.20
E	1.40	1.60
F	0.00	0.15
J	2.51	2.72
L	14.00	15.00
L1	0.90	1.10
S	0.63	0.84
b1	0.35	0.44
b2	0.43	0.52
С	0.35	0.44
е	2.51	2.57
e1	1.24	1.30

NOTES :

- 1. DIMENSIONS IN MILLIMETERS (mm) UNLESS NOTED OTHERWISE.
- 2. PACKAGE DIMENSIONS DO NOT INCLUDE MOLD FLASHES AND PROTRUSIONS.
- 3. DIMENSION A AND D DO NOT INCLUDE MOLD GATE AND SIDE FLASH (PROTRUSION) of MAXIMUM 0.127 mm PER SIDE.
- 4. THE LEADS MAY BE SLIGHTLY DEFORMED DURING TRANSPORTATION IF PACKED IN BULK (BAG), AFFECTING e1 DIMENSION. IT IS RECOMMENDED TO ORDER RADIAL TAPE (REEL OR AMMOPACK) IF SUCH DEFORMATION IS CRITICAL FOR THE LEAD FORMING PROCESS, EVEN IF MANUAL LOADING INTO THE TOOL IS FORESEEN.

12.2.2 TO92-3L - Sensitive spot



3-wire $\mu Power$ programmable ASIL B capable Hall Effect Latch/Switch <code>Datasheet</code>



12.2.3 TO92-3L – Package marking / Pin definition



Pin #	Name	Туре	Function
1	VDD	Supply	Supply Voltage pin
2	GND	Ground	Ground pin
3	OUT	Out	Open drain output pin

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



12.3 SOIC8 (DC Package)

12.3.1 SOIC8– Package dimensions



N≻∑BOL	MINIMUM	MAXIMUM
А	1.52	1.73
A1	0.10	0.25
A2	1.37	1.57
D	4.80	4.98
E	3.81	3.99
Н	5.80	6.20
L	0.41	1.27
b	0.35	0.49
С	0.19	0.25
h	0.25	0.50
е	1.27 BSC	
α	0°	8°

TOP VIEW

SIDE VIEW



SIDE VIEW

NOTE :

1. ALL DIMENSIONS IN MILLIMETERS (mm) UNLESS OTHERWISE STATED.

2. DIMENSION D DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS OF MAX 0.15 mm PER SIDE.

3. DIMENSION E DOES NOT INCLUDE INTERLEADS FLASH OR PROTRUSIONS OF MAX 0.25 mm PER SIDE.

4. DIMENSION & DOES NOT INCLUDE DAMBAR PROTRUSION OF MAX 0.08 mm.

3-wire $\mu Power$ programmable ASIL B capable Hall Effect Latch/Switch <code>Datasheet</code>



12.3.2 SOIC8- Sensitive spot



3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



12.3.3 SOIC8- Package marking / Pin definition



Marking





Pin #	Name	Туре	Function
1	GND2	Ground	Ground pin DIE 2
2	OUT2	Output	Output DIE 2
3	N.C.		Not connected
4	OUT1	Output	Output DIE 1
5	GND1	Ground	Ground DIE 1
6	VDD1	Supply	Supply DIE 1
7	N.C.		Not connected
8	VDD2	Supply	Supply DIE 2

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



13 Standard information regarding manufacturability

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD's (Surface Mount Devices)

- IPC/JEDEC J-STD-020 Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113
 Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20 Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15 Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD's (Through Hole Devices)

• EN60749-15 Resistance to soldering temperature for through-hole mounted devices

Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

• EIA/JEDEC JESD22-B102 and EN60749-21 Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website: <u>http://www.melexis.com/quality.aspx</u>

14 ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD). Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

3-wire µPower programmable ASIL B capable Hall Effect Latch/Switch Datasheet



15 Contact

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For additional information, please contact our Direct Sales team and get help for your specific needs:

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