

# CBTVS2A16-1F3

## Circuit breaker with transient voltage suppressor

### Datasheet - production data



## Features

- Transient voltage suppressor (TVS)
- Non-resettable over current protection (OCP)
- Electrostatic discharge protection
- Electrical overstressed protection (OVP)
- Unidirectional device
- Low clamping factor V<sub>CL</sub> / V<sub>BR</sub>
- Fast response time
- Very thin package: 0.5 mm

## Complies with the following standards:

- IEC 61000-4-2 level 4:
  - ±15 kV (air discharge)
  - ±8 kV (contact discharge)

## Description

The CBTVS2A16-1F3 is a single line diode TVS integrating a fuse designed specifically for the protection of integrated circuits in portable equipment and miniaturized electronic devices subject to ESD, OVP and OCP.

### Figure 1. Pin configuration (bump side)



### Figure 2. Configuration



1. B1 and B2 bumps must be grounded on the PCB together.

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This is information on a product in full production.

# 1 Characteristics

Symbol	Parameter	Test condition	Value	Unit	
D	Peak pulse power dissipation (10/1000 µs pulse) on A2-B2	T <sub>i</sub> initial = T <sub>amb</sub>	70	W	
P <sub>PP</sub>	Peak pulse power dissipation (8/20 µs pulse) on A2-B2	i jilliliai = l'amb	350	vv	
Тj	Maximum operating junction tempe	125	°C		
T <sub>stg</sub>	Storage temperature range	-55 to +150	°C		

Table '	1 Absoluto	maximum	ratings	(T <sub>amb</sub> = 25 °C)
Iable	I. ADSUIULE	maximum	raunys	$(1_{amb} - 2J = 0)$

## Figure 3. Electrical characteristics (definitions)



### Table 2. Electrical characteristics (at operating temperature: $T_{op} = -30$ °C to +85 °C, unless otherwise specified)

Symbol	Test conditions	Min.	Тур.	Max.	Unit
V <sub>BR</sub>	I <sub>R</sub> = 20 mA	16			V
I <sub>RM</sub>	V <sub>RM</sub> = 12 V			100	nA
V <sub>CL</sub>	$I_{PP}$ = 1 A, 8/20 µs pulse waveform, between A1-B1			19	V
V <sub>F</sub>	I <sub>F</sub> = 850 mA, between A1-B1			1.4	V
T <sub>fuse2</sub>	At 3.2 A, A <sub>1</sub> -A <sub>2</sub> , A <sub>2</sub> -A <sub>1</sub>			24	hours
C <sub>line</sub>	$V_R = 0 V$ , $V_{OSC} = 30 mV$ , $F = 1 MHz$		125		pF
R <sub>A1-A2</sub>	At T <sub>amb</sub> = 25 °C at 100 mA			50	mΩ
R <sub>A1-A2</sub>	After fused	1			MΩ
T <sub>Fuse</sub>	At 5 A (maximum opening time) $A_2$ - $A_1$ , $A_1$ - $A_2$			100	ms
T <sub>fuse Lifetime</sub>	$I_{DC}$ = 2 A (continuous current) at $T_{amb}$ = 25 °C	1000			hours

0.1

14 16 18 20 22 V<sub>FM</sub>(V)

3.0

2.5

## current (typical values) (A) 100.0 8/20us A1/B1 A2/B2 10.0 1.0 Vci (V)

# Figure 4. Clamping voltage versus peak pulse



26 28 30 32 34 36 38 40

24



Figure 8. ESD response to IEC 61000-4-2 (+8 kV contact discharge)



1.5

2.0

Figure 5. Forward voltage drop versus peak

forward current (typical values)

100 <u>I<sub>FM</sub>(A)</u>

10

0.1

0.01

0.001

0.0001

0.0

A1/B1 A2/B2

0 5

1.0

applied voltage (typical values)



Figure 9. ESD response to IEC 61000-4-2 (-8 kV contact discharge)





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# 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK<sup>®</sup> is an ST trademark.



### Figure 11. Foot print recommendations

### Figure 12. Marking







Note:

More information is available in the application notes:

AN2348: "400 µm Flip Chip: Package description and recommendations for use" AN1751: "EMI Filters: Recommendations and measurements"



### **Ordering information** 3



## Figure 14. Ordering information scheme

### Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
CBTVS2A16-1F3	ET	Flip Chip	0.659 mg	10 000	Tape and reel (7")

#### **Revision history** 4

### Table 4. Document revision history

Date	Revision	Changes
01-Apr-2015	1	Initial release.



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